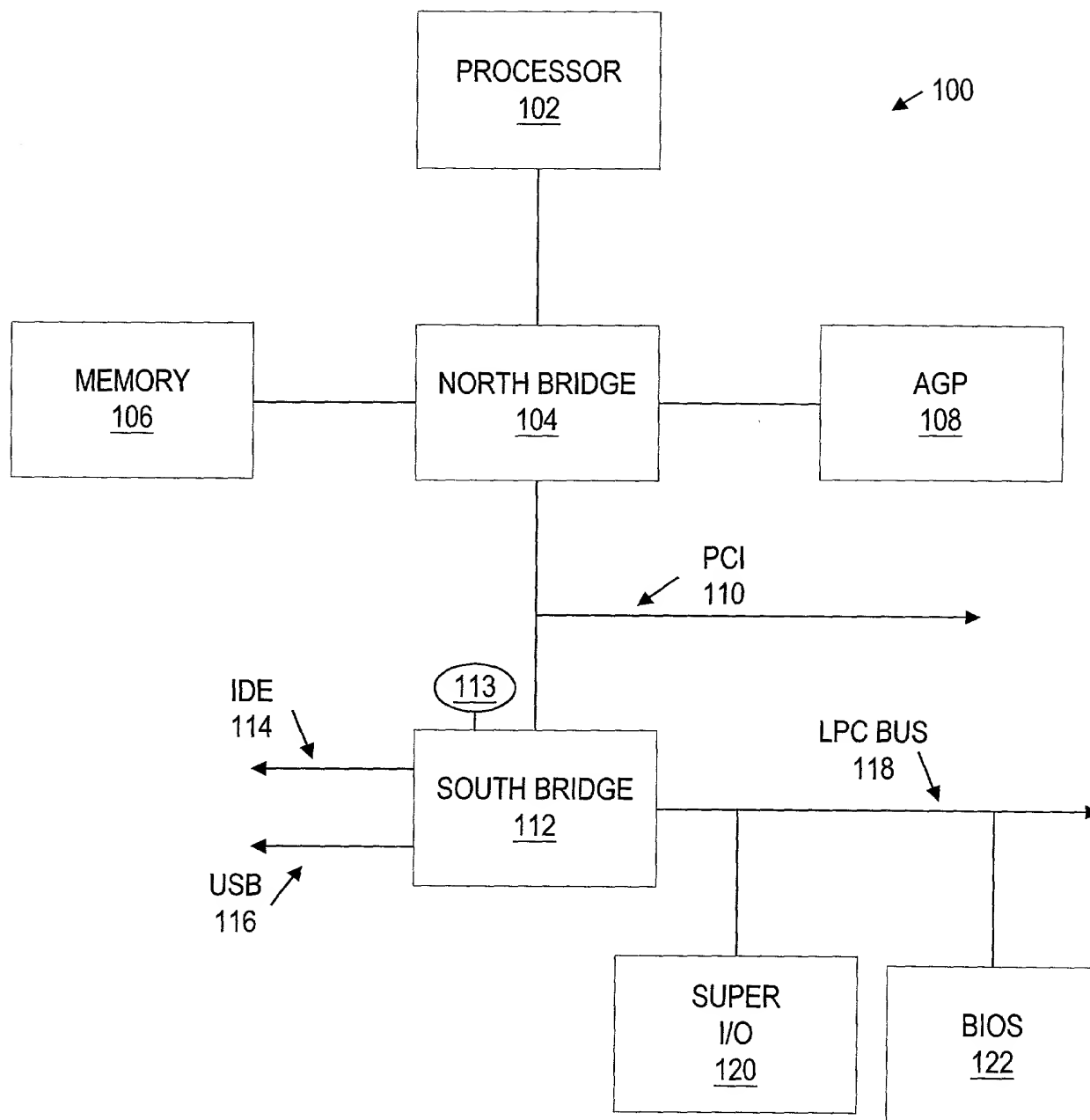
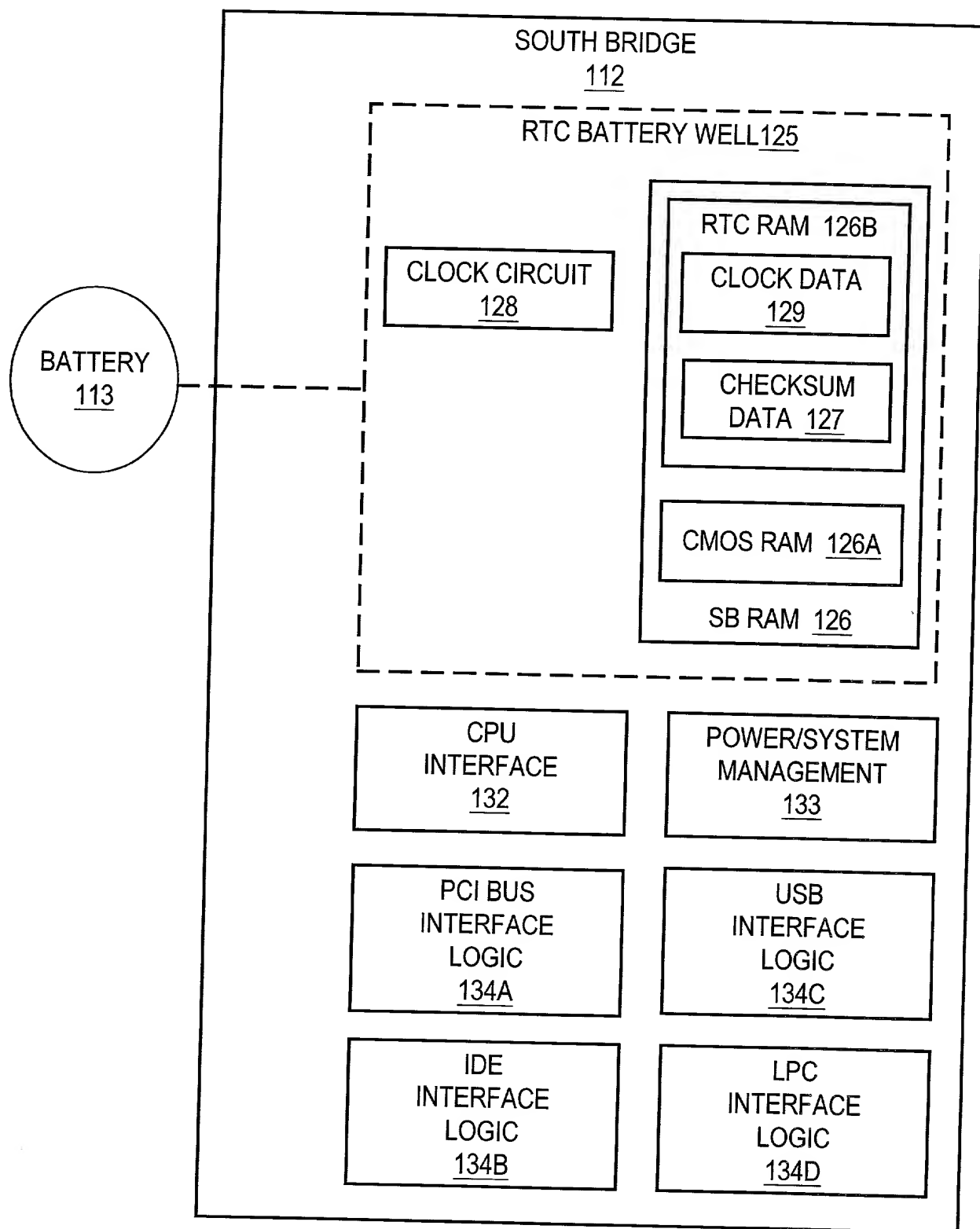


1 / 73



**Fig. 1A**  
**(Prior Art)**

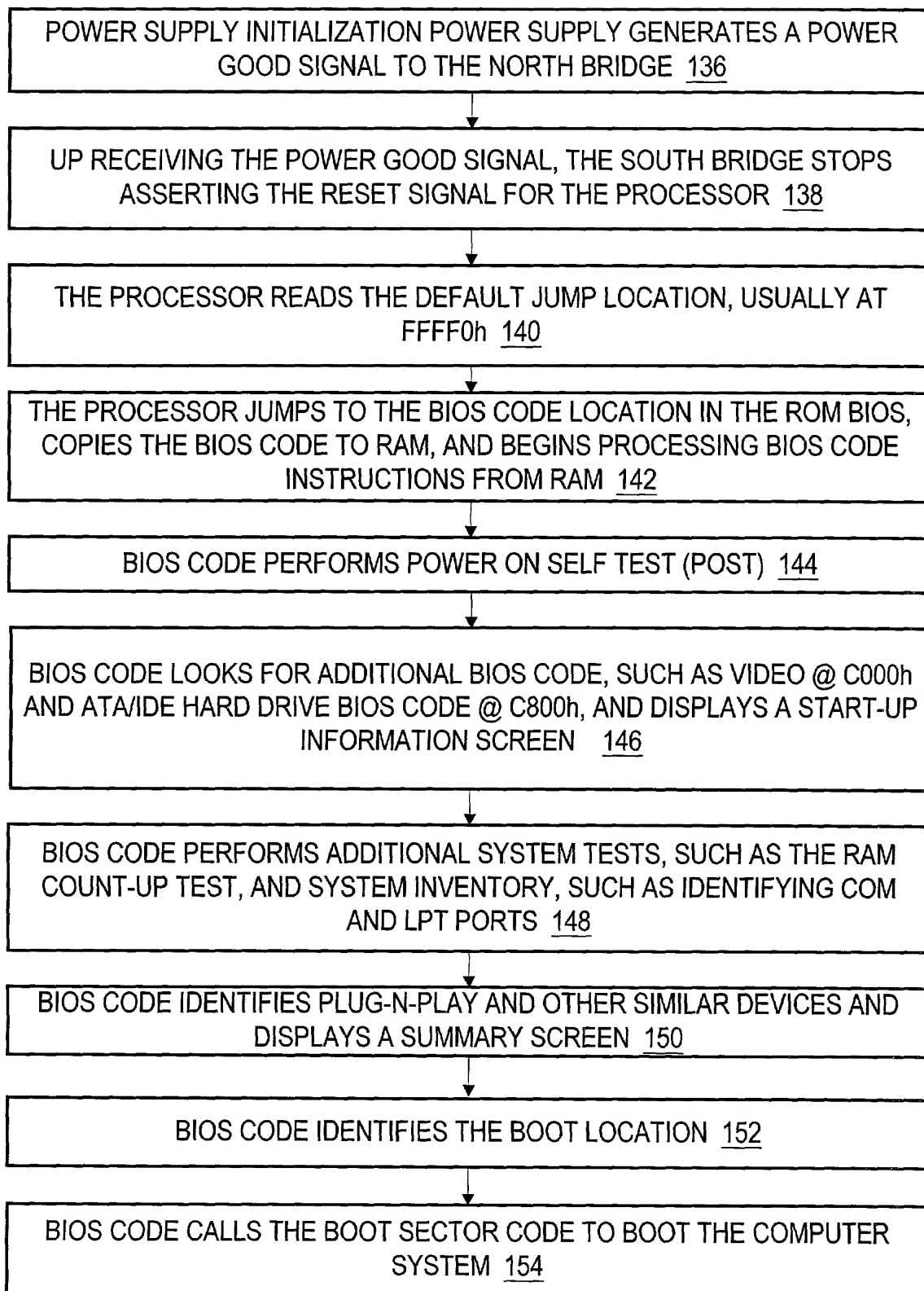
2 / 73



**Fig. 1B**  
**(Prior Art)**

3 / 73

135

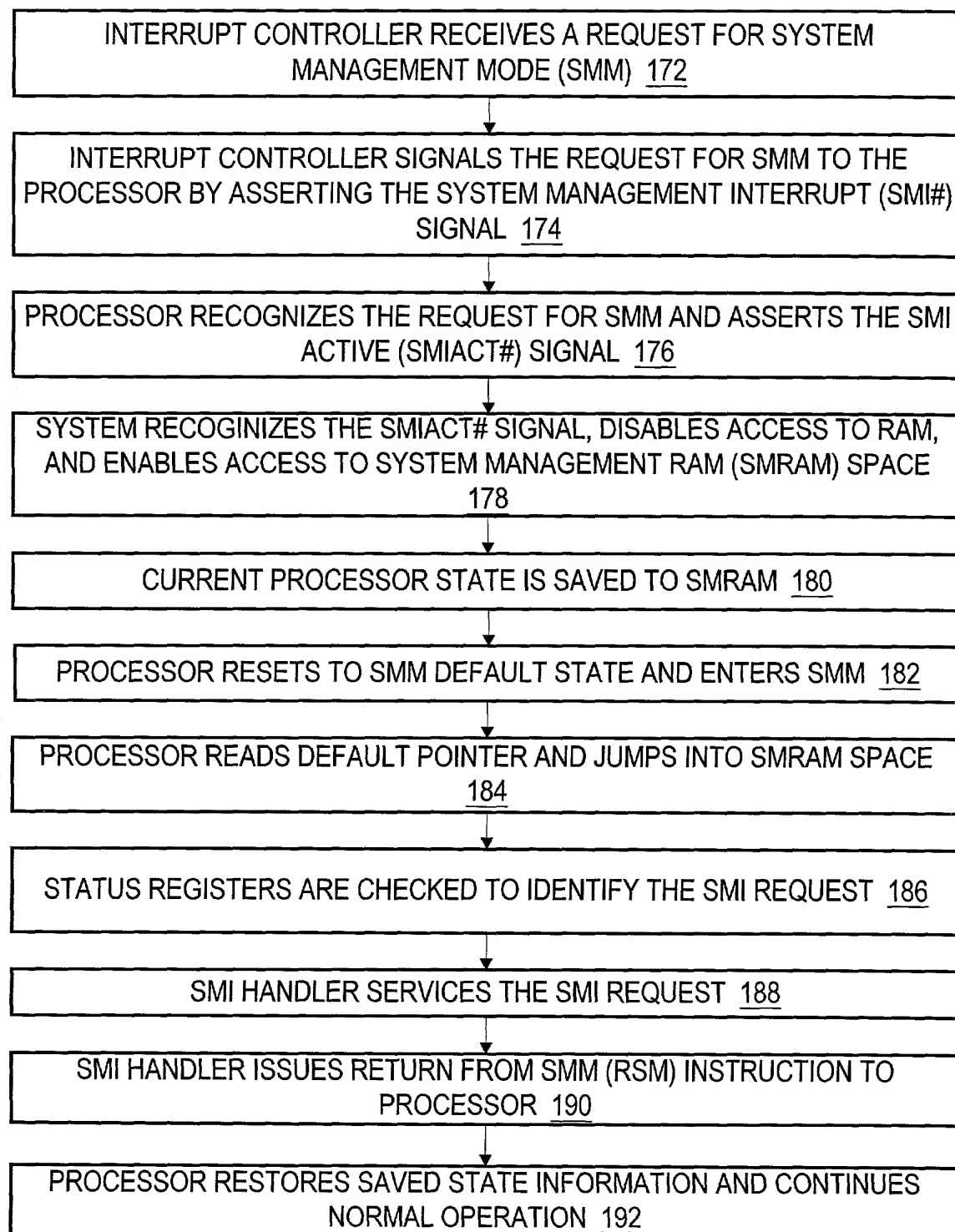


**Fig. 2A**  
**(Prior Art)**

T00550" 6880/2860

4 / 73

170



**Fig. 2B**  
**(Prior Art)**

5 / 73

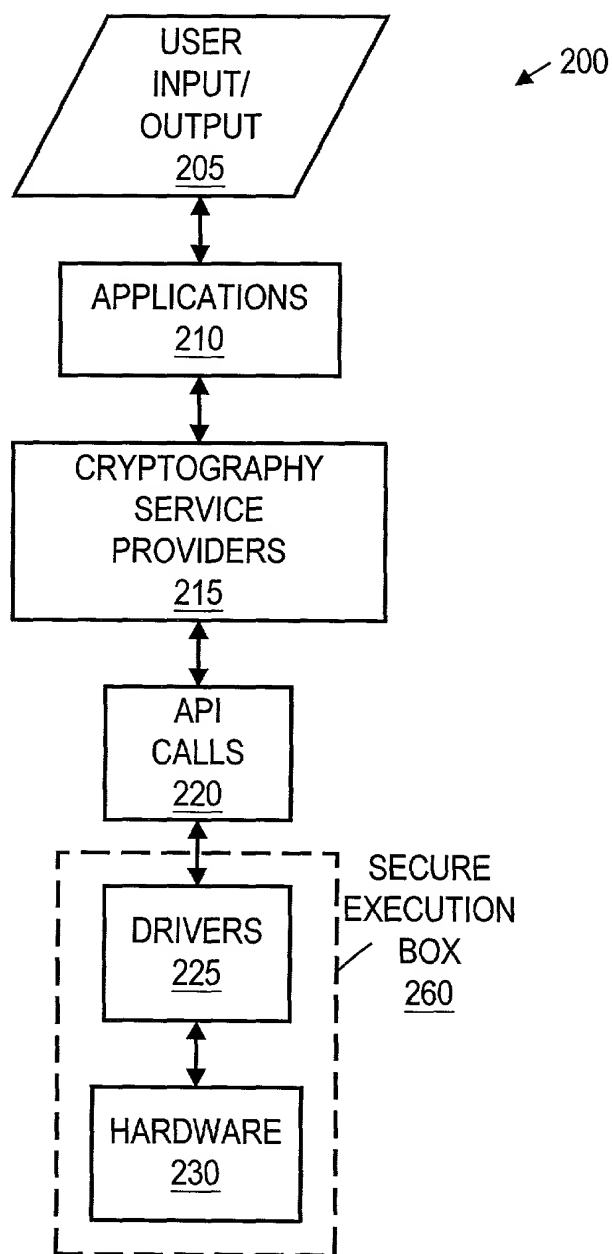
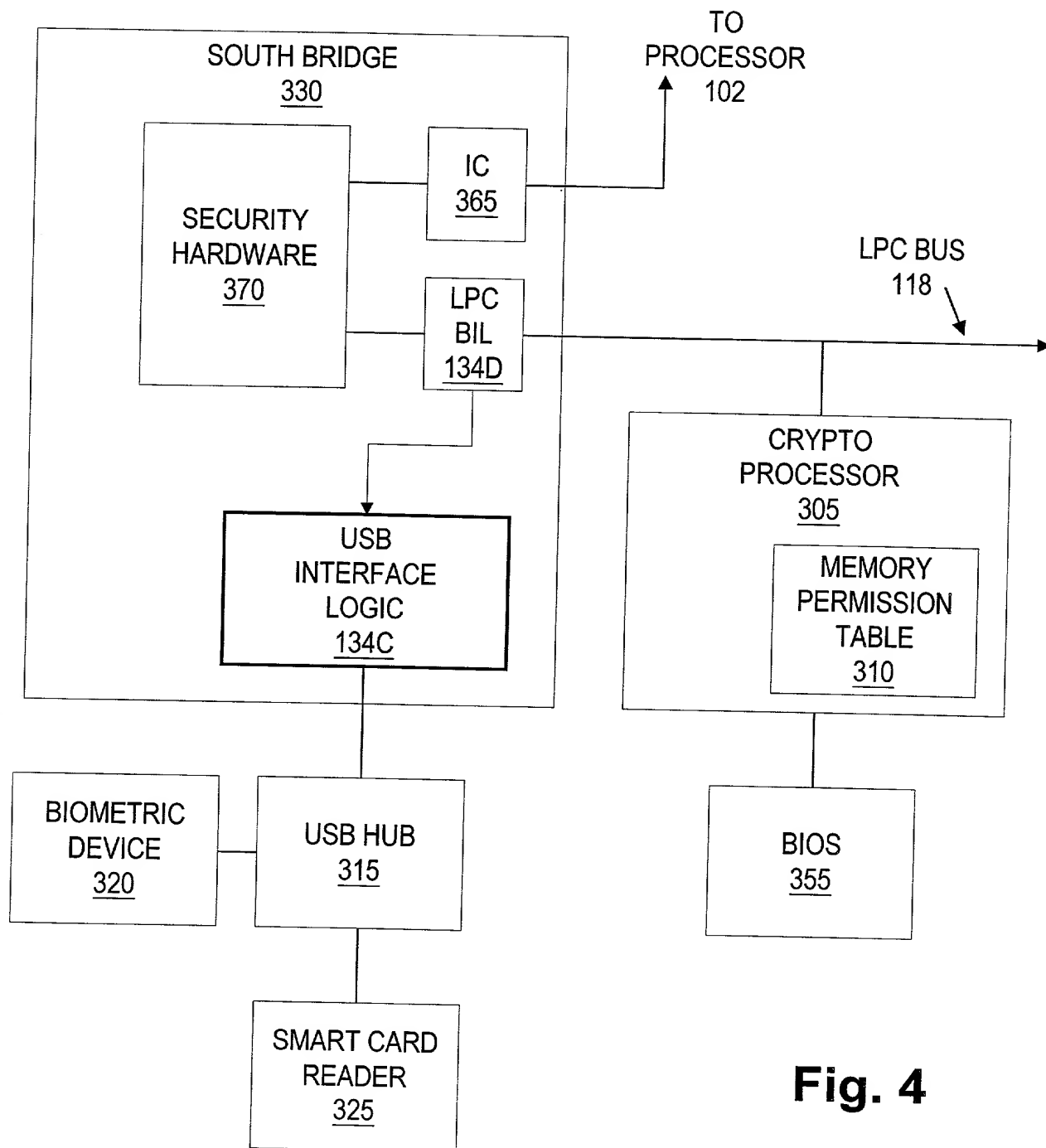
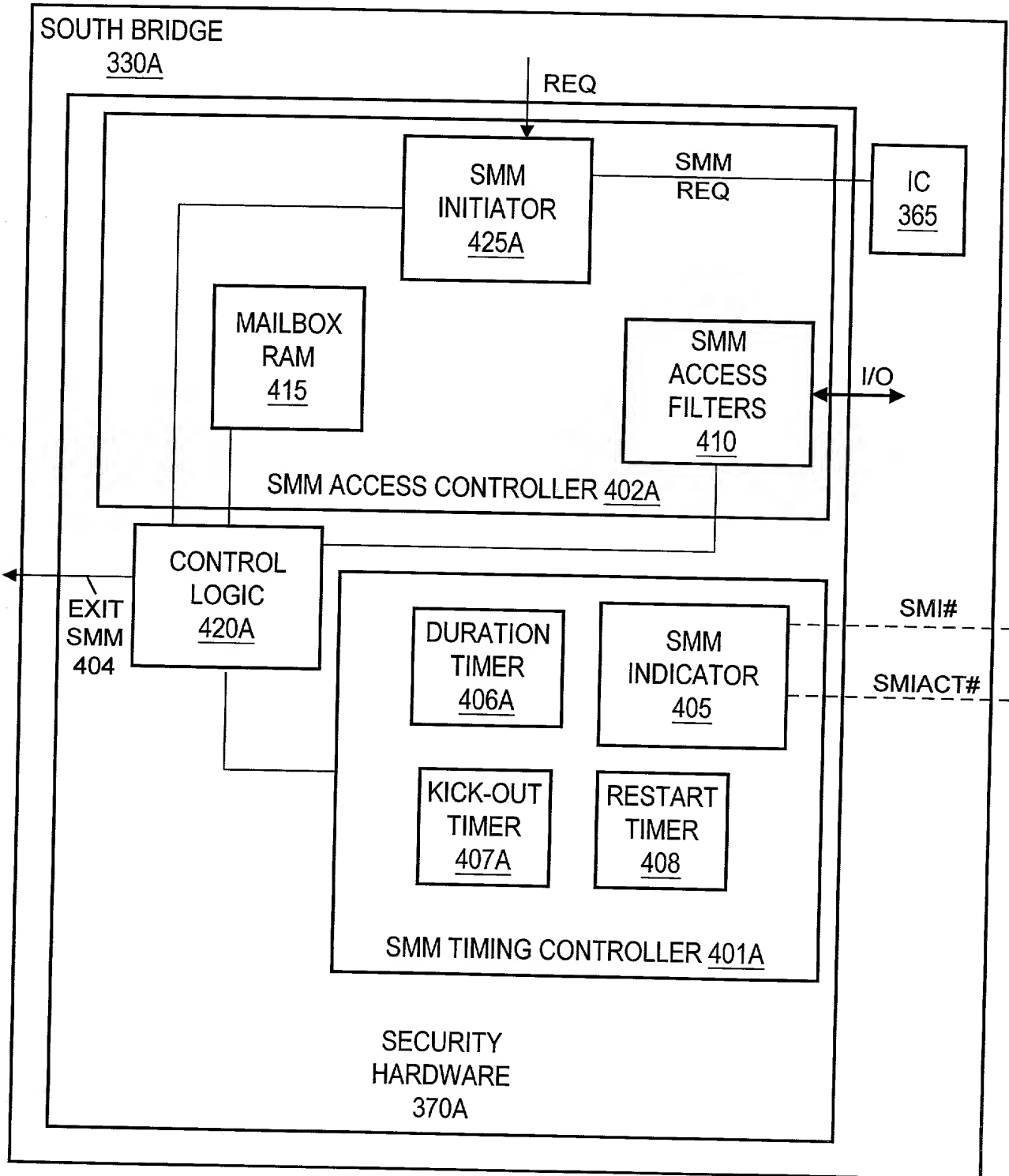


Fig. 3

6 / 73

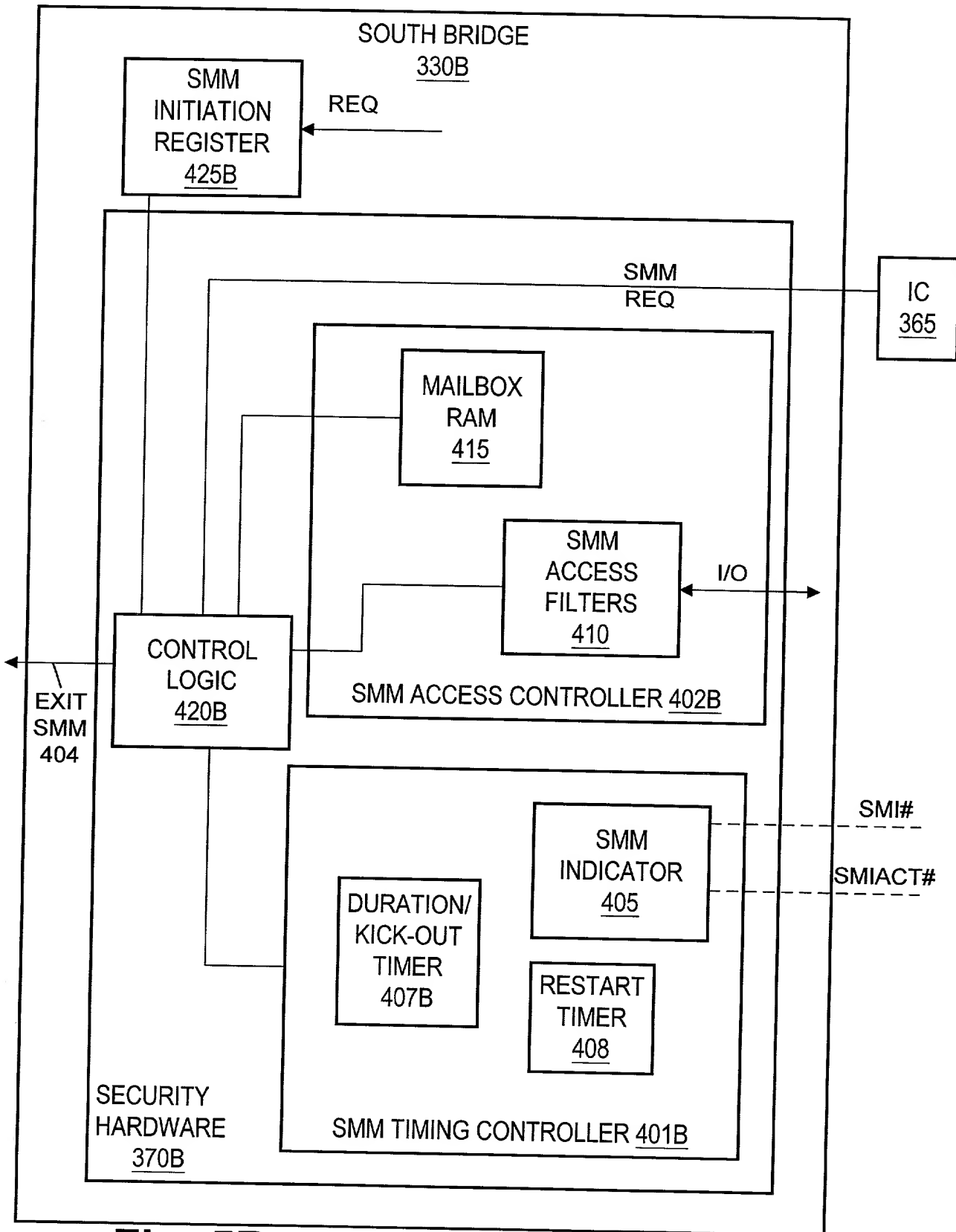


**Fig. 4**



**Fig. 5A**

8 / 73

**Fig. 5B**



9 / 73

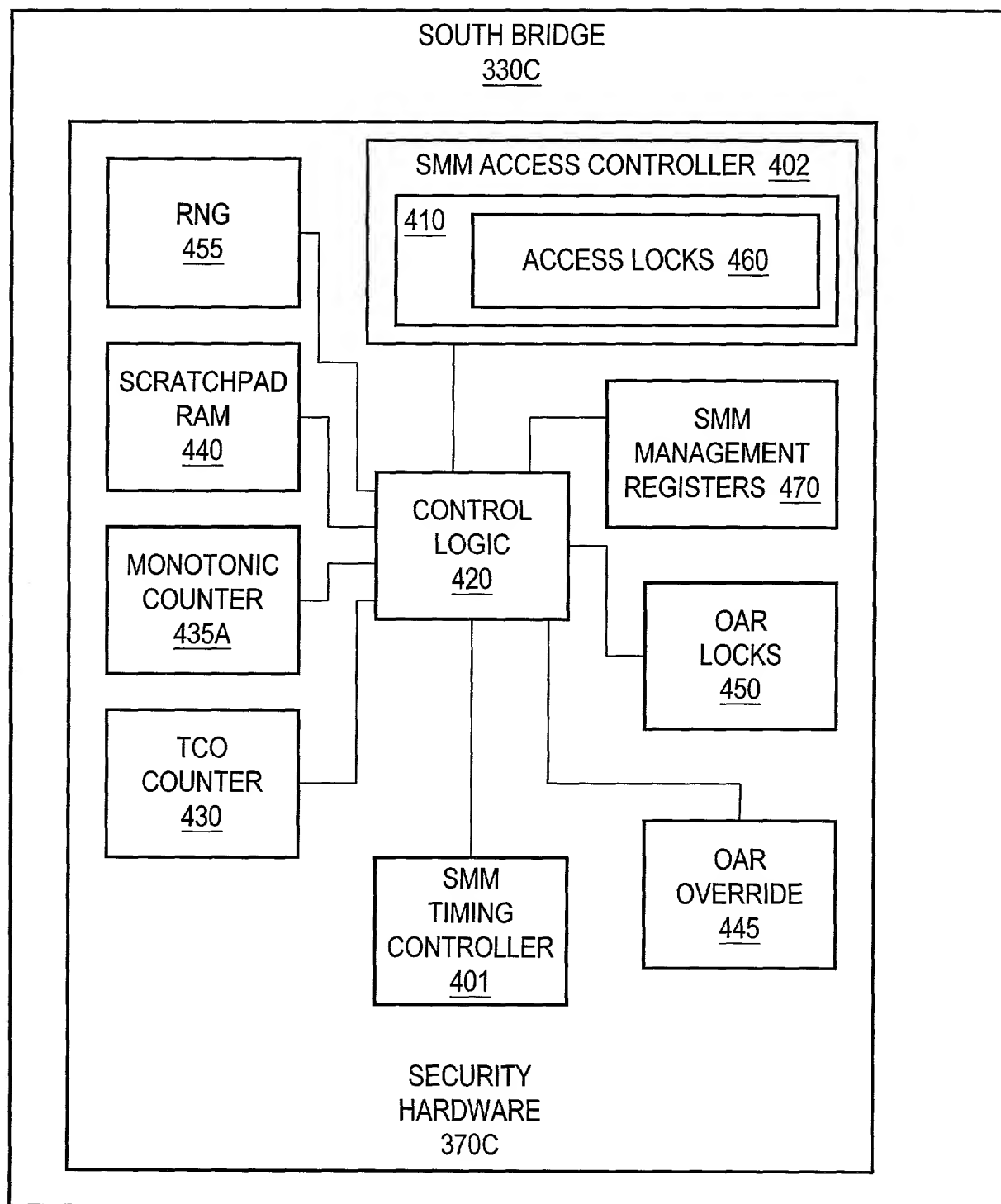


Fig. 6

10 / 73

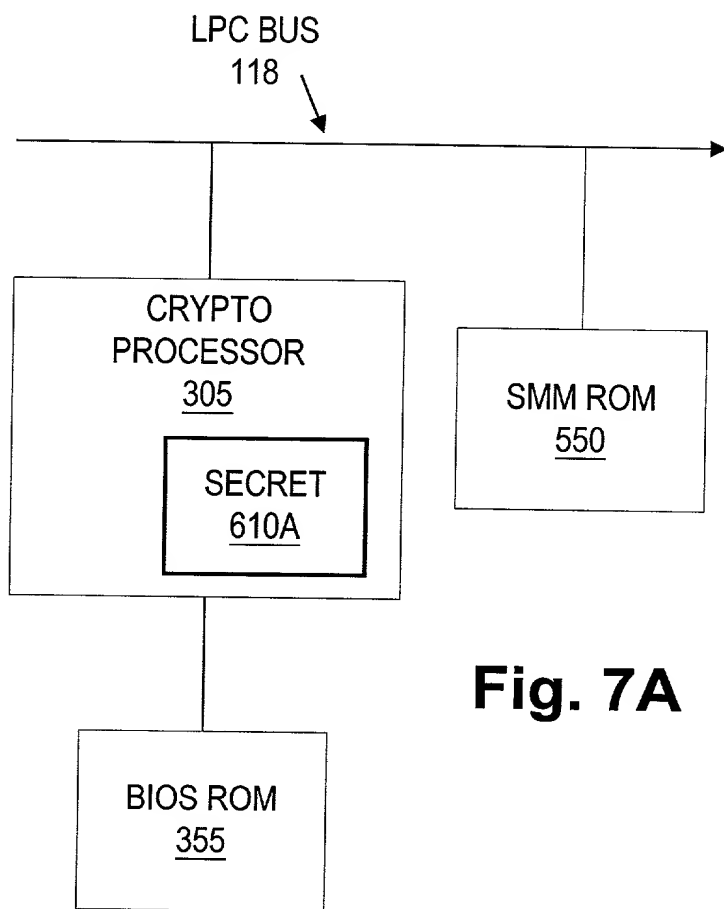


Fig. 7A

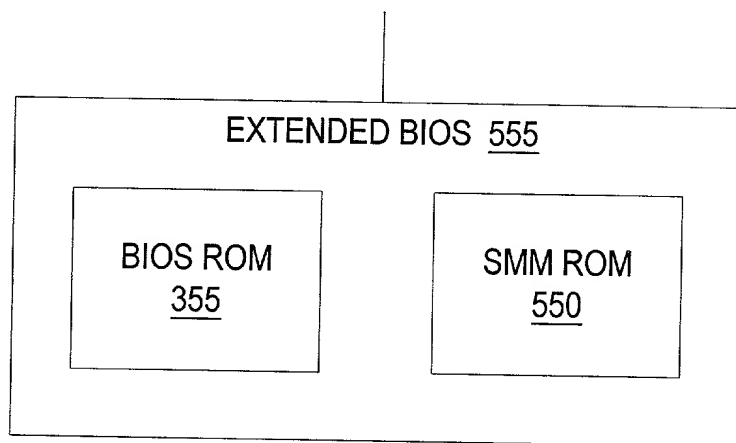


Fig. 7B

11 / 73

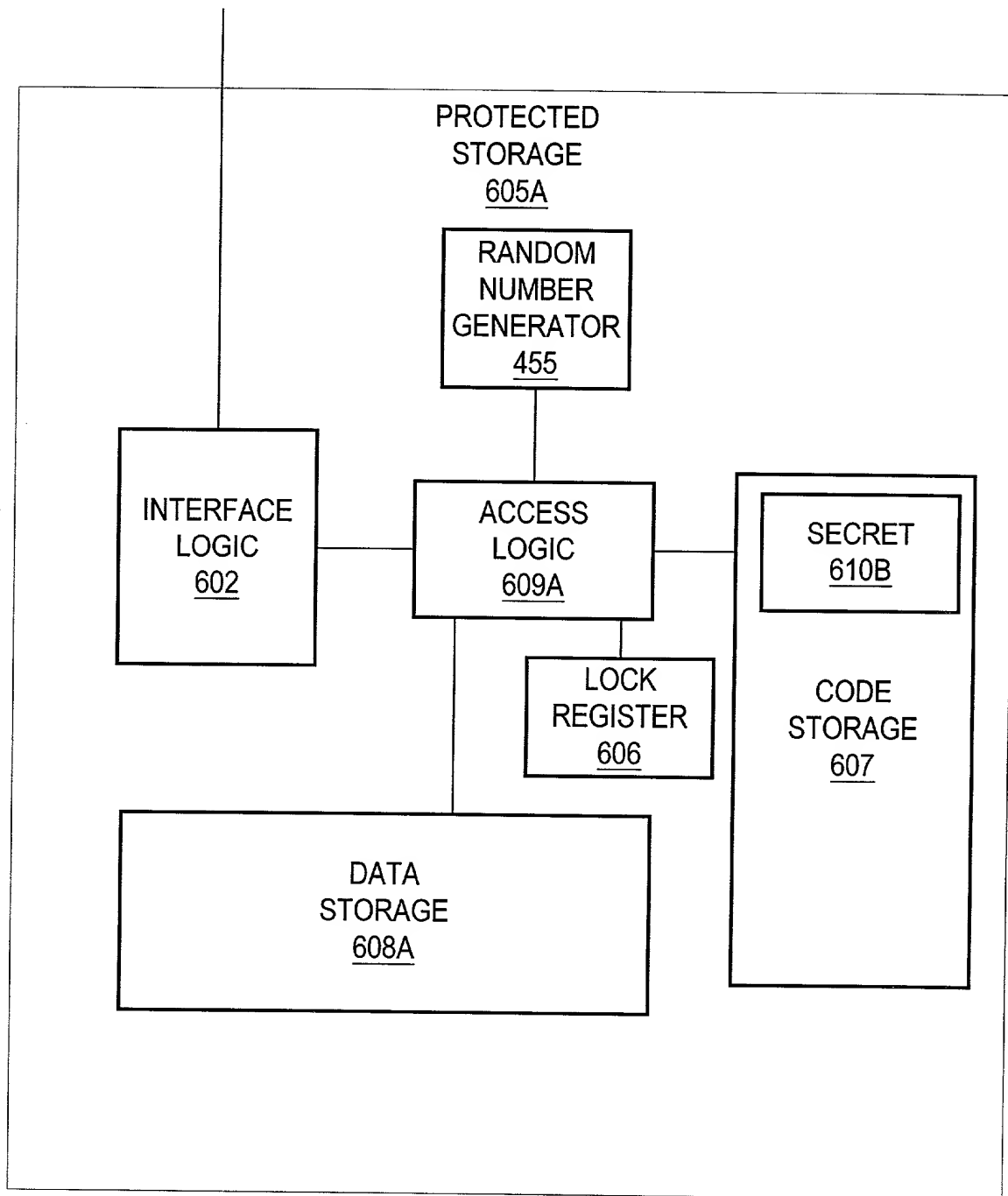


Fig. 7C

12 / 73

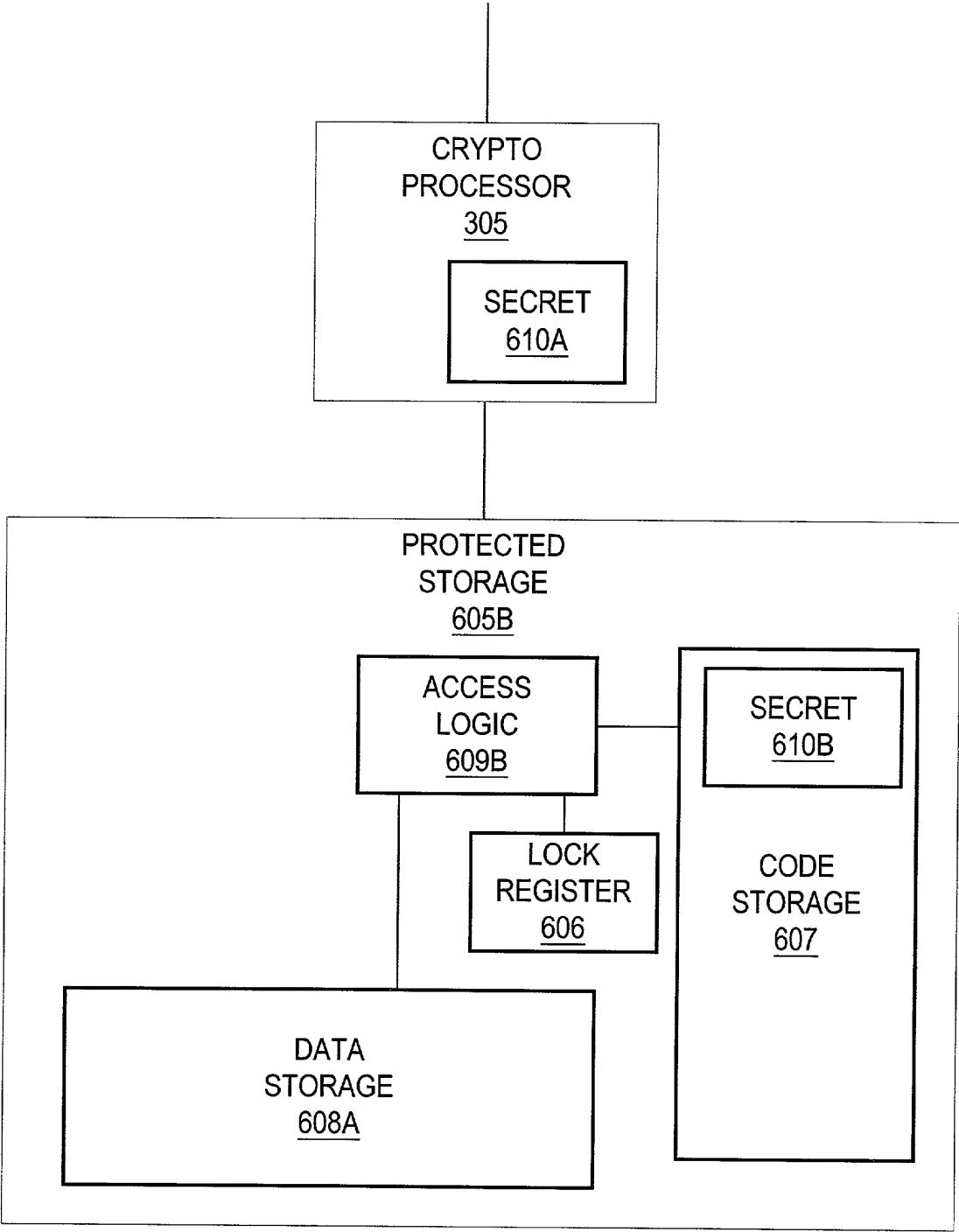


Fig. 7D

13 / 73

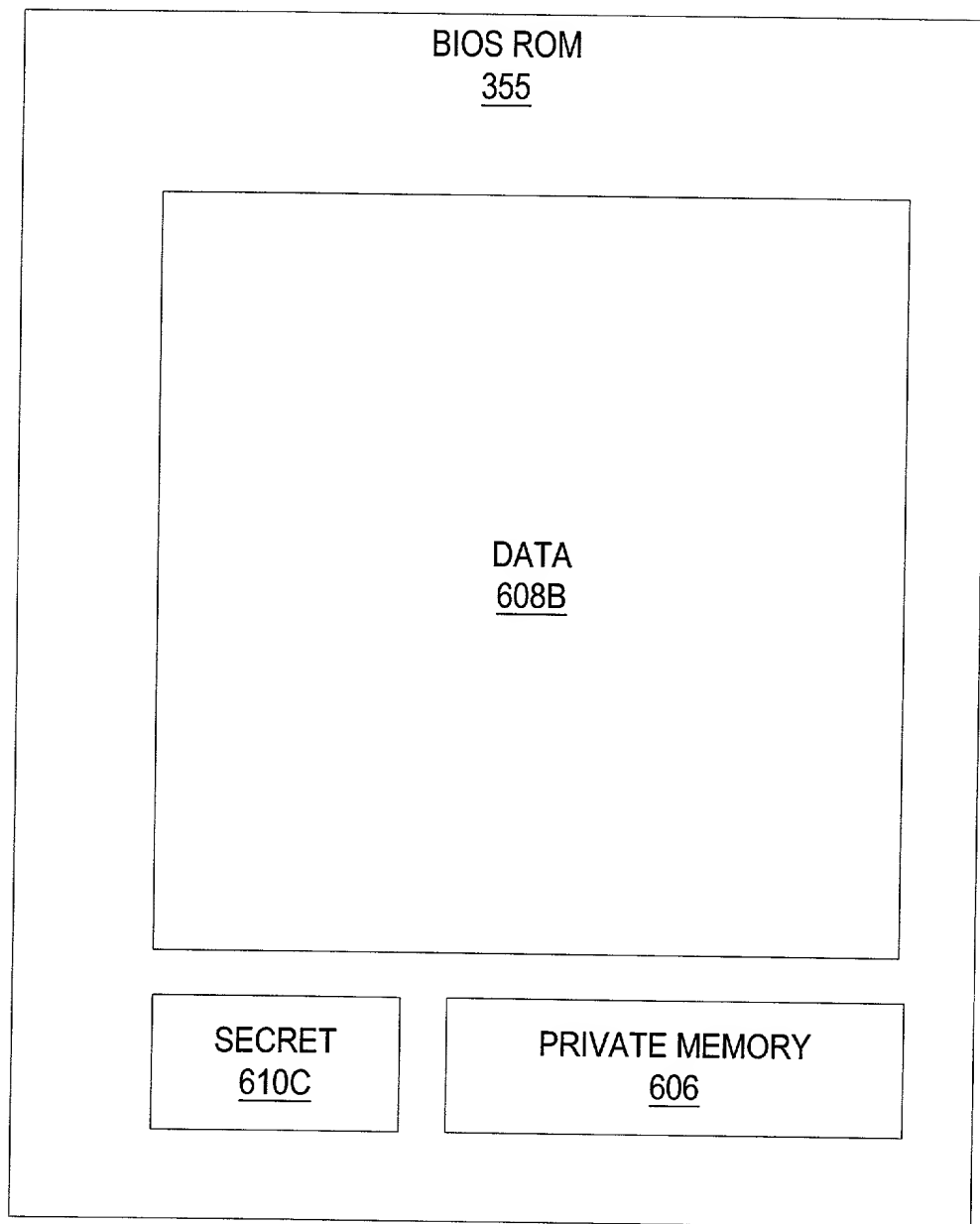


Fig. 8A

14 / 73

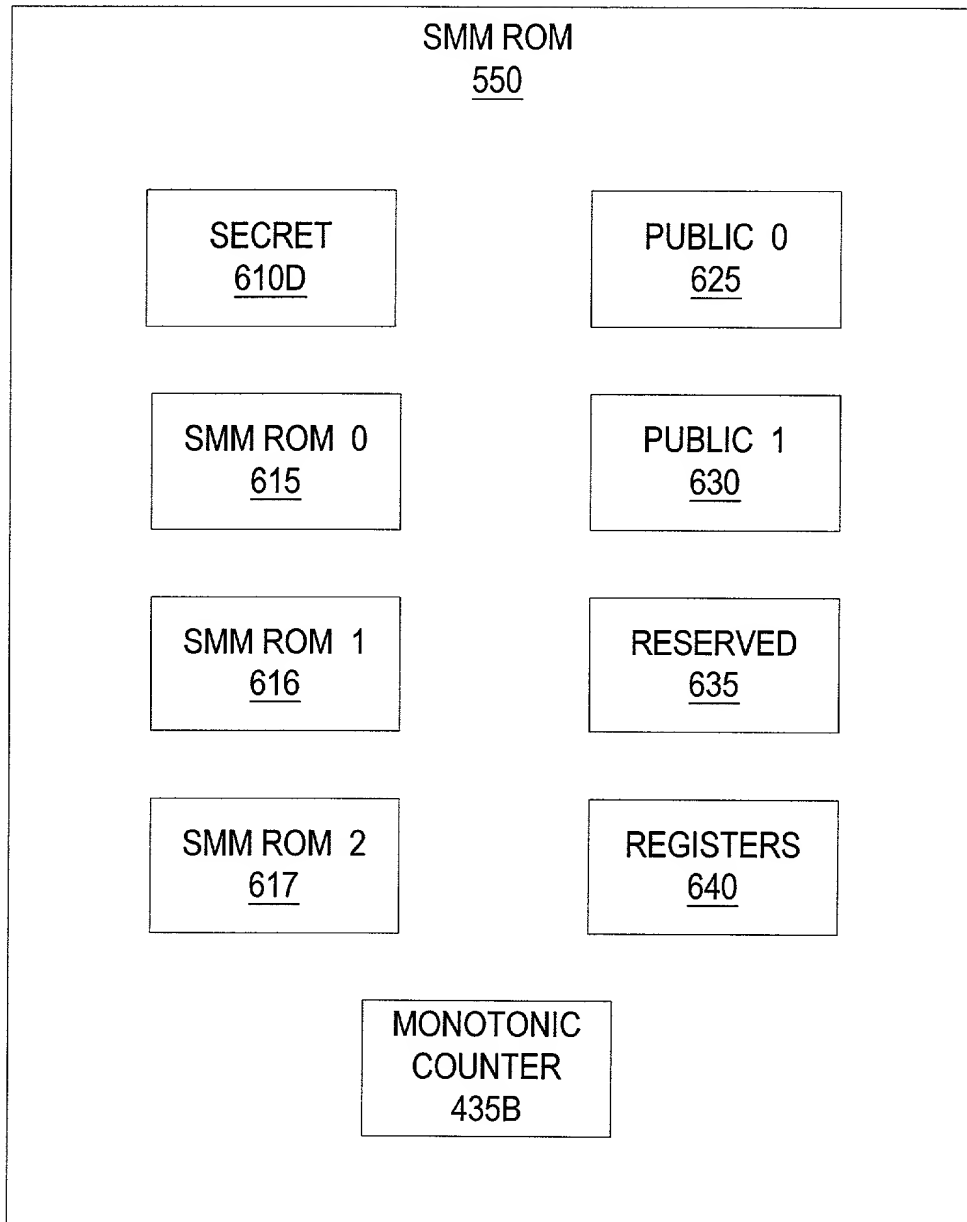


Fig. 8B

15 / 73

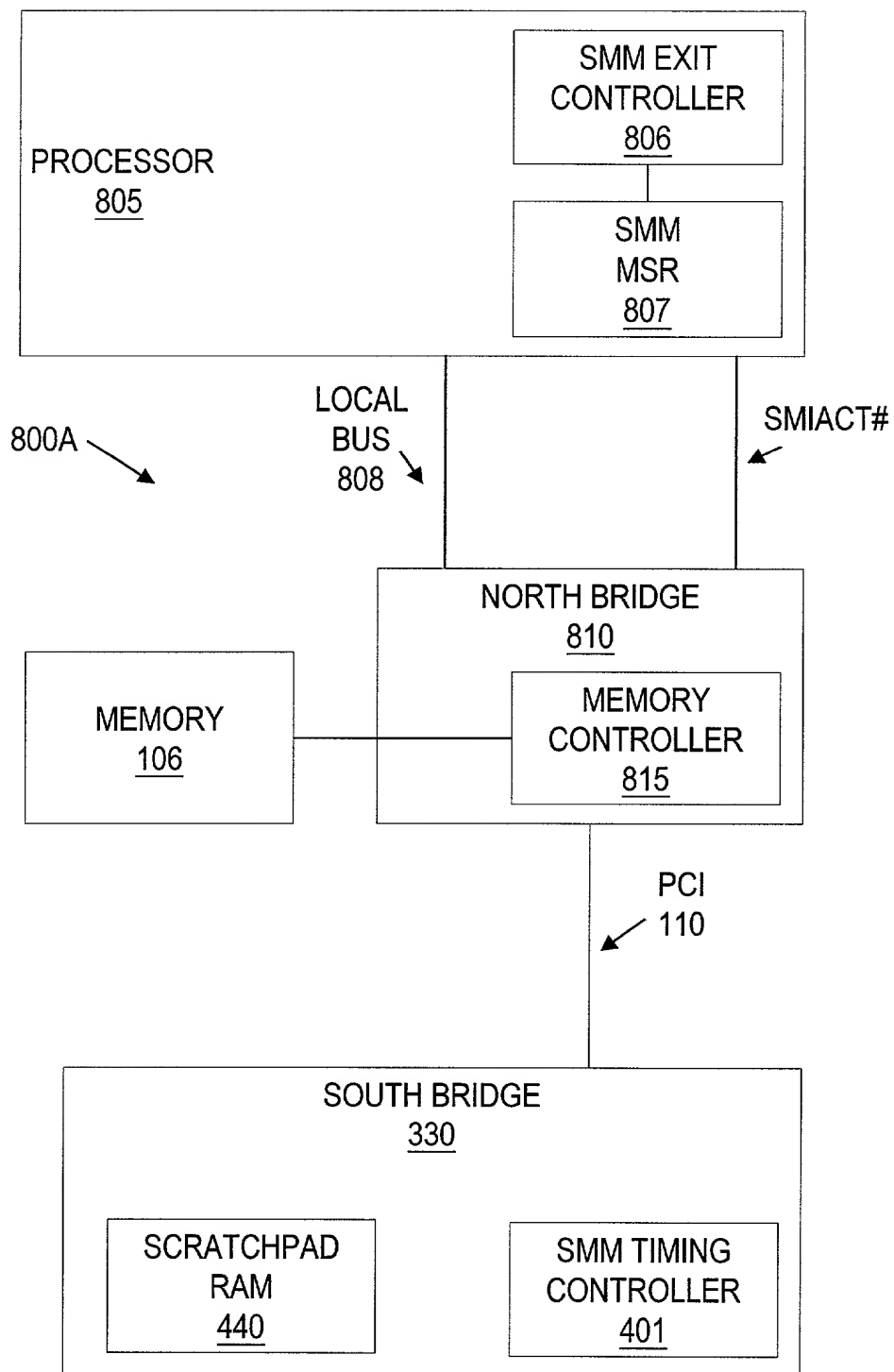


Fig. 9A

16 / 73

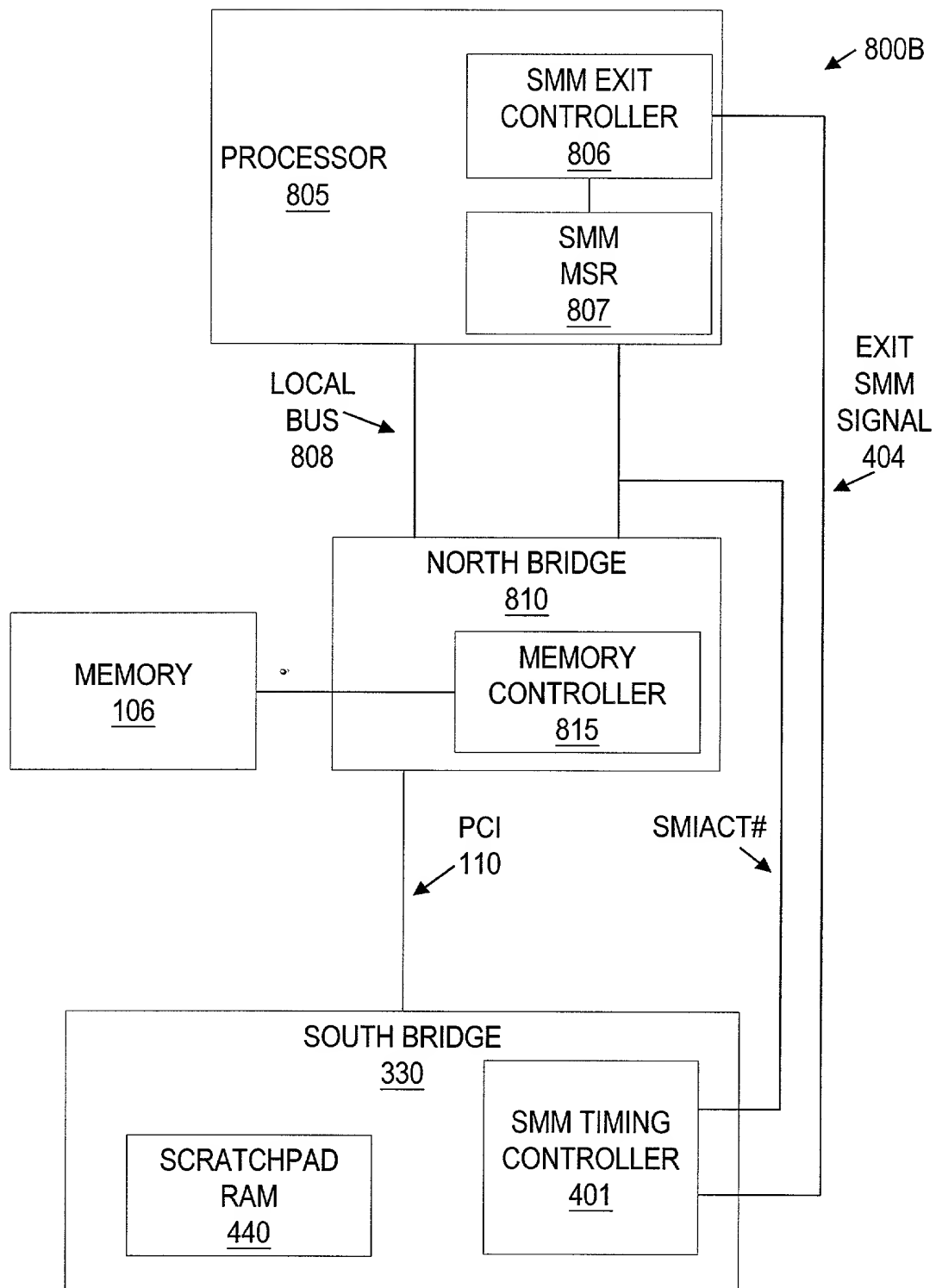


Fig. 9B



17 / 73

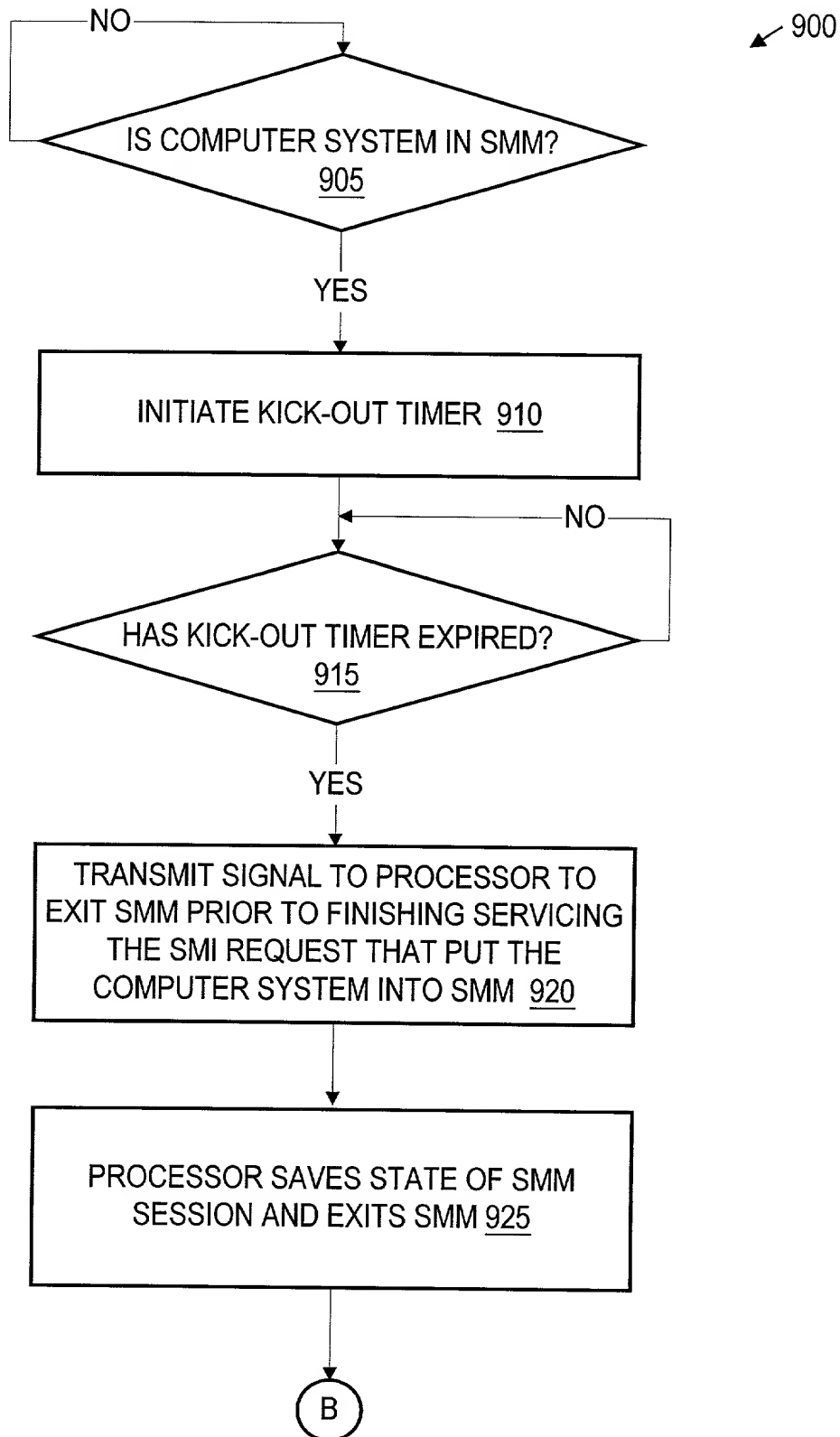


Fig. 10A

18 / 73

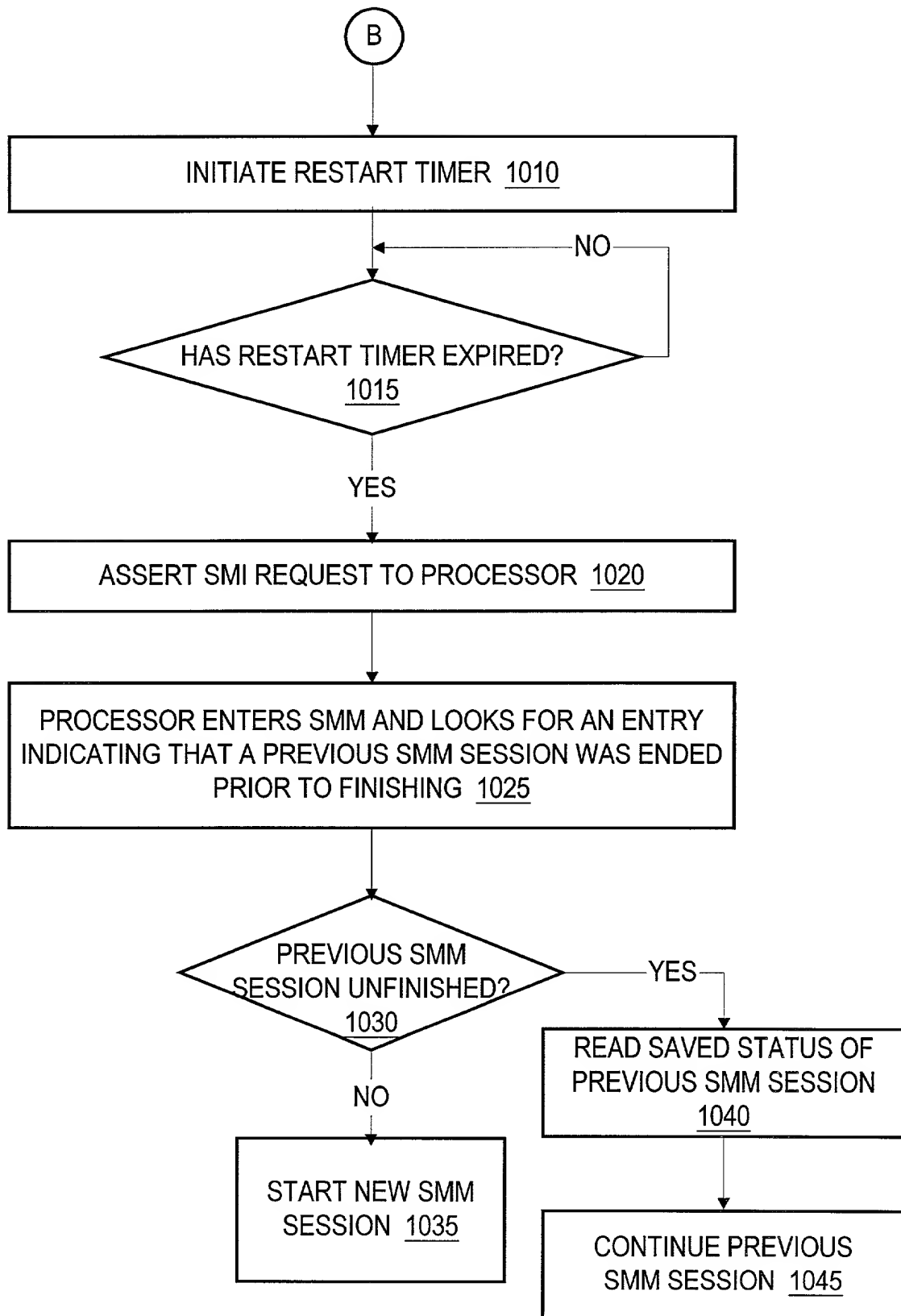


Fig. 10B

19 / 73

1100A

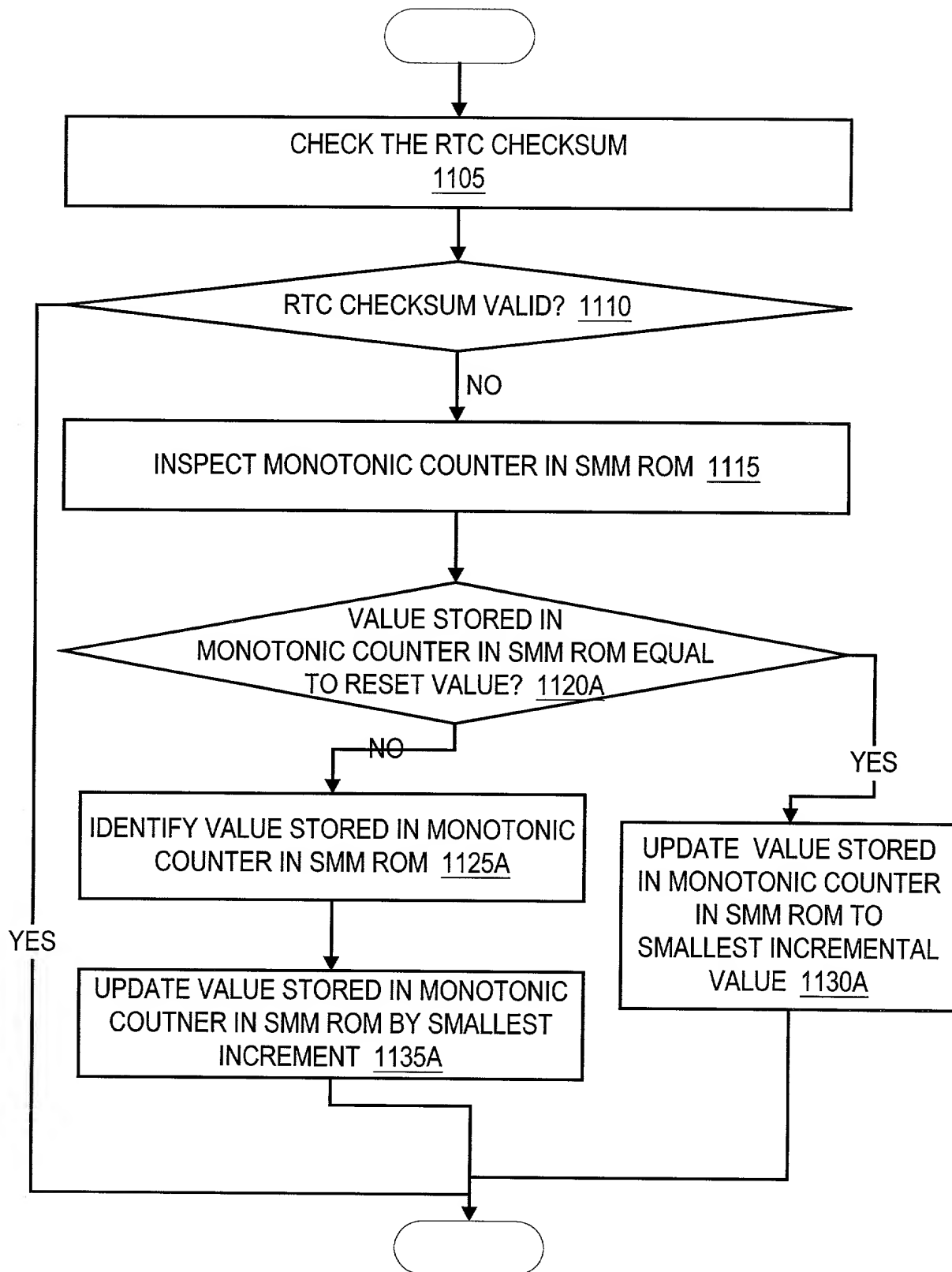


Fig. 11A

20 / 73

1100B

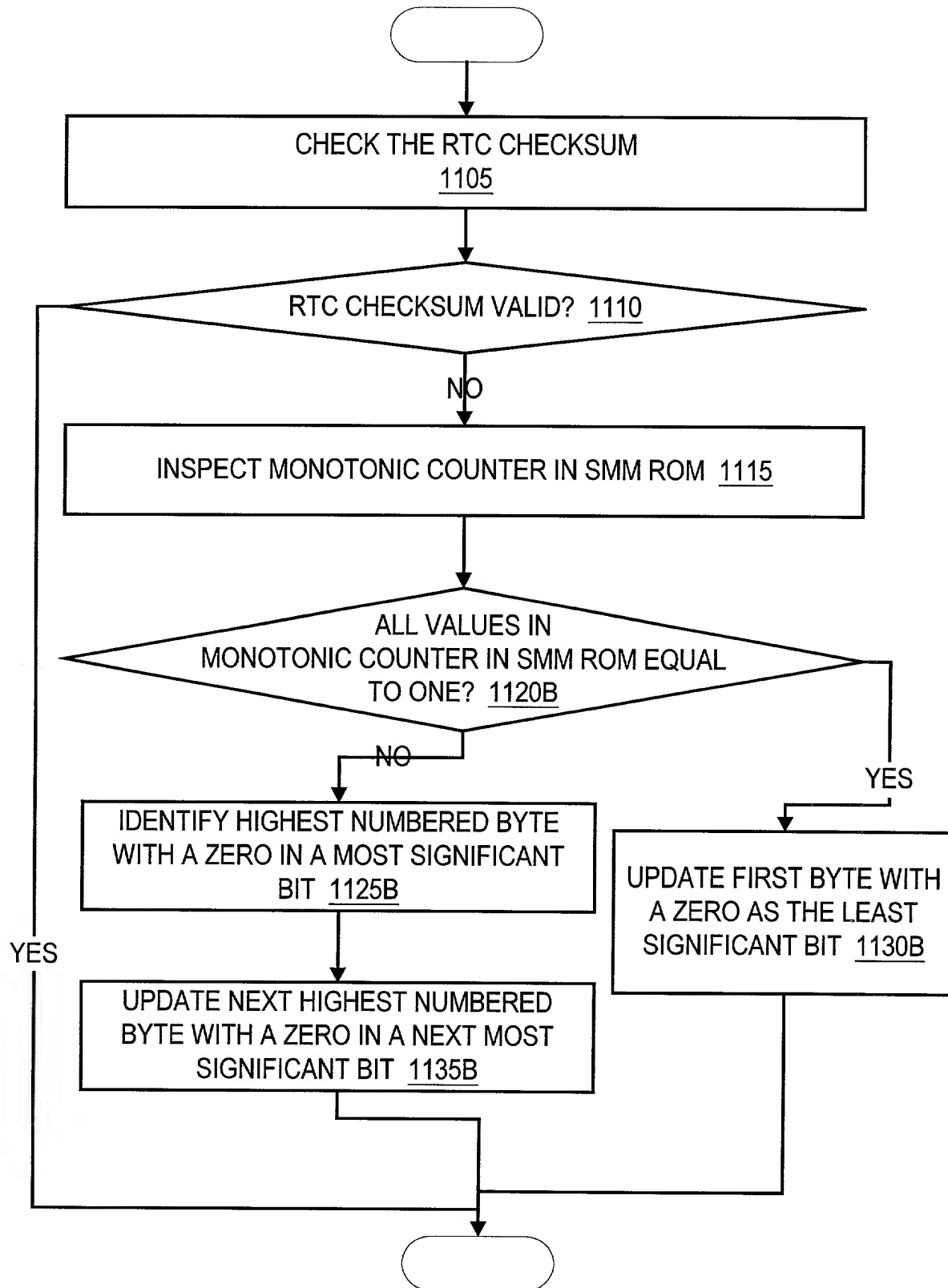


Fig. 11B

21 / 73

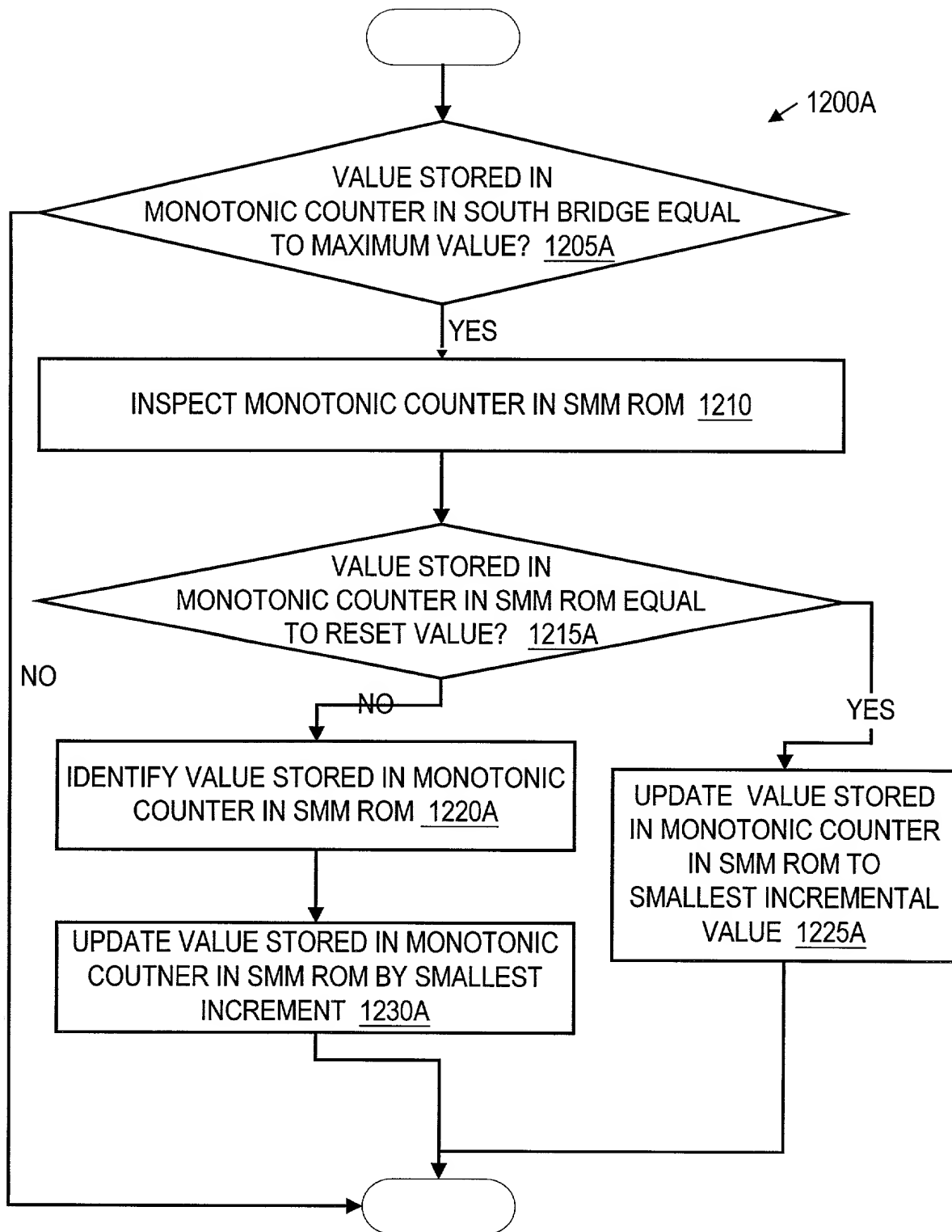


Fig. 12A

22 / 73

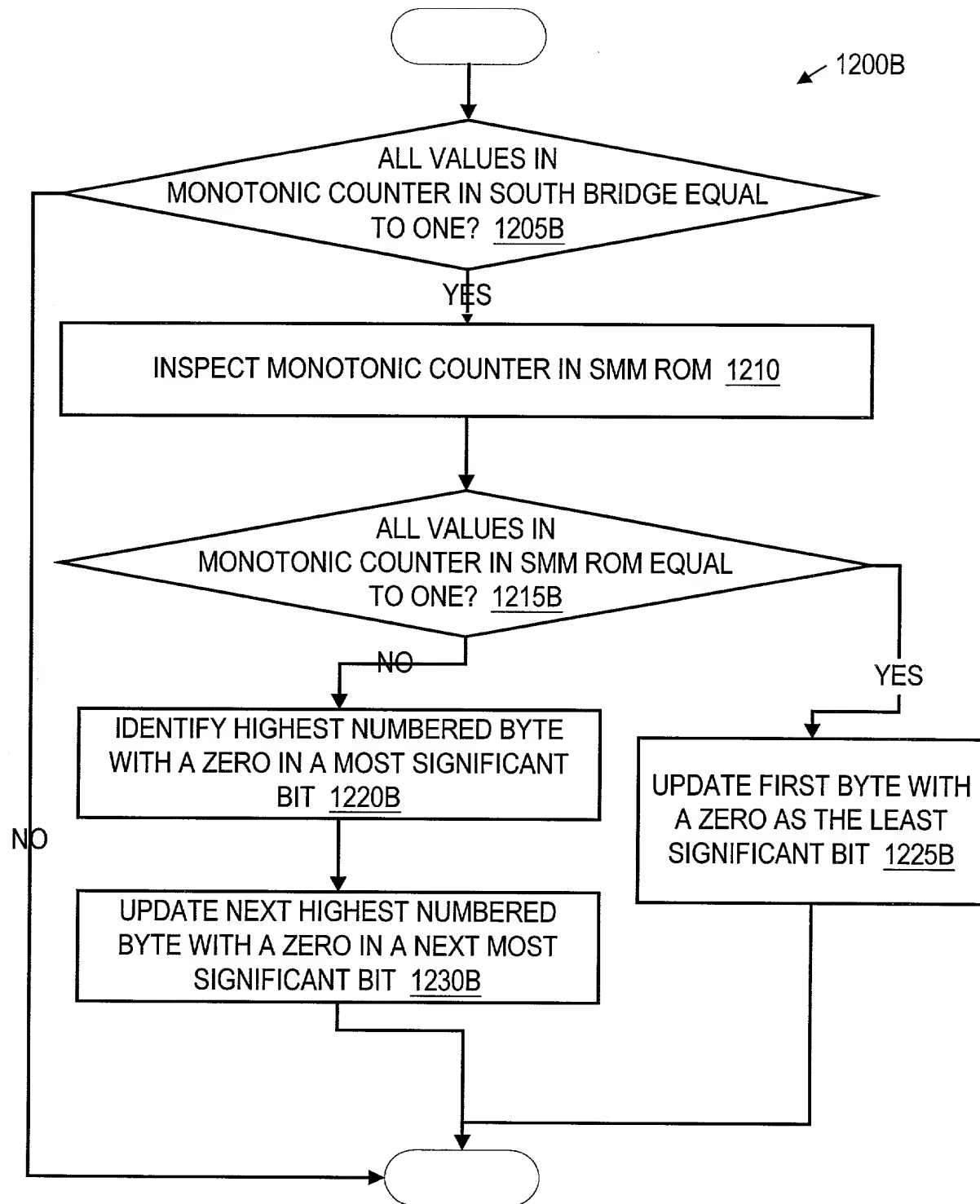


Fig. 12B

23 / 73

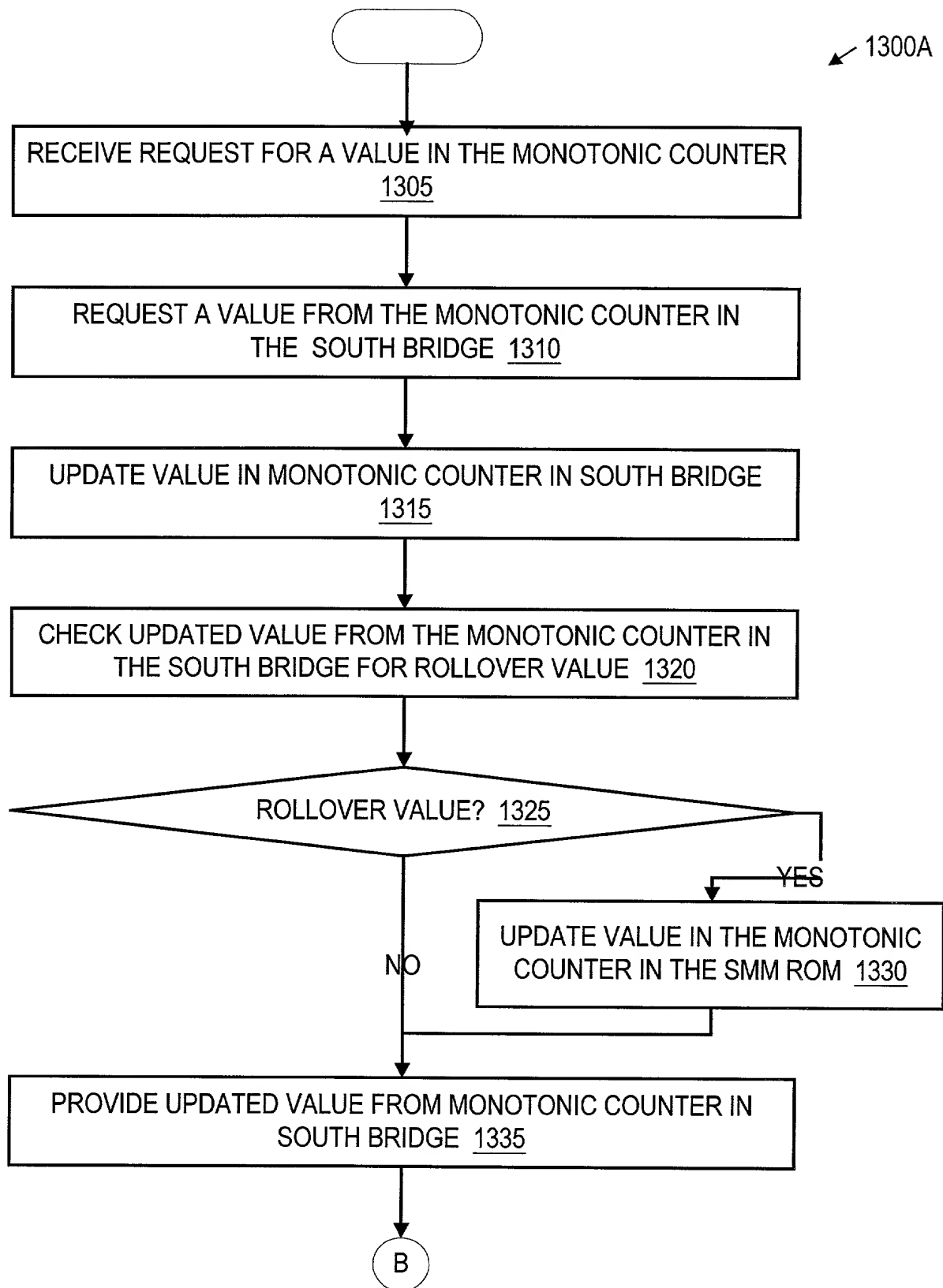


Fig. 13A

24 / 73

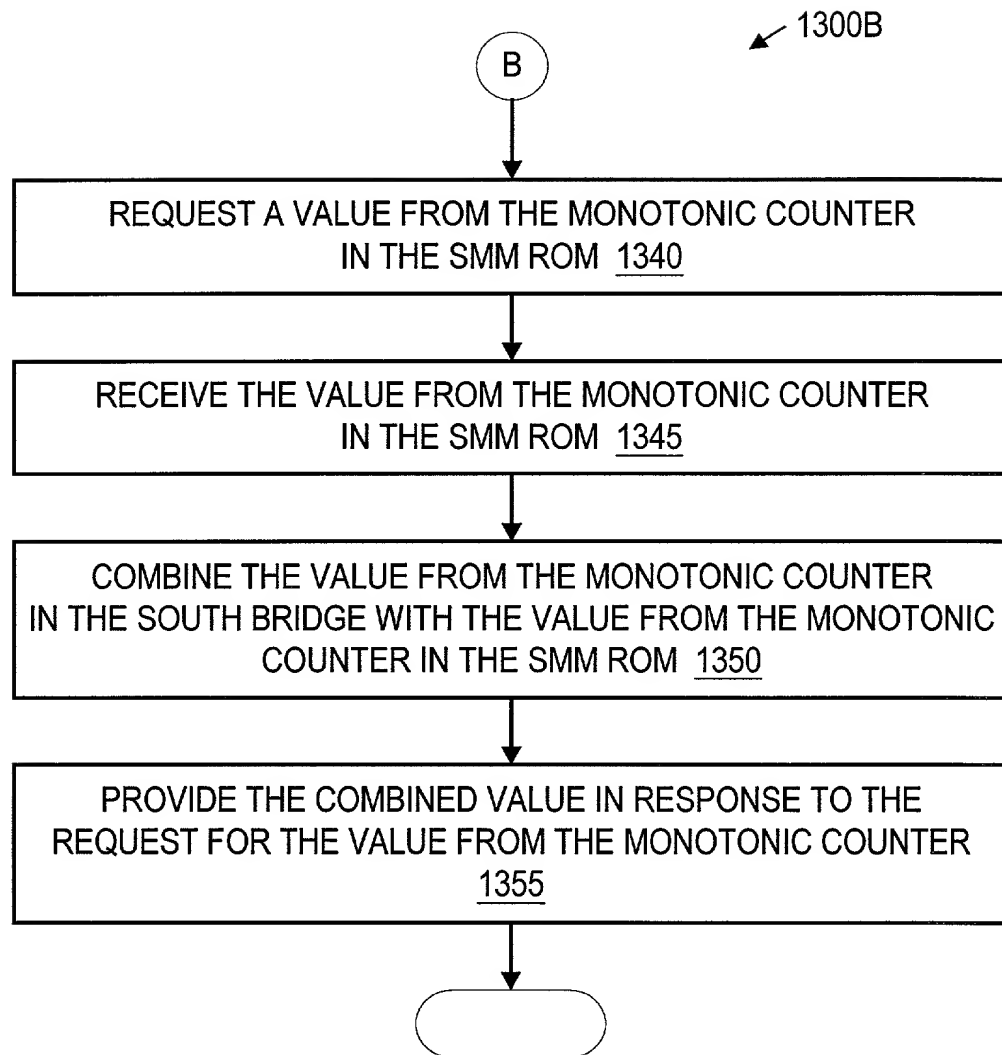


Fig. 13B



25 / 73

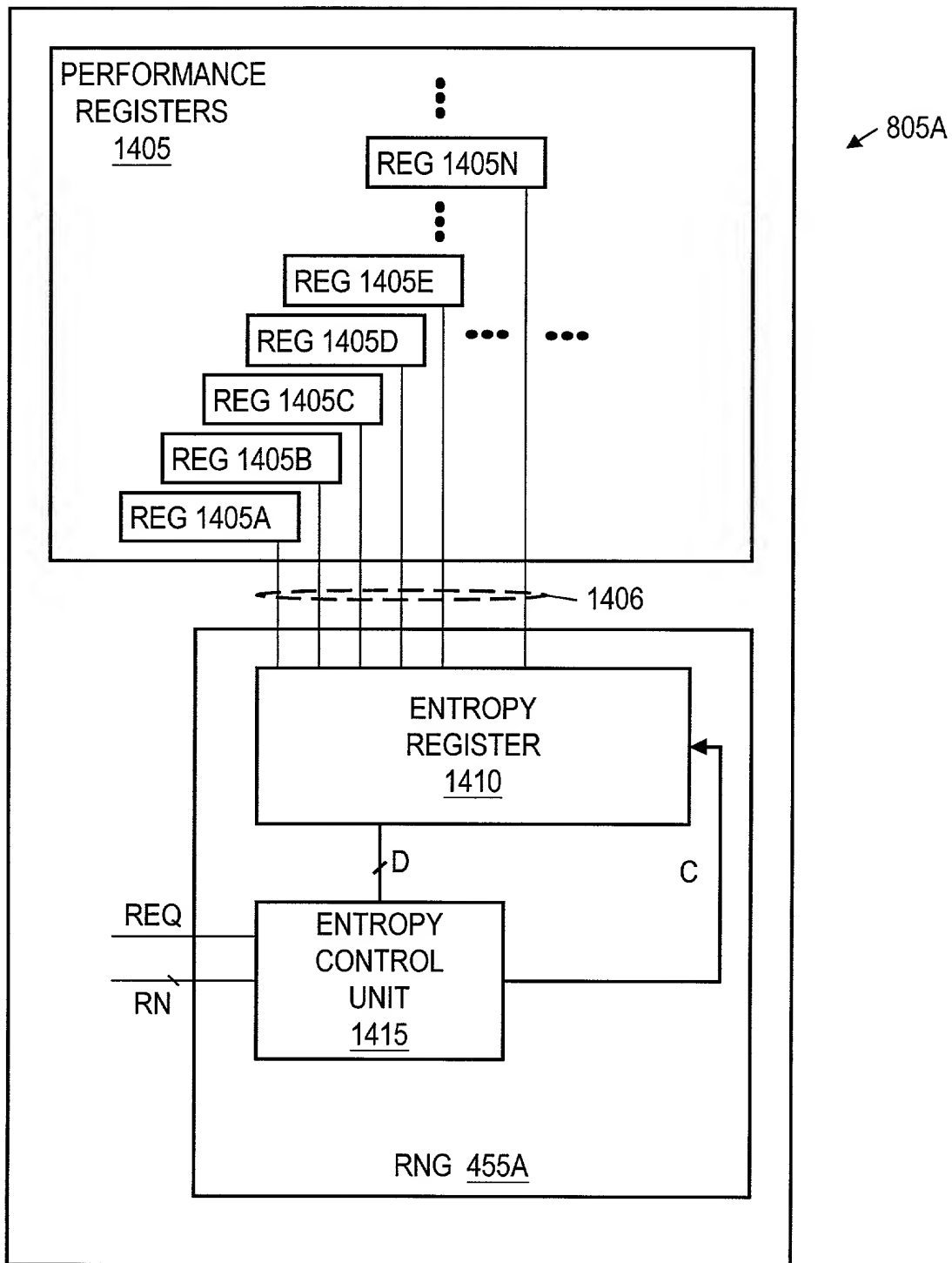


Fig. 14A

26 / 73

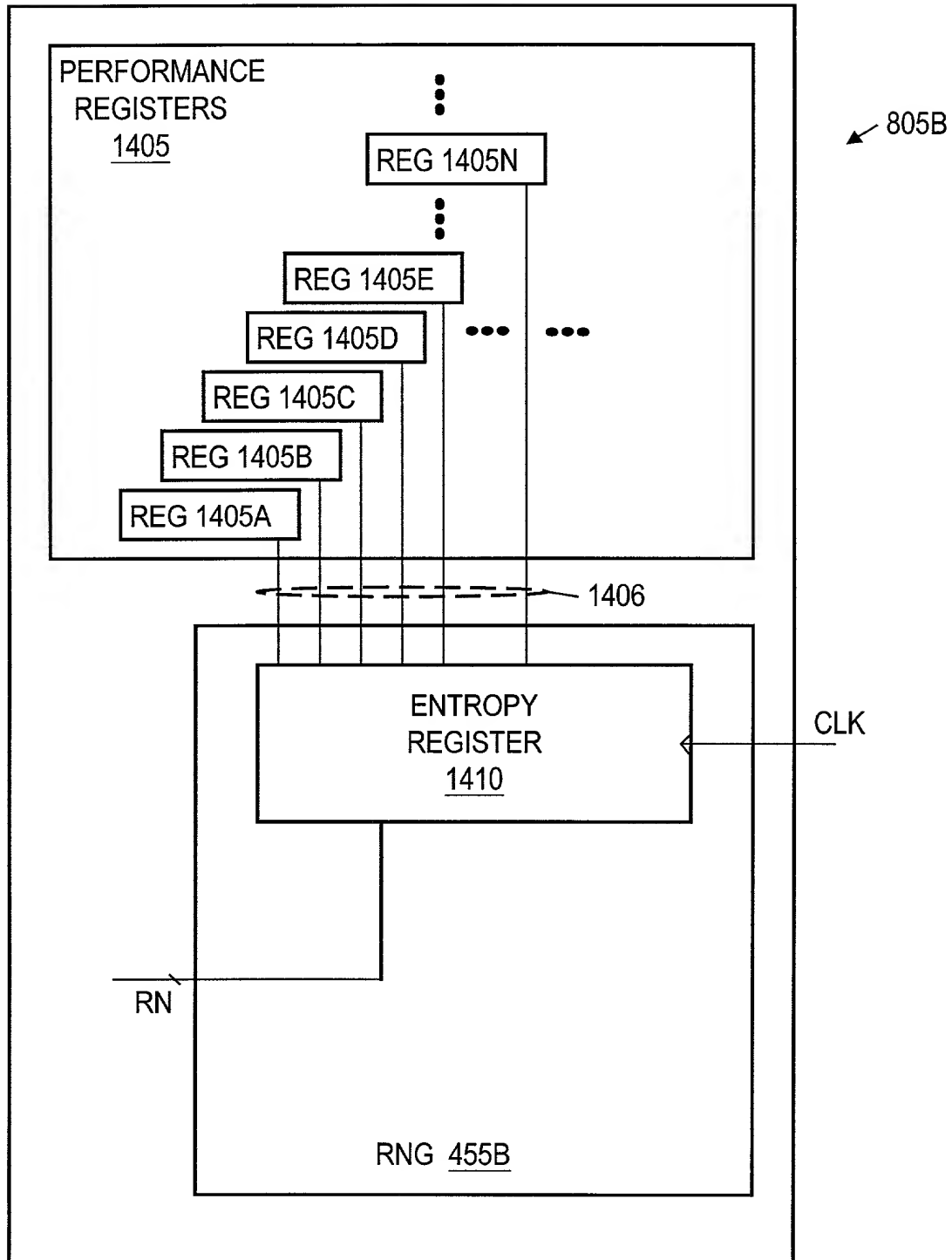


Fig. 14B

27 / 73

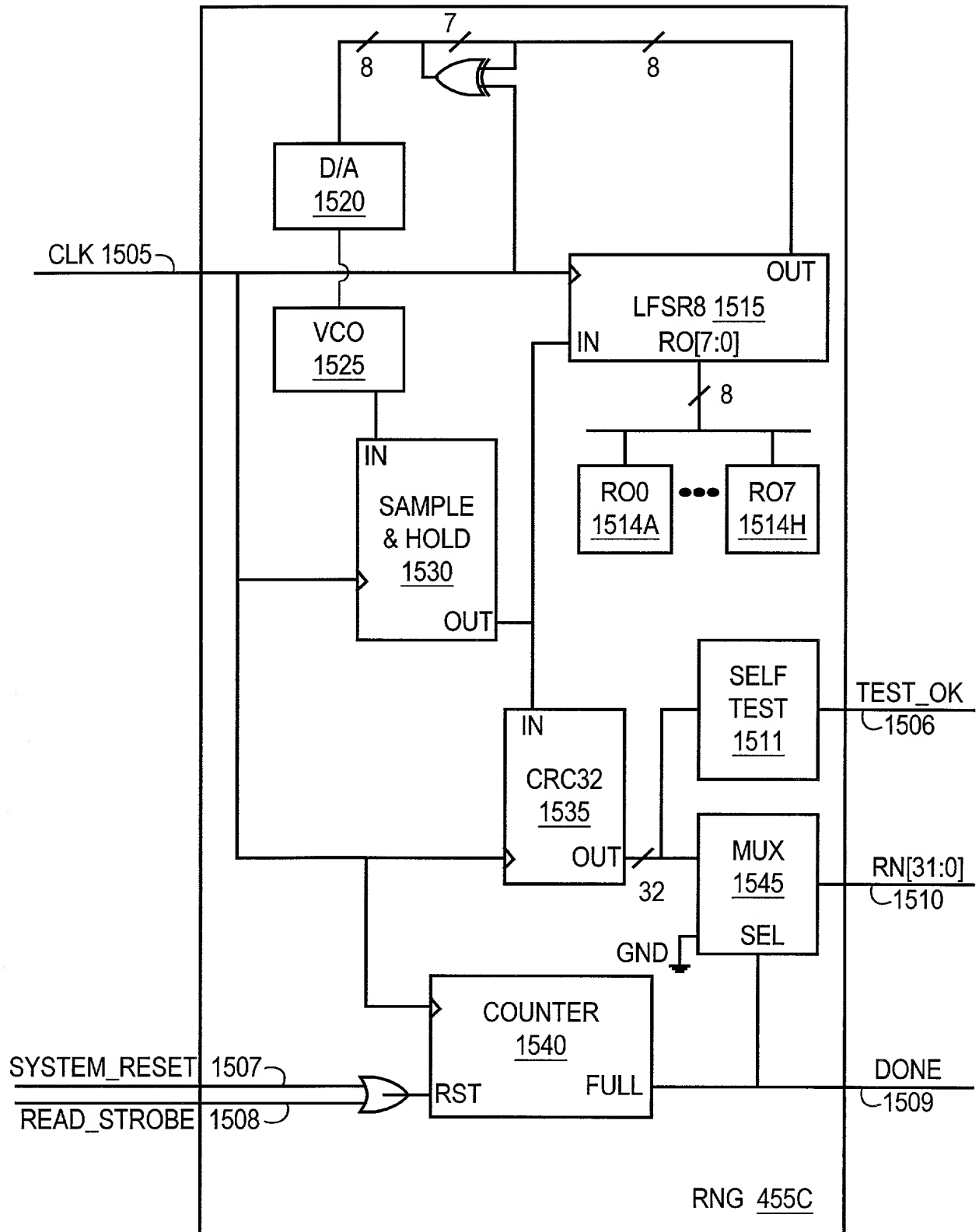


Fig. 15

28 / 73

1600A

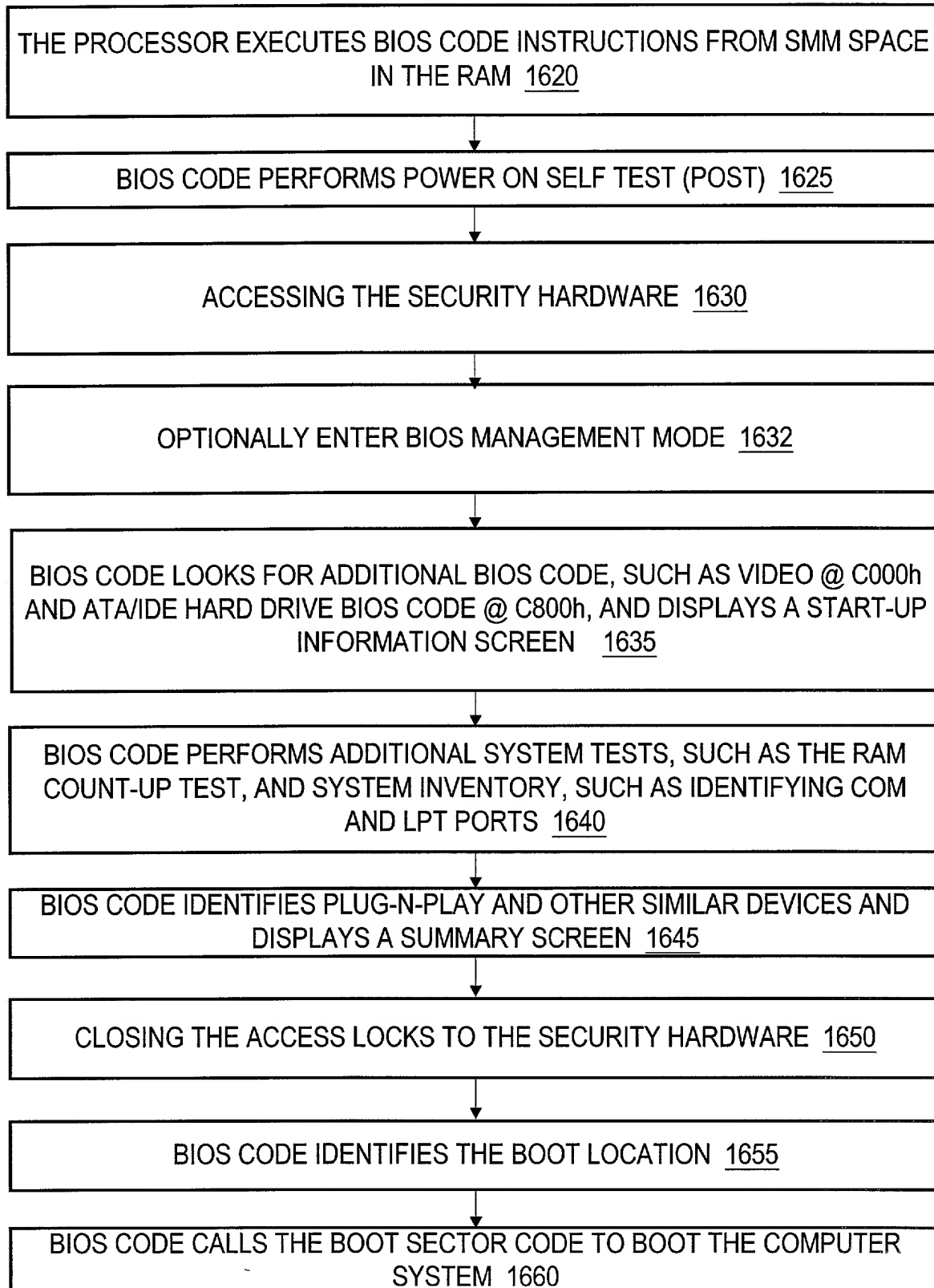


Fig. 16A

29 / 73

1600B

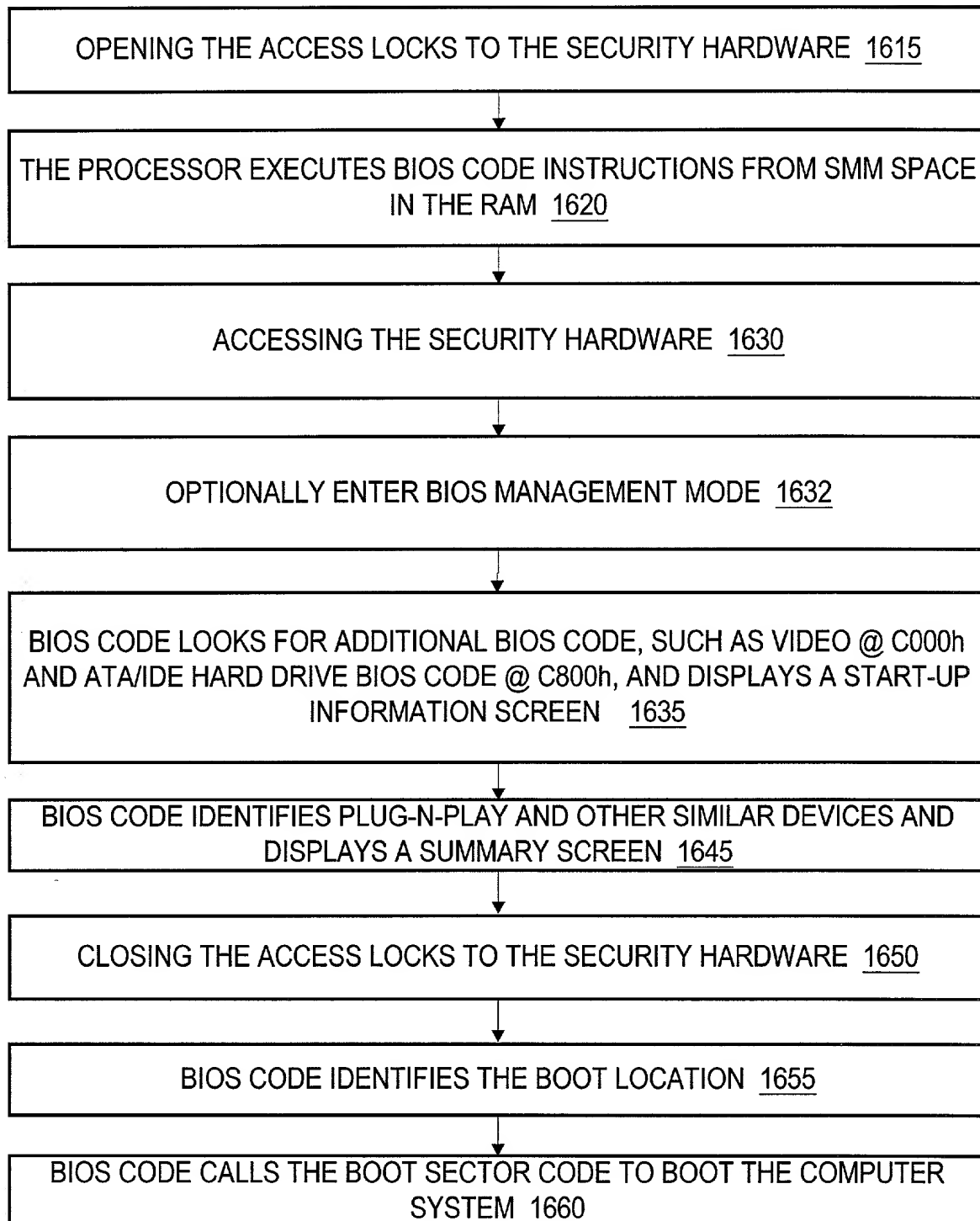


Fig. 16B

30 / 73

1600C

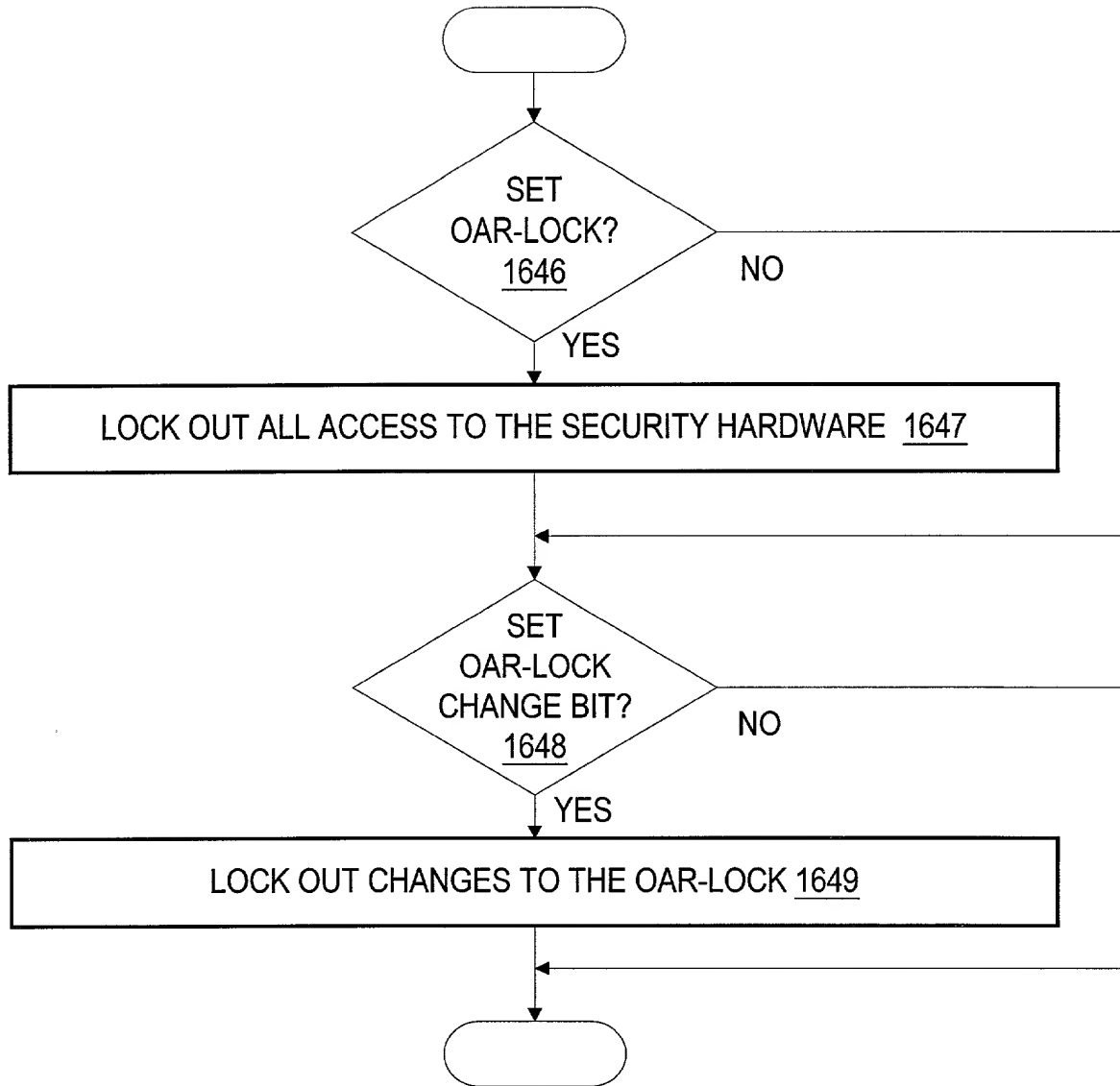


Fig. 16C

31 / 73

1600D

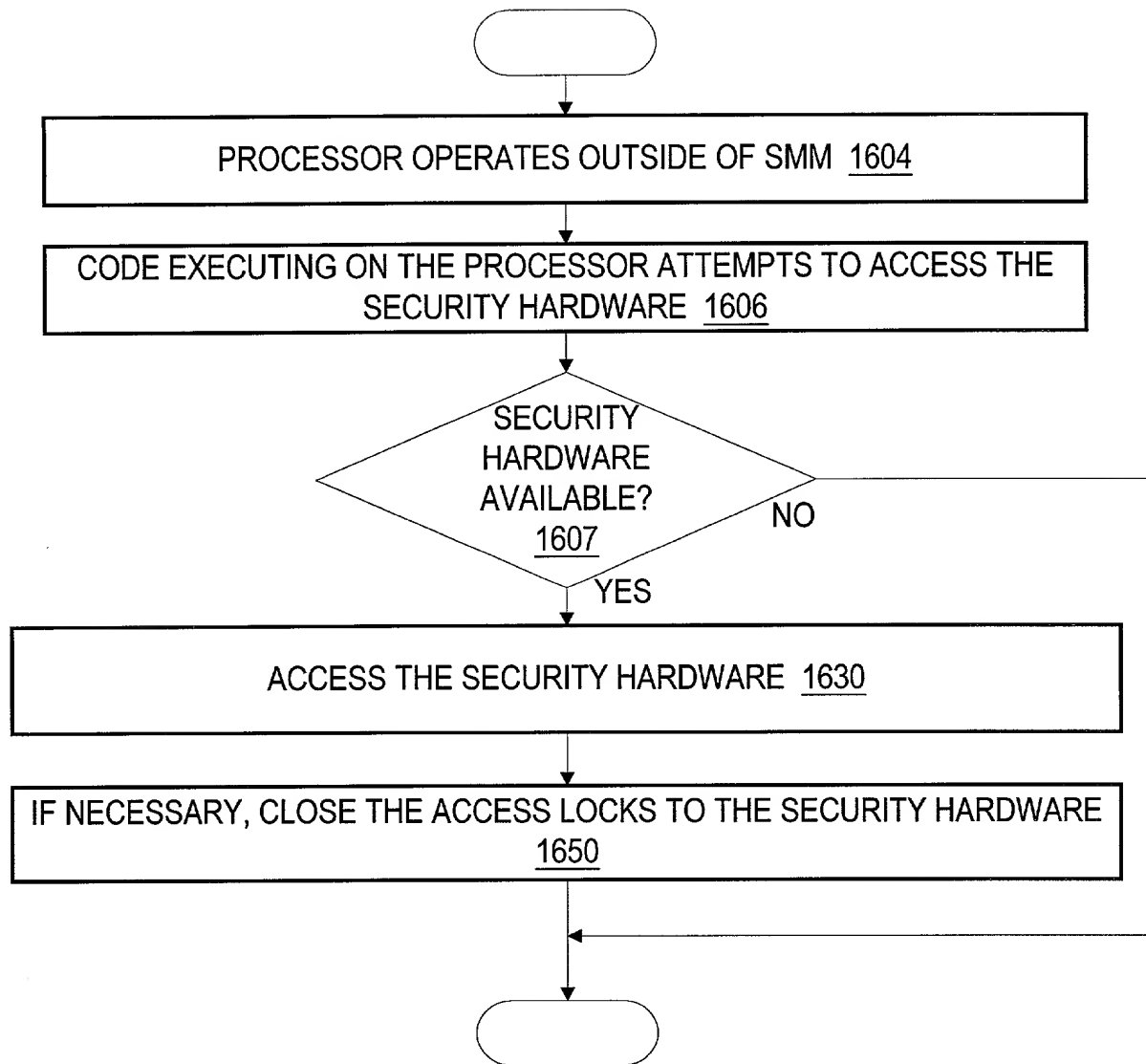


Fig. 16D

32 / 73

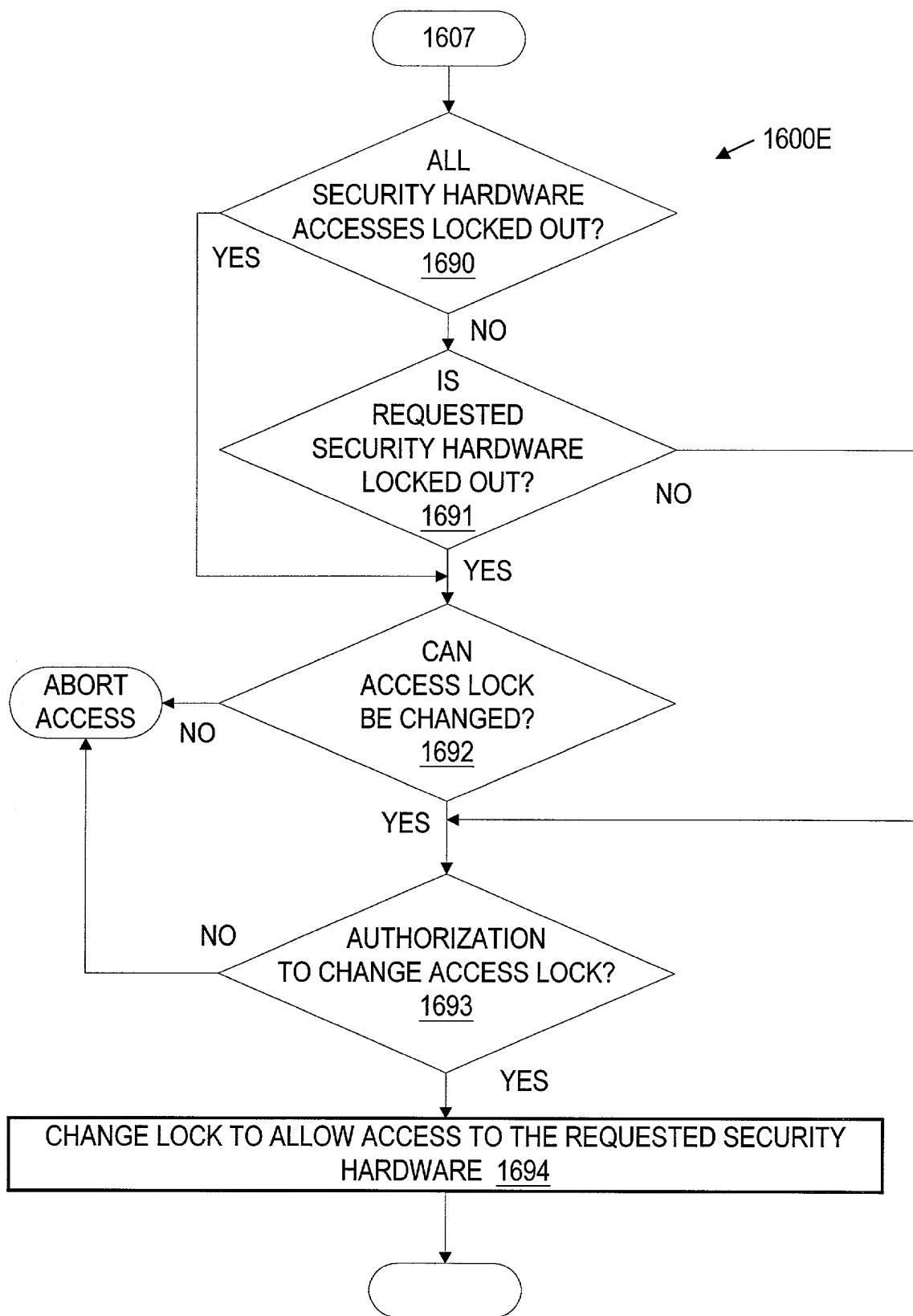


Fig. 16E



33 / 73

1600F

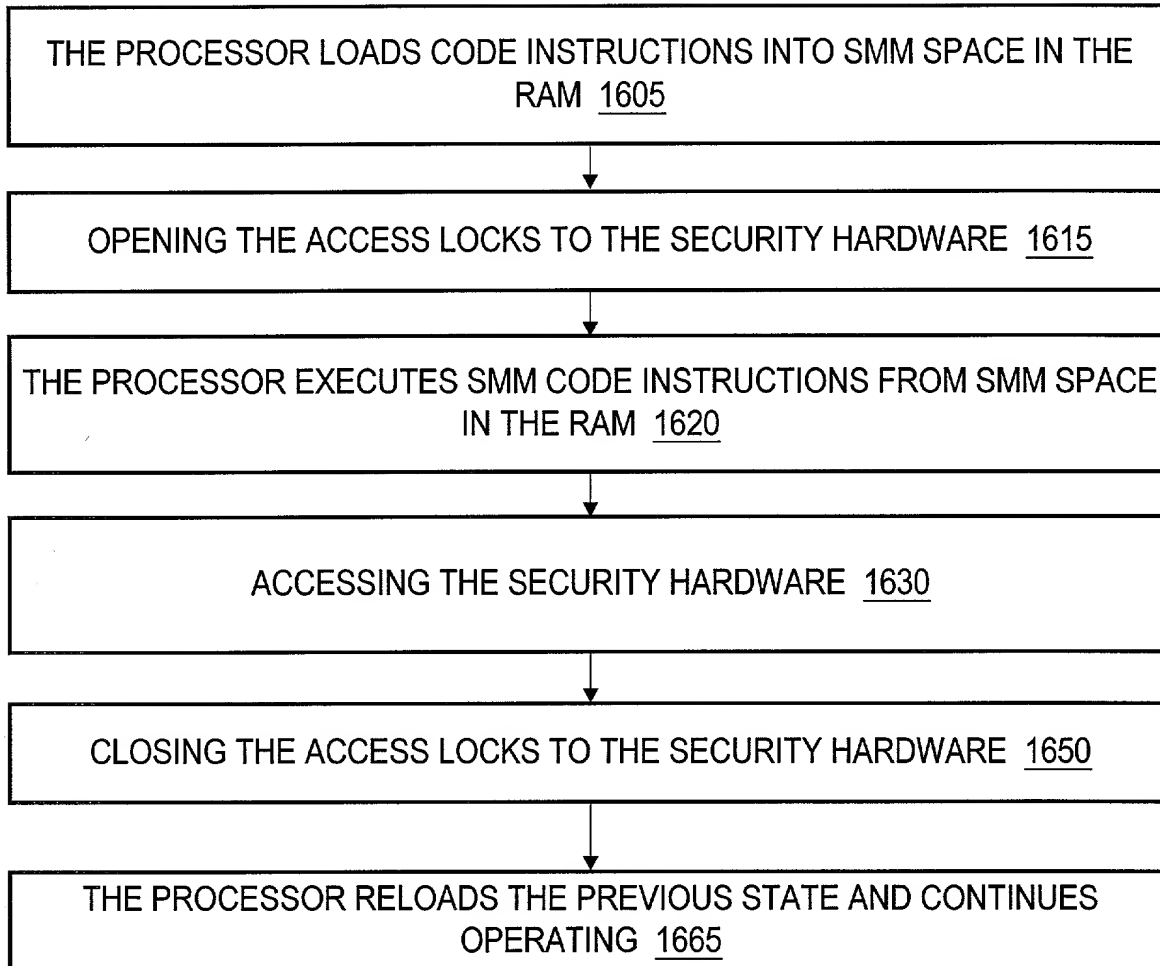


Fig. 16F

34 / 73

1600G

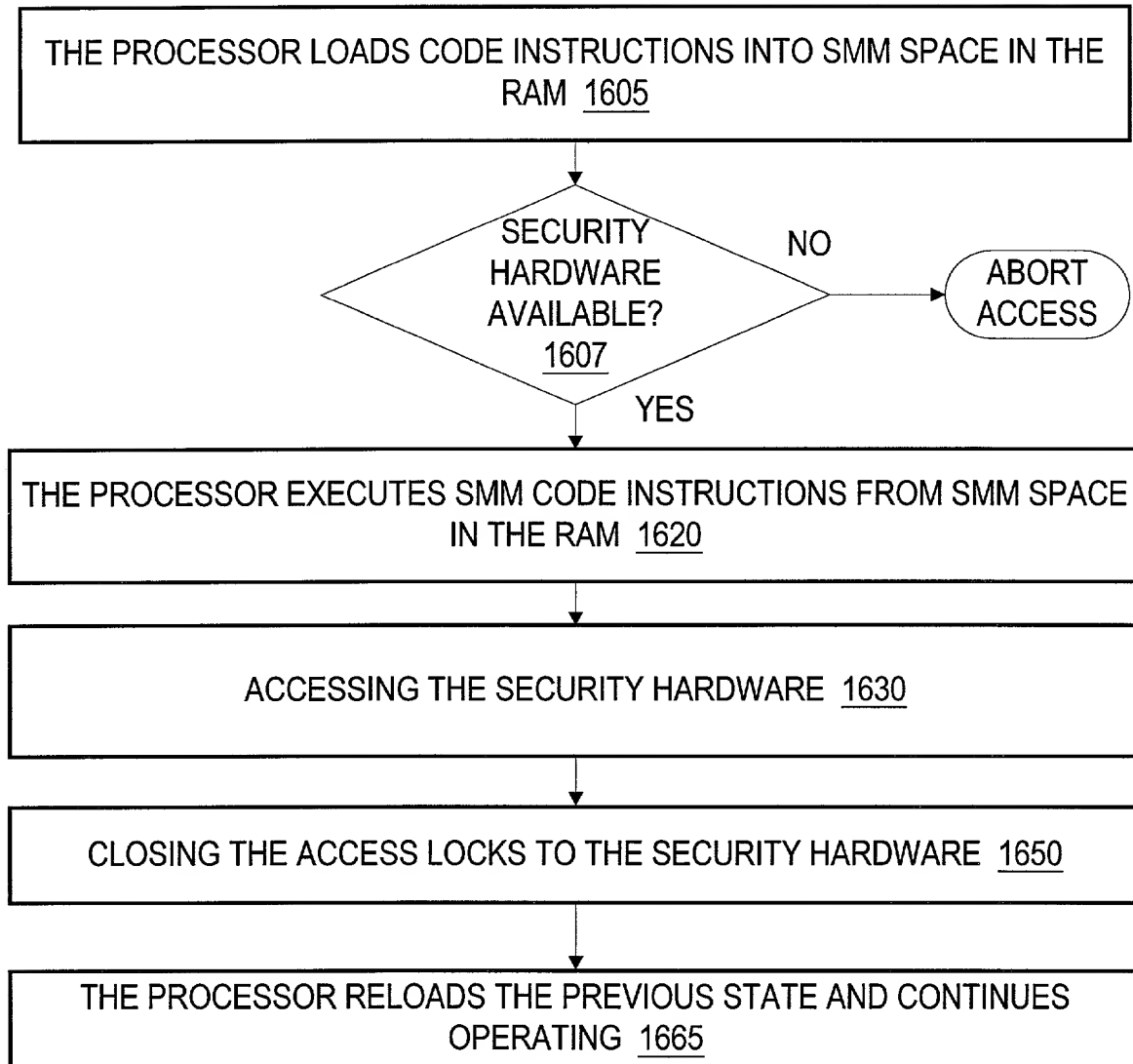


Fig. 16G

35 / 73

460A

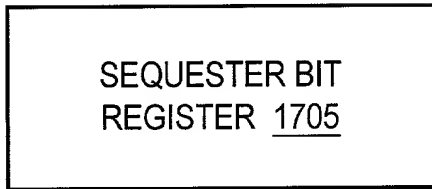


Fig. 17A

460B



Fig. 17B

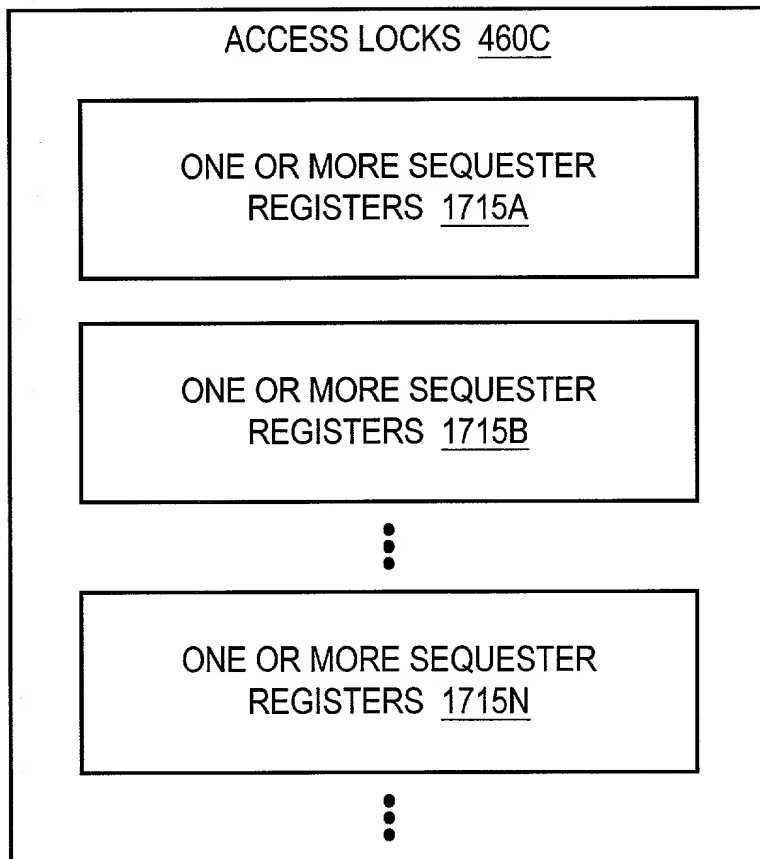


Fig. 17C

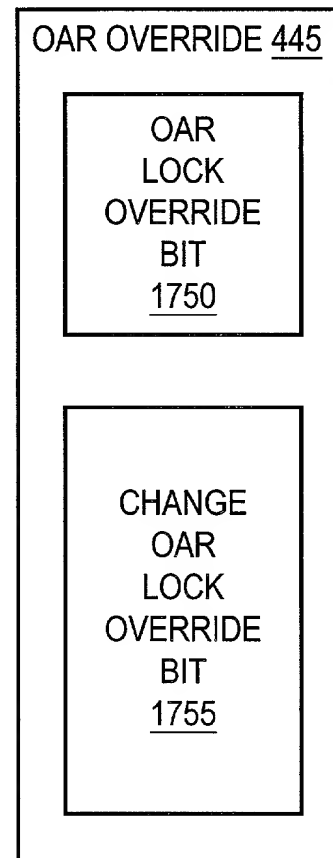
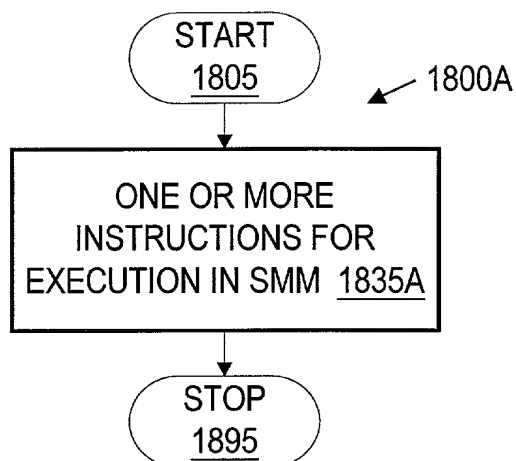


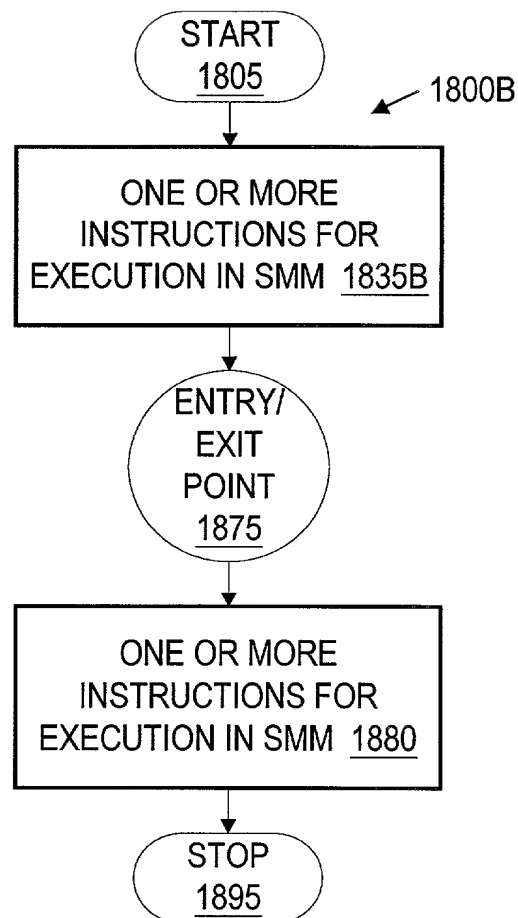
Fig. 17D

09020901 053000

36 / 73



**Fig. 18A**  
**PRIOR ART**



**Fig. 18B**

37 / 73

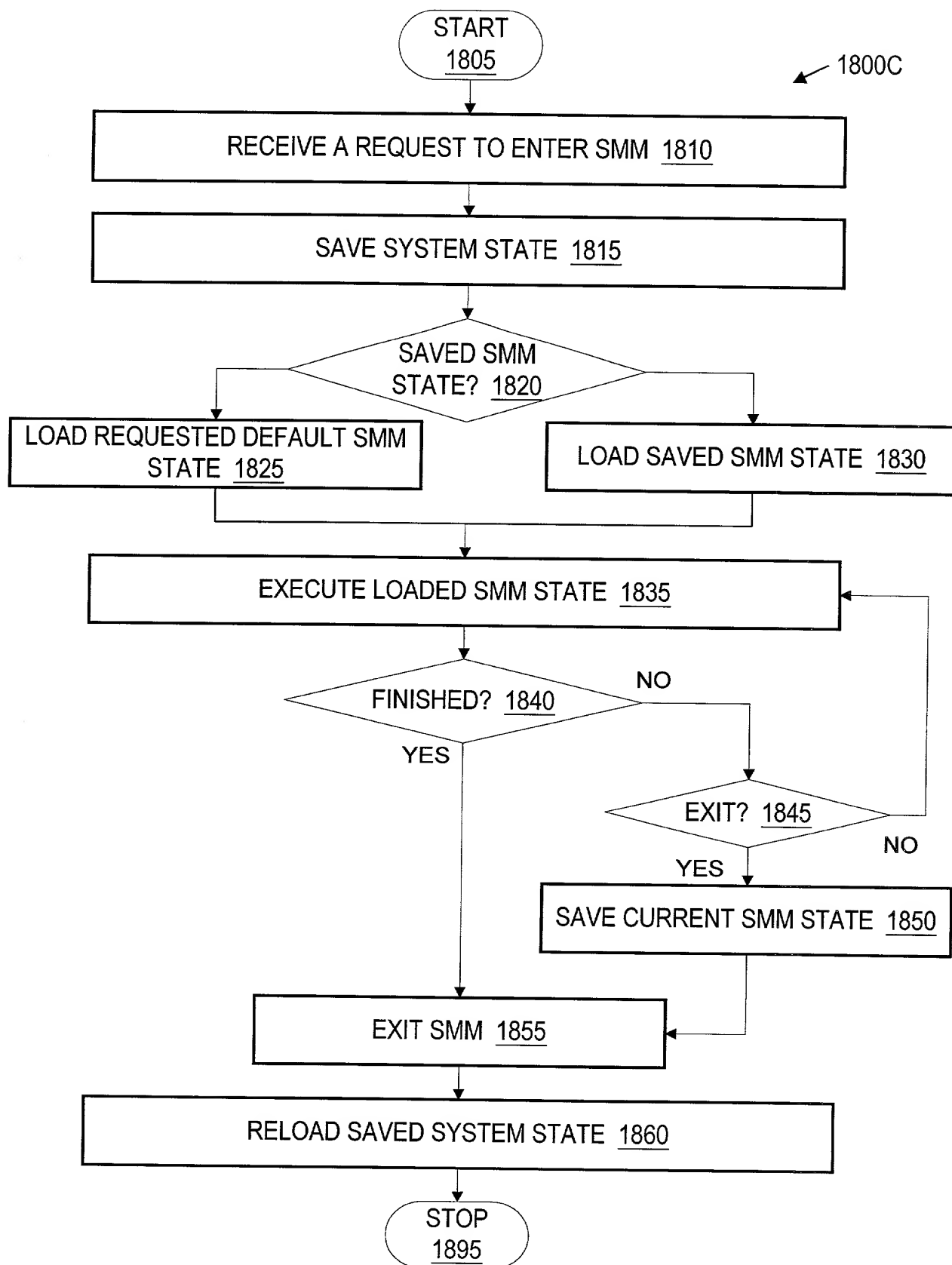


Fig. 18C

38 / 73

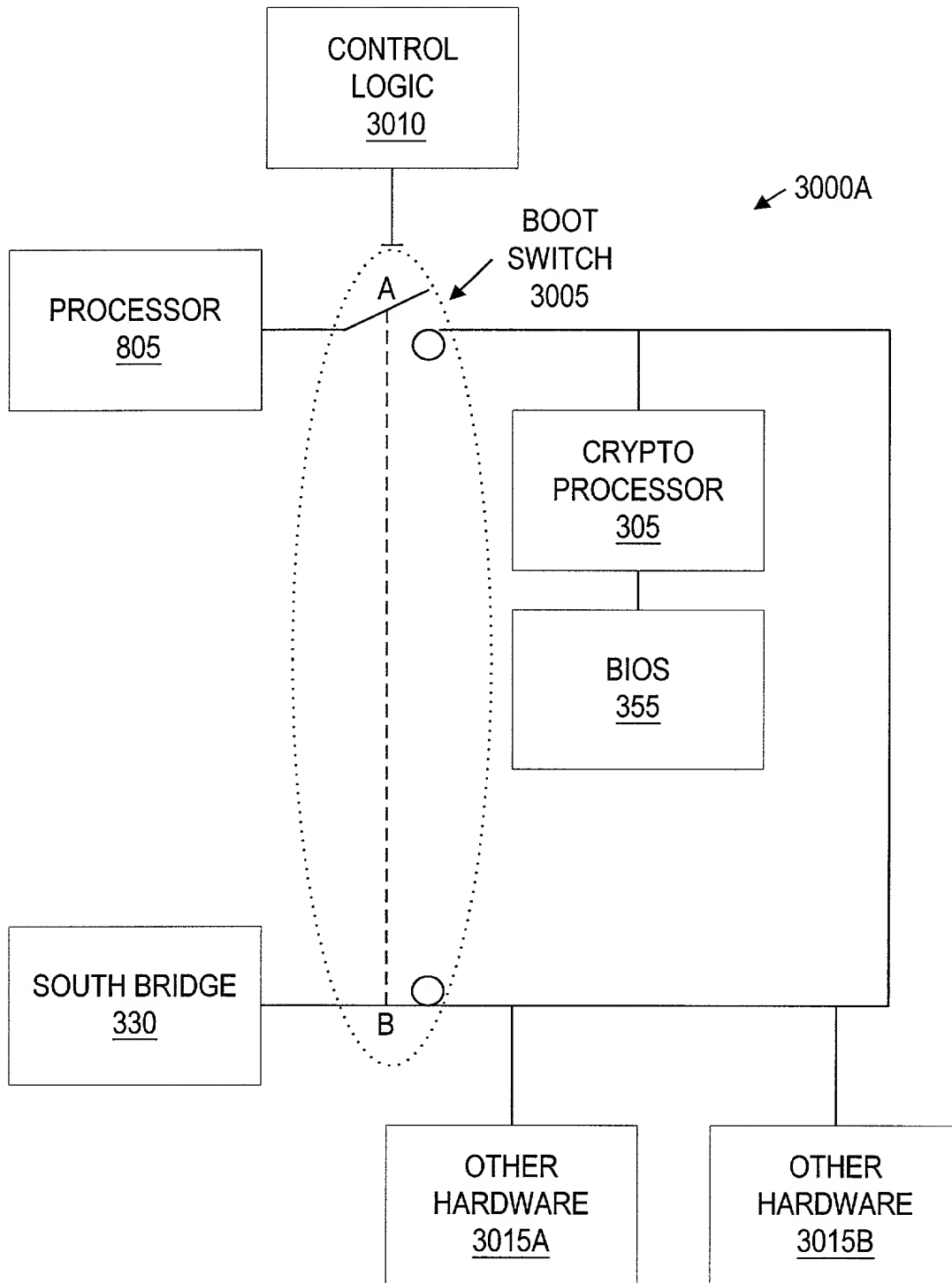
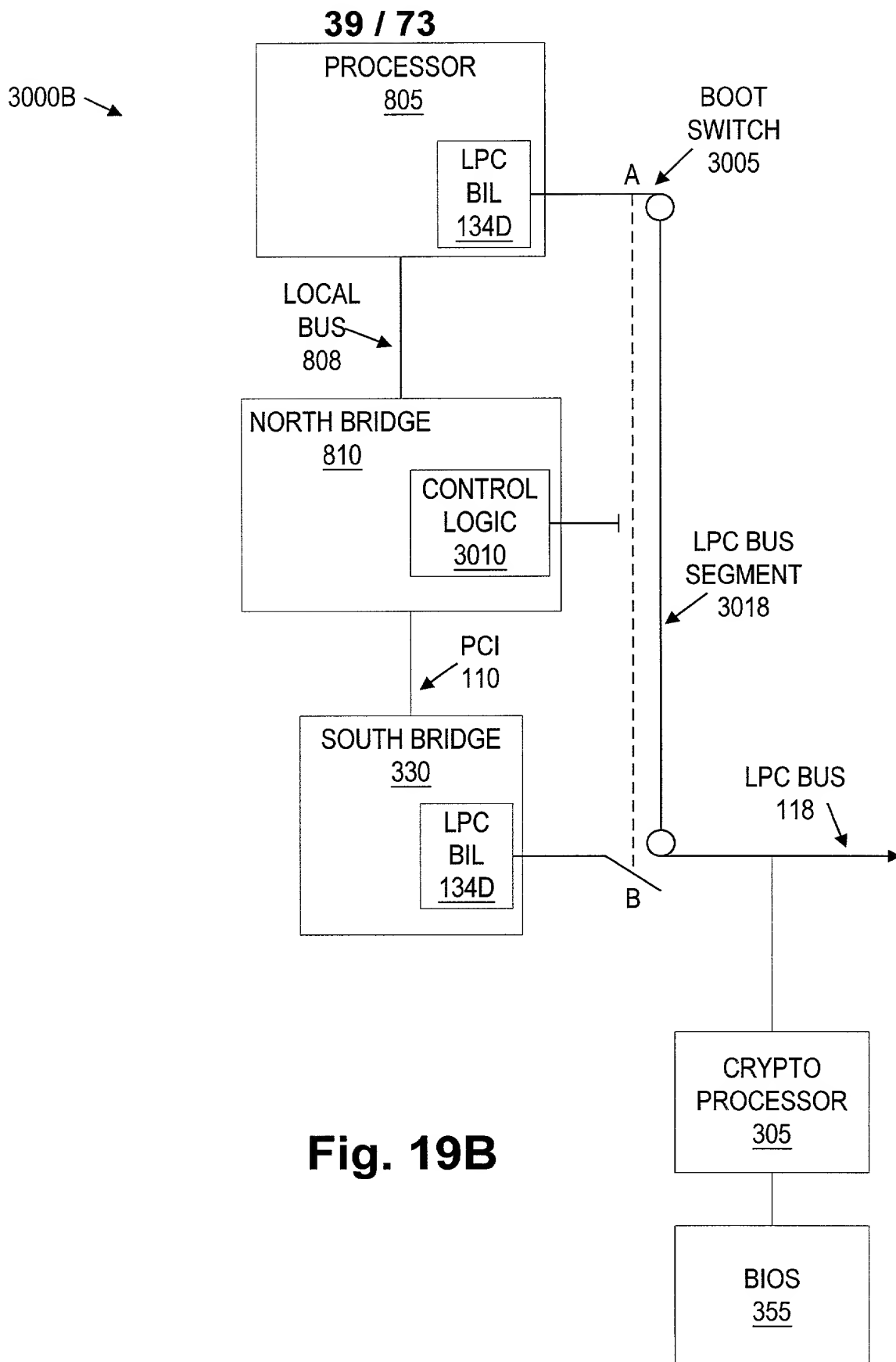


Fig. 19A



**Fig. 19B**

40 / 73

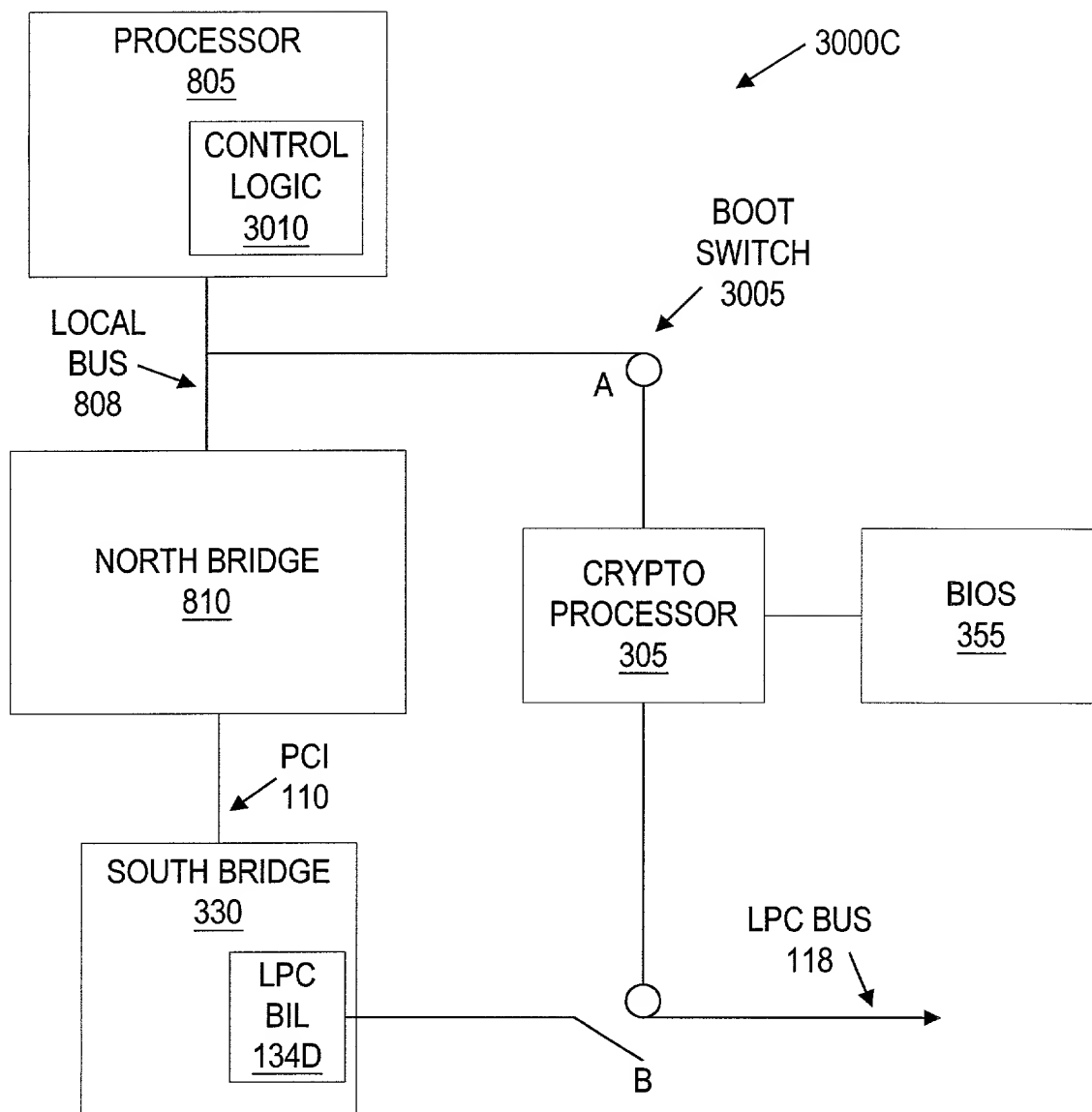
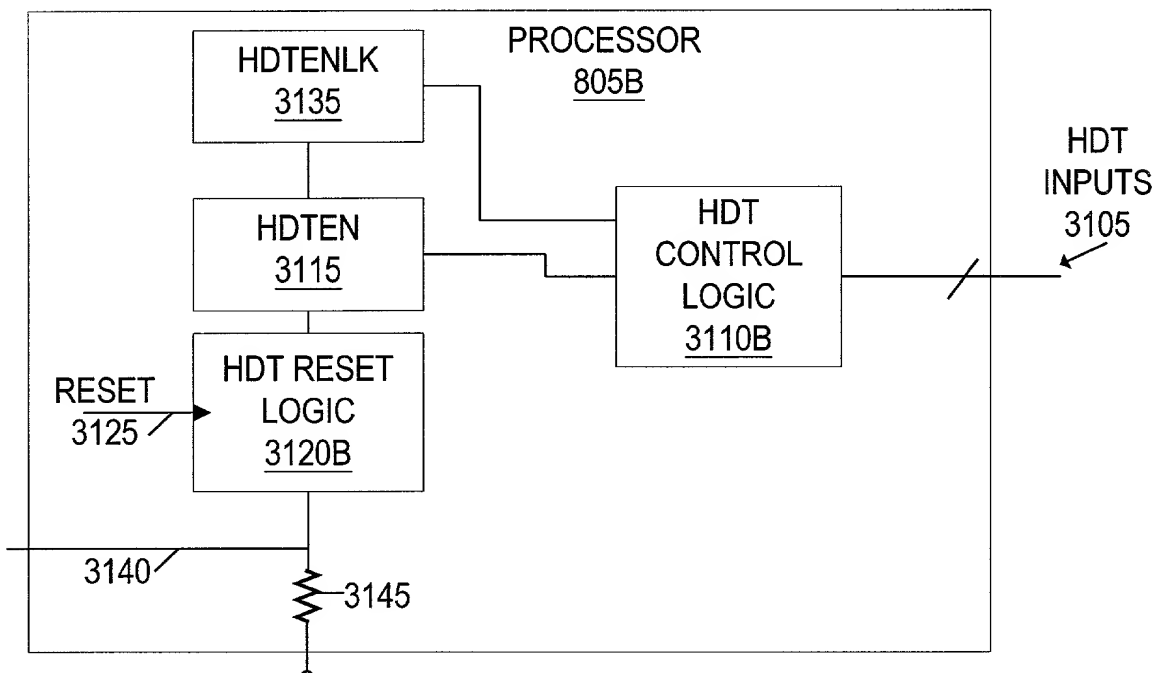
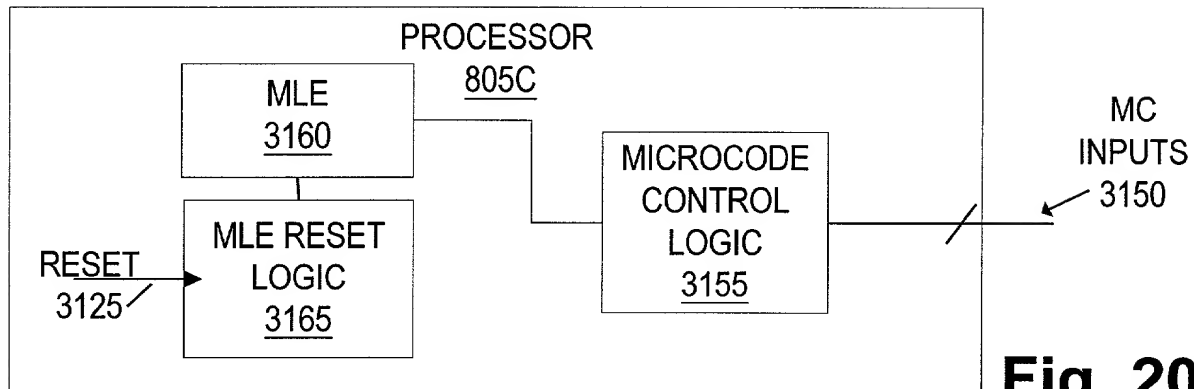


Fig. 19C

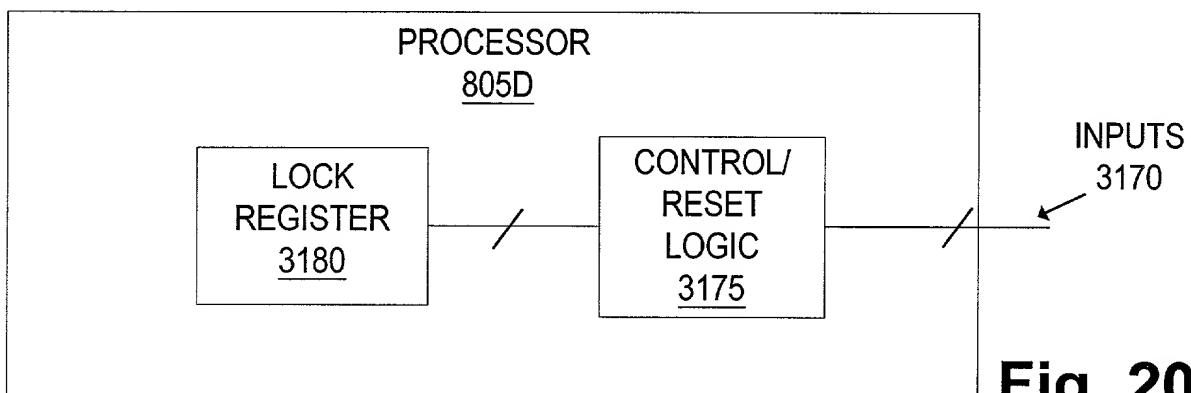




42 / 73



**Fig. 20C**



**Fig. 20D**

T.00050" 0880/2860

43 / 73

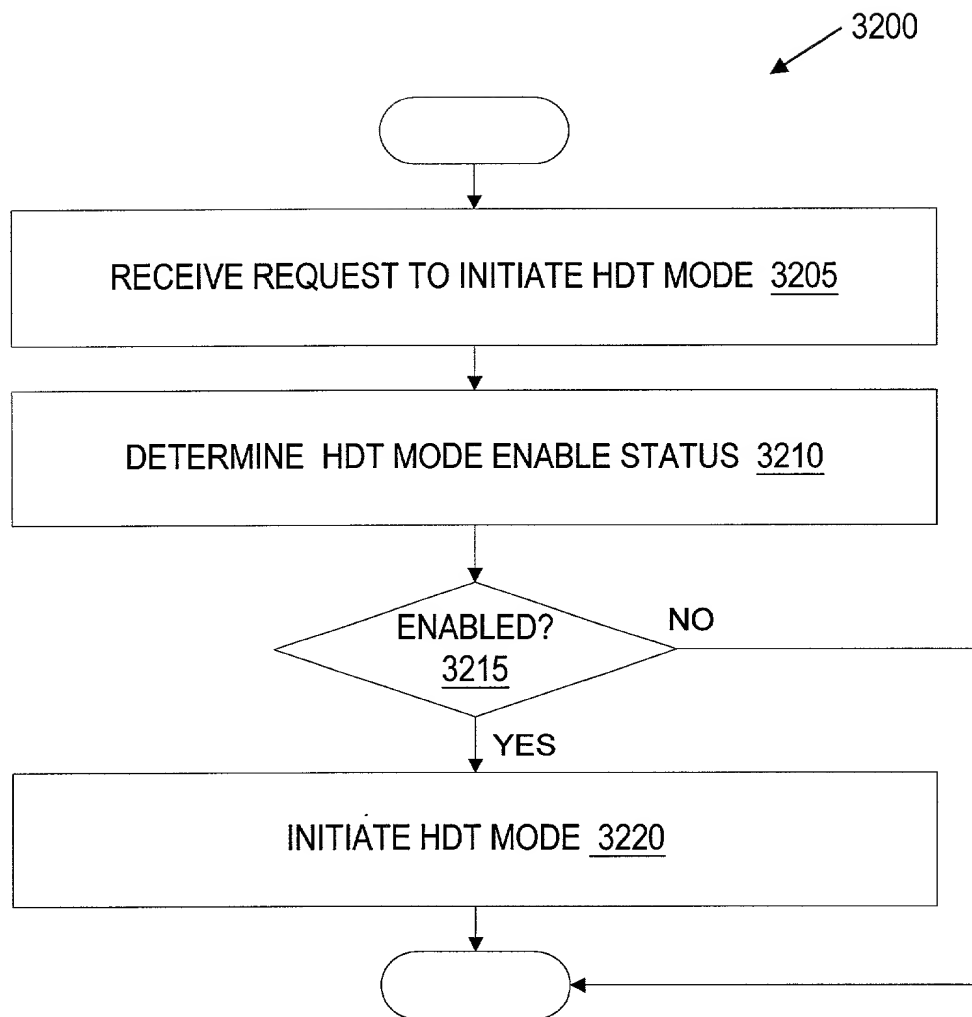


Fig. 21

44 / 73

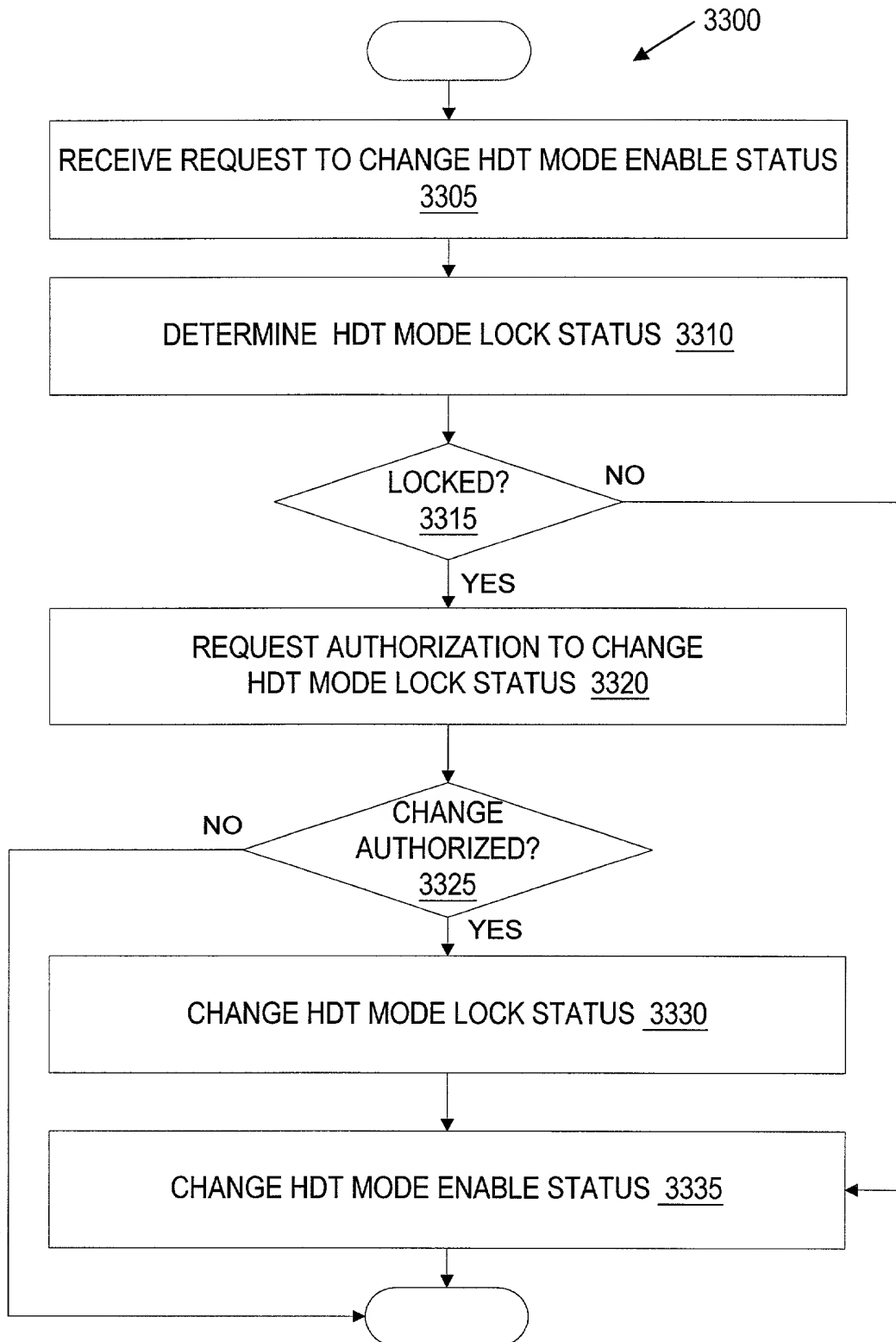


Fig. 22

45 / 73

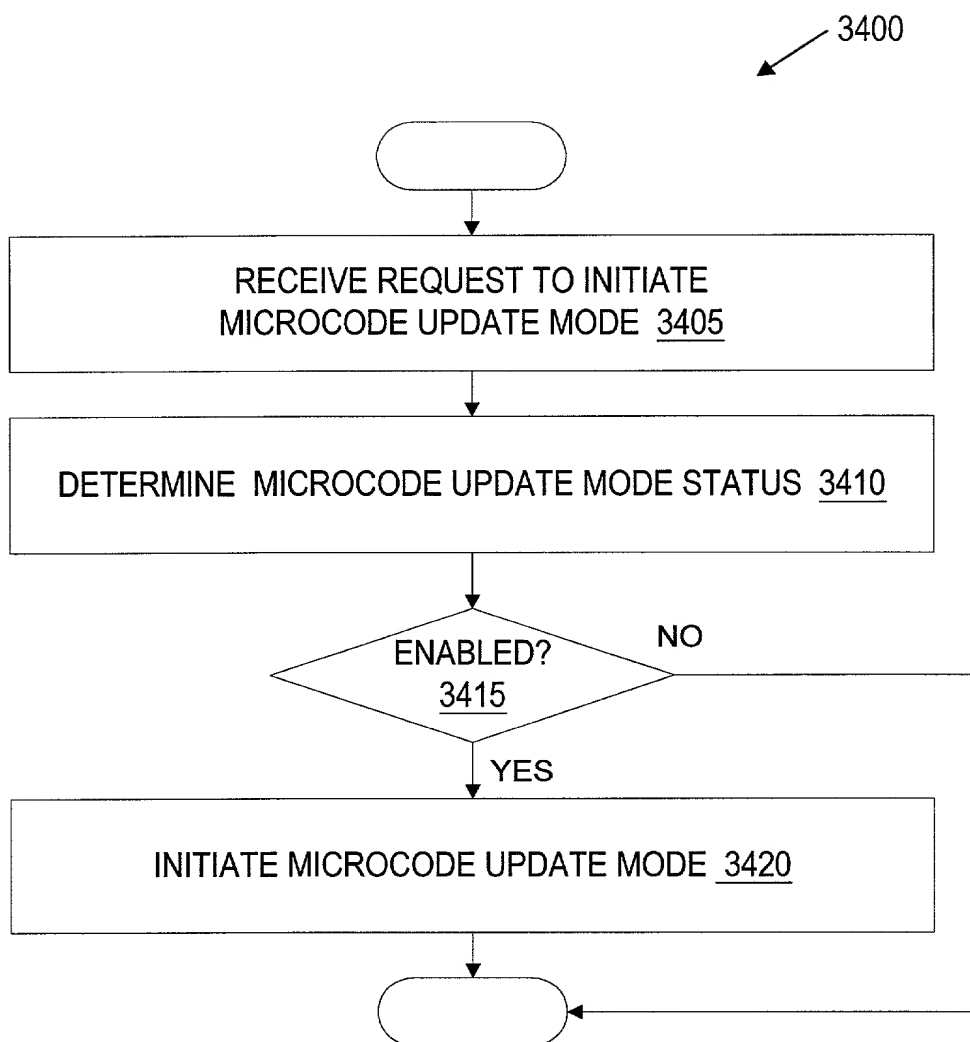


Fig. 23

46 / 73

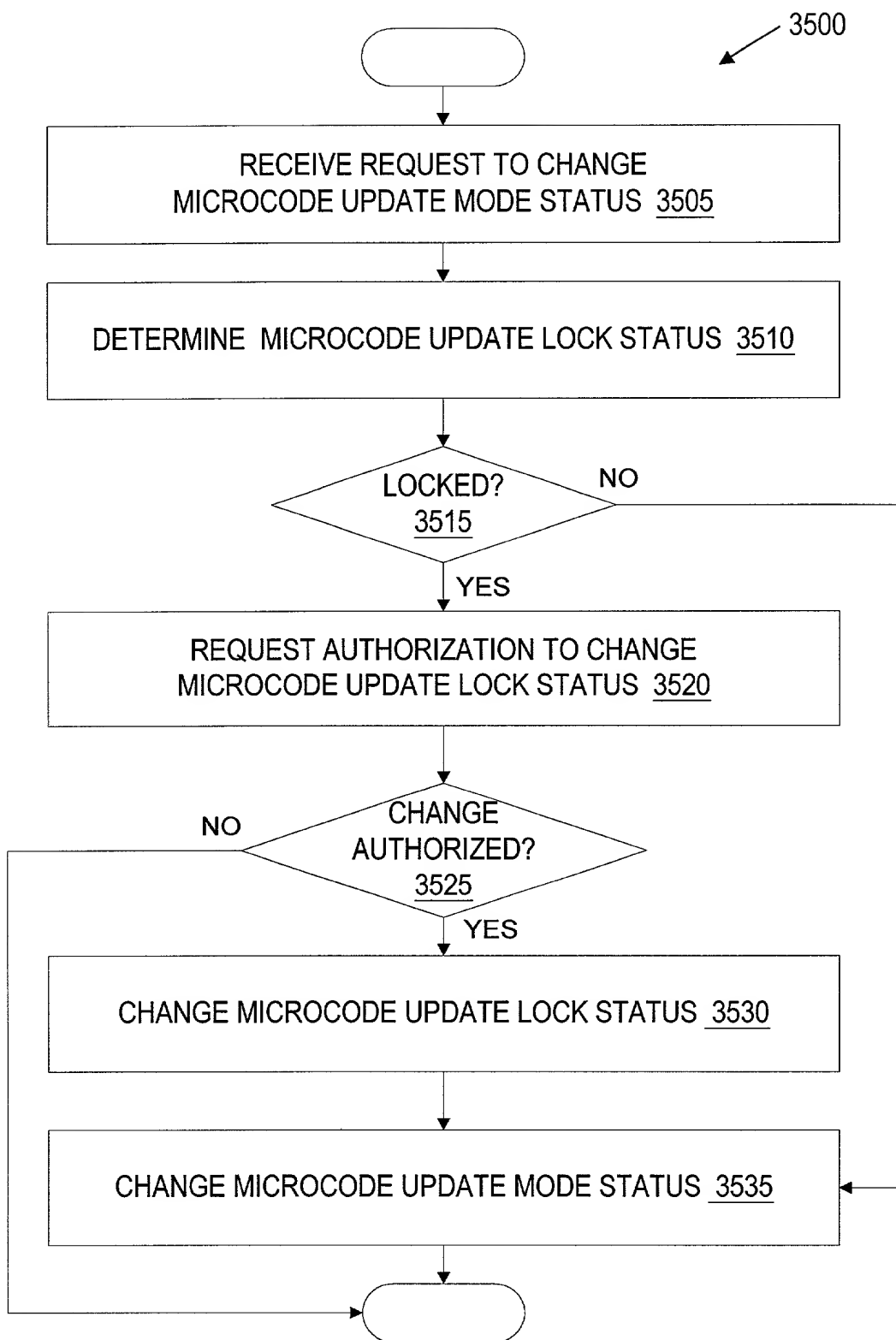
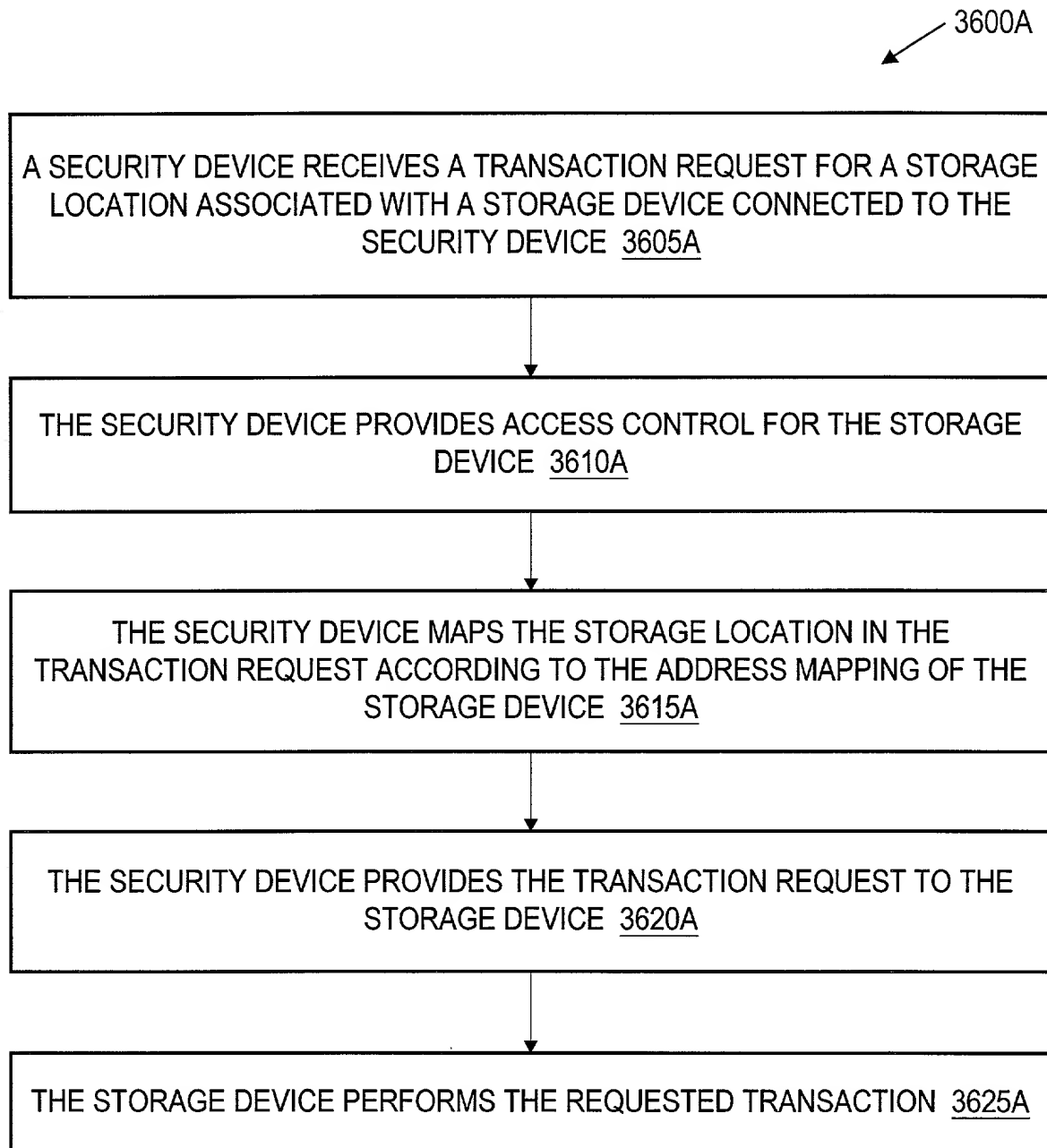


Fig. 24

47 / 73



**Fig. 25A**

48 / 73

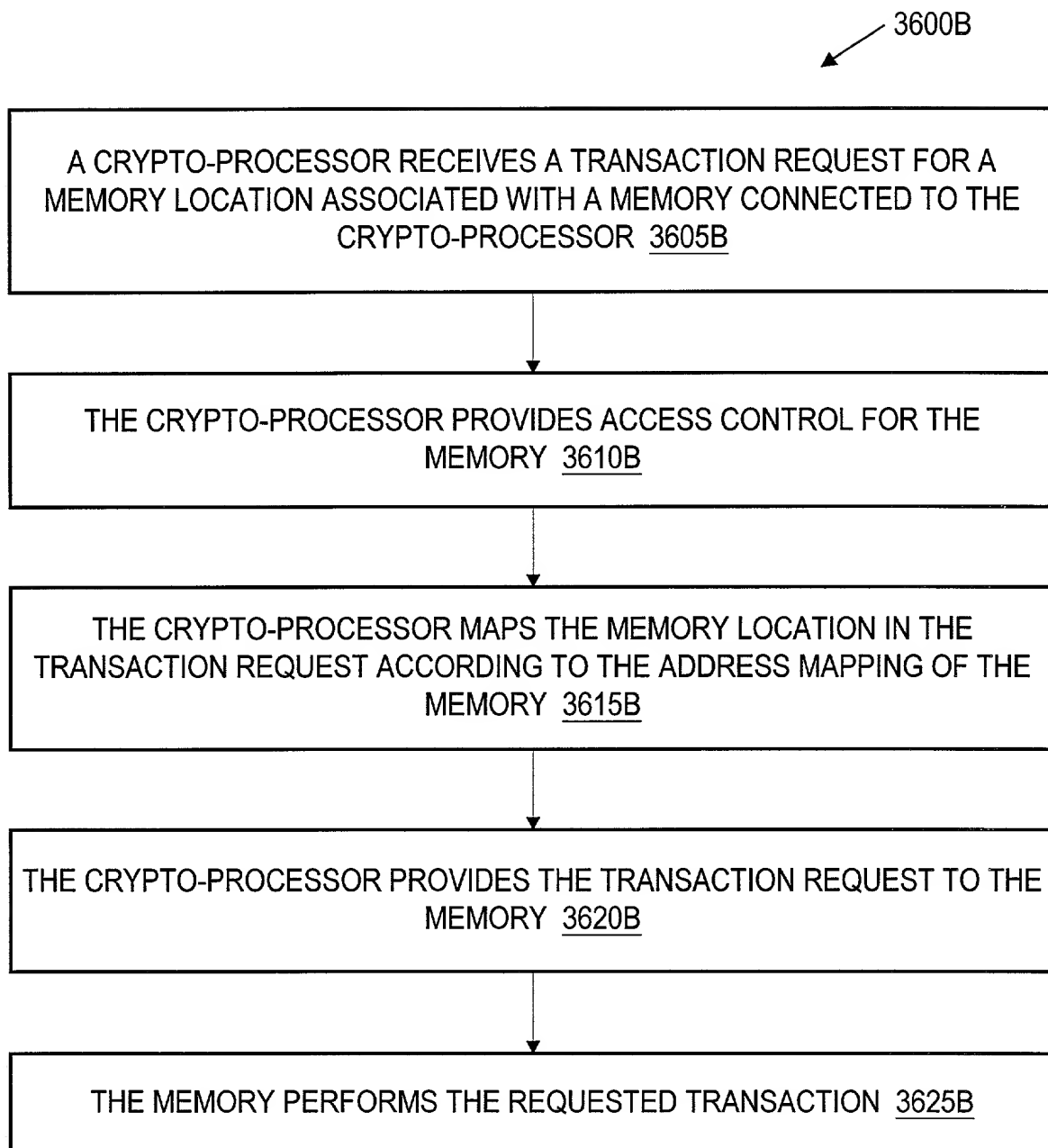


Fig. 25B



49 / 73

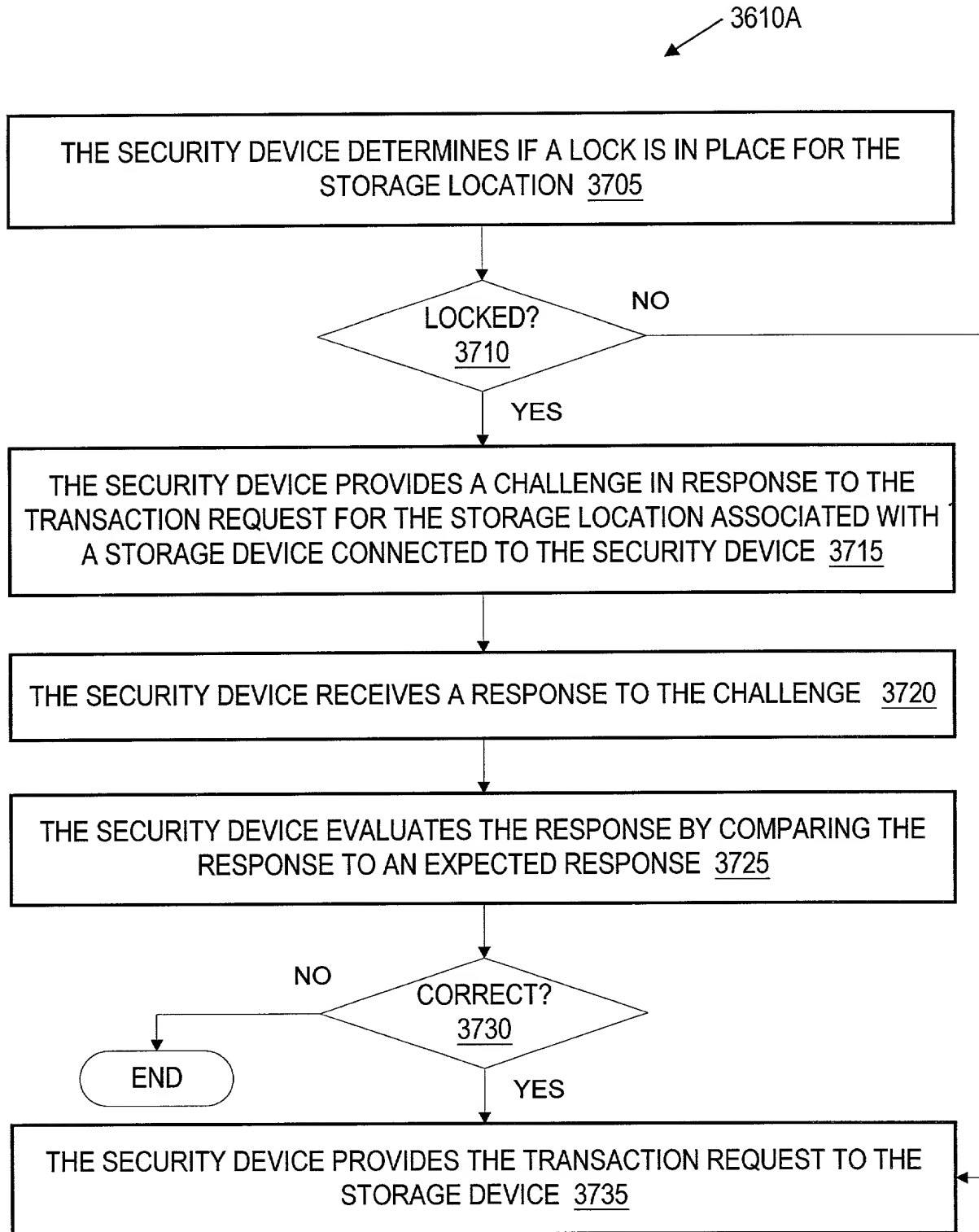


Fig. 26

50 / 73

3620

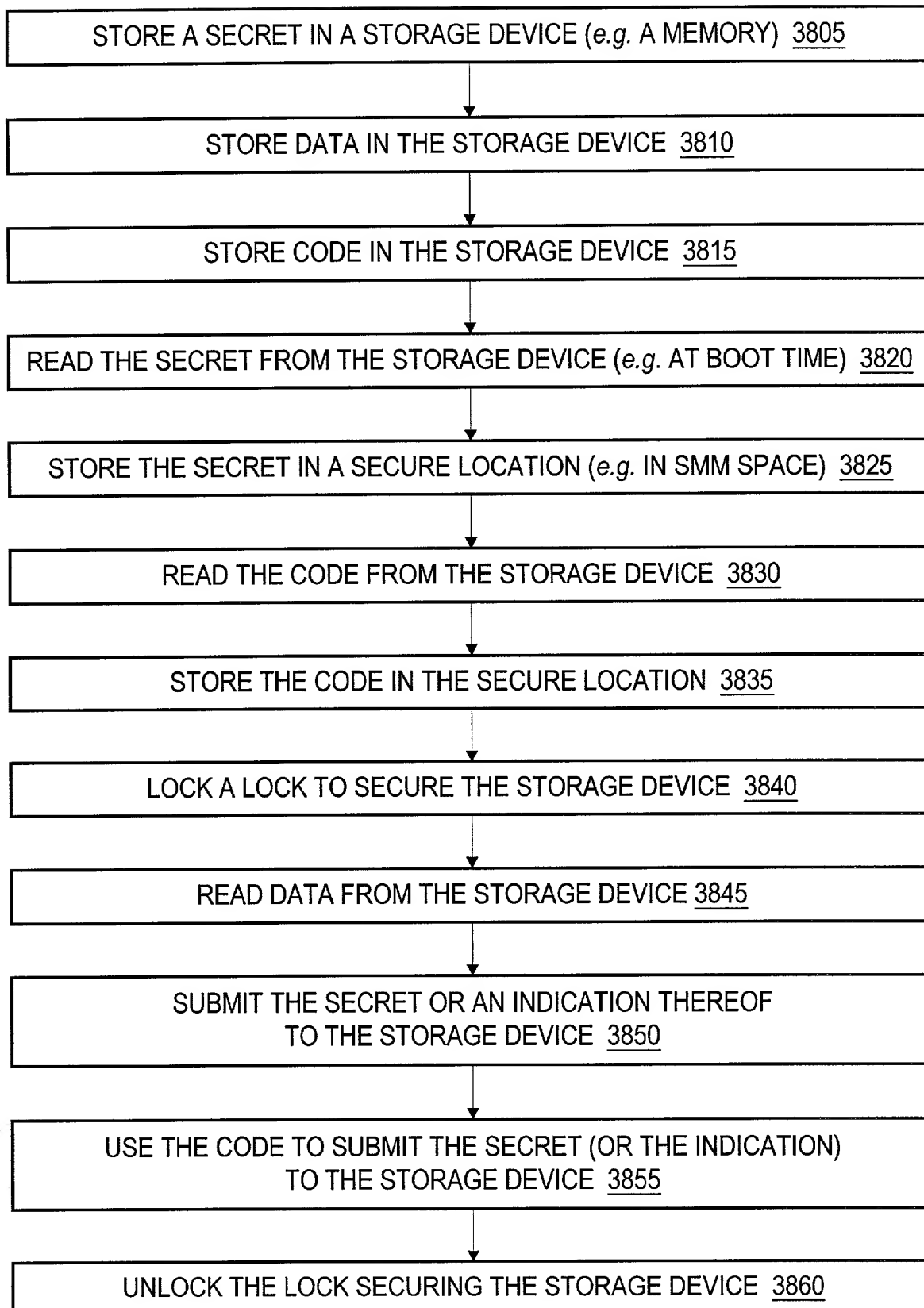
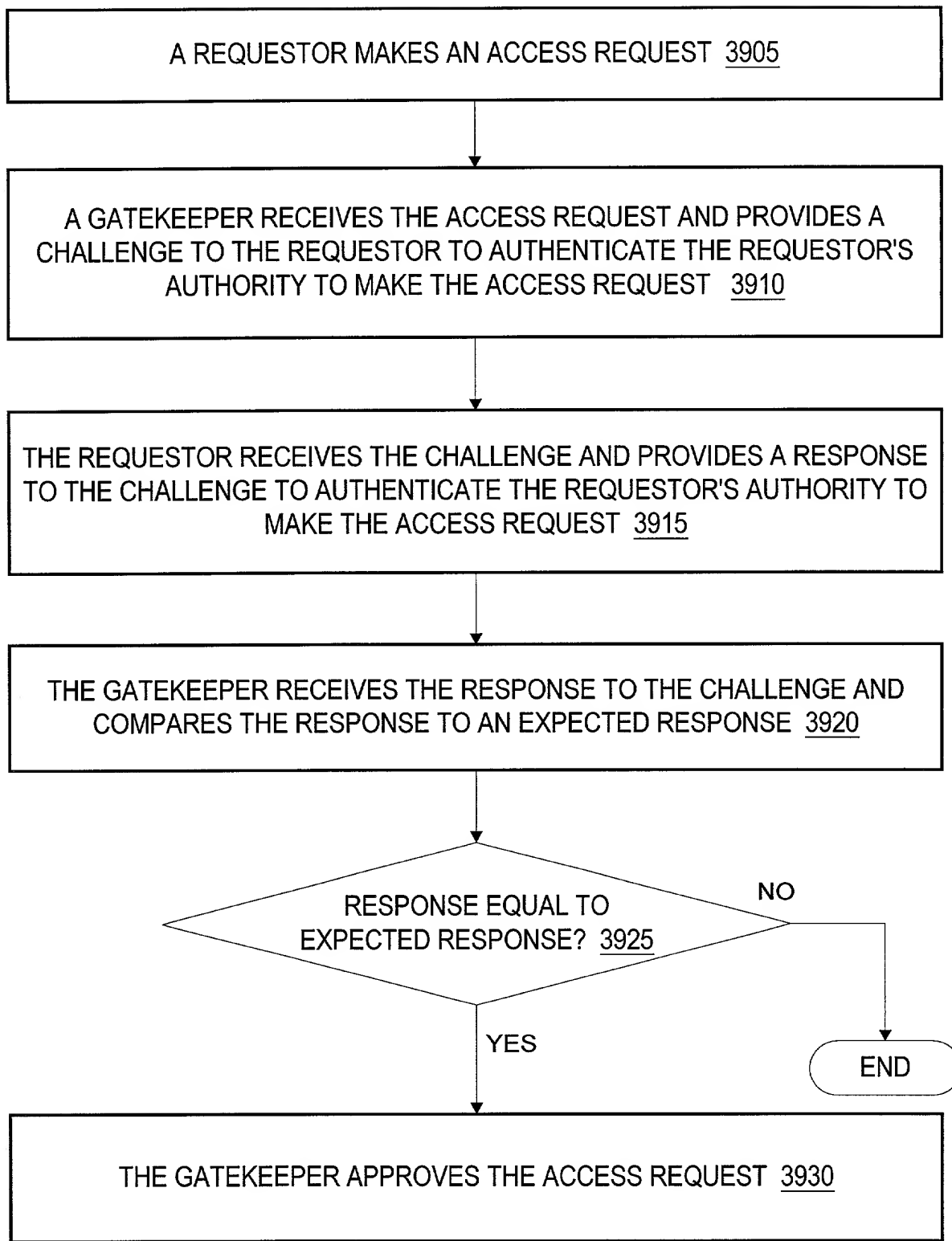


Fig. 27

51 / 73

3900



**Fig. 28**  
**(Prior Art)**

52 / 73

4000A

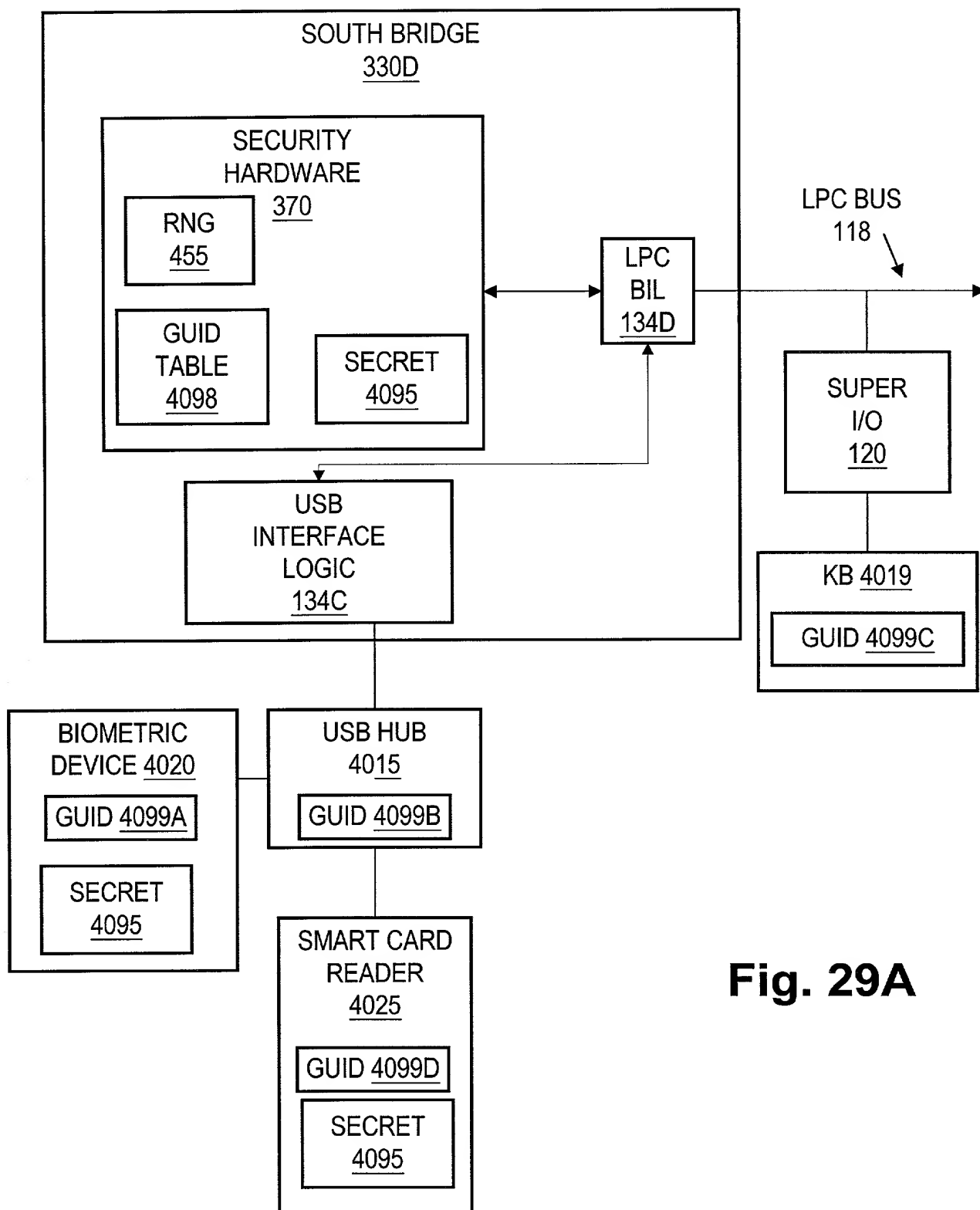


Fig. 29A

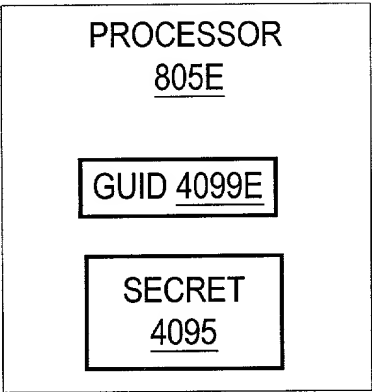


Fig. 29B

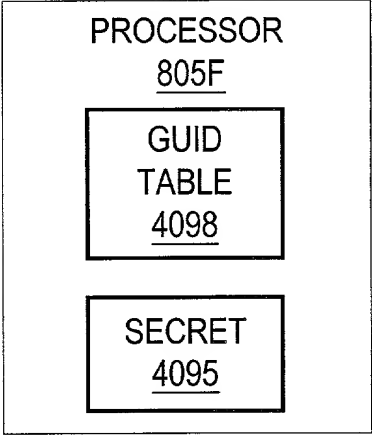


Fig. 29C

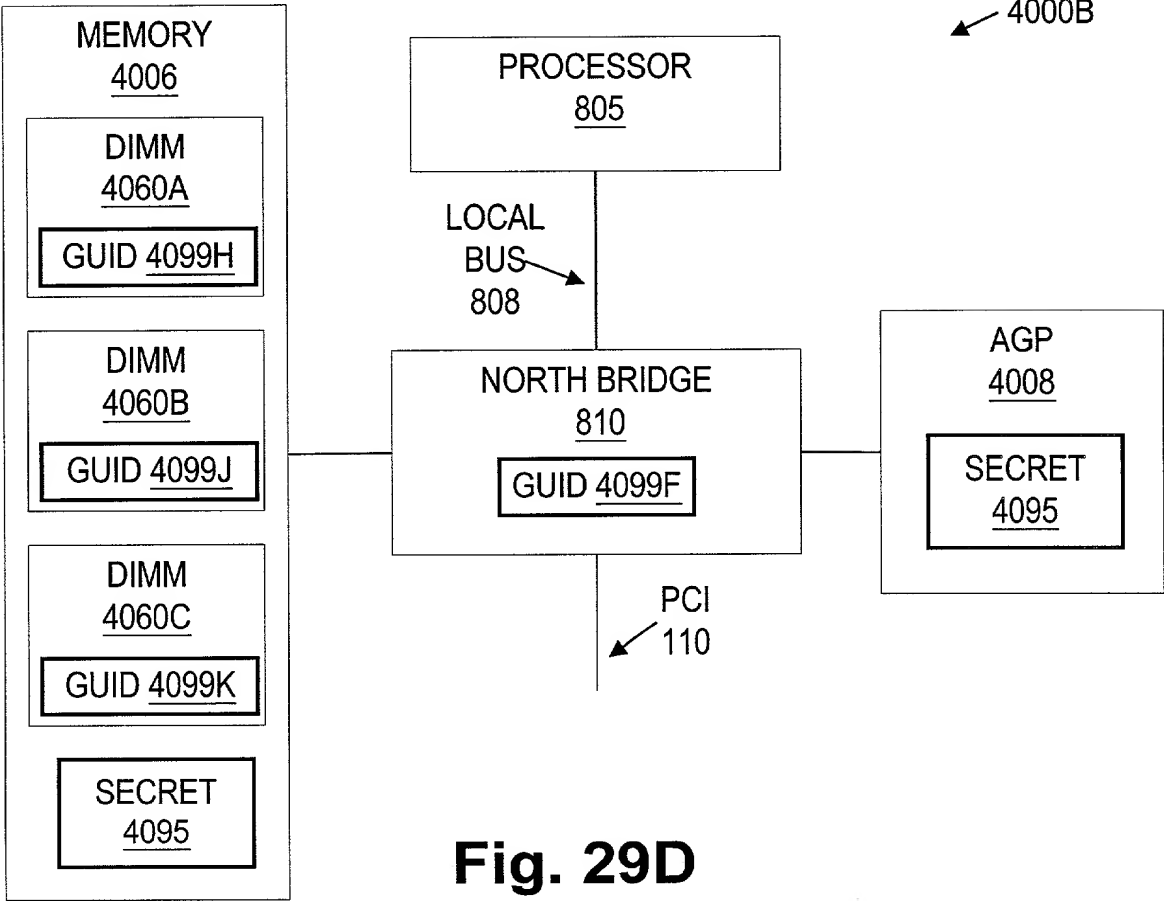


Fig. 29D

54 / 73

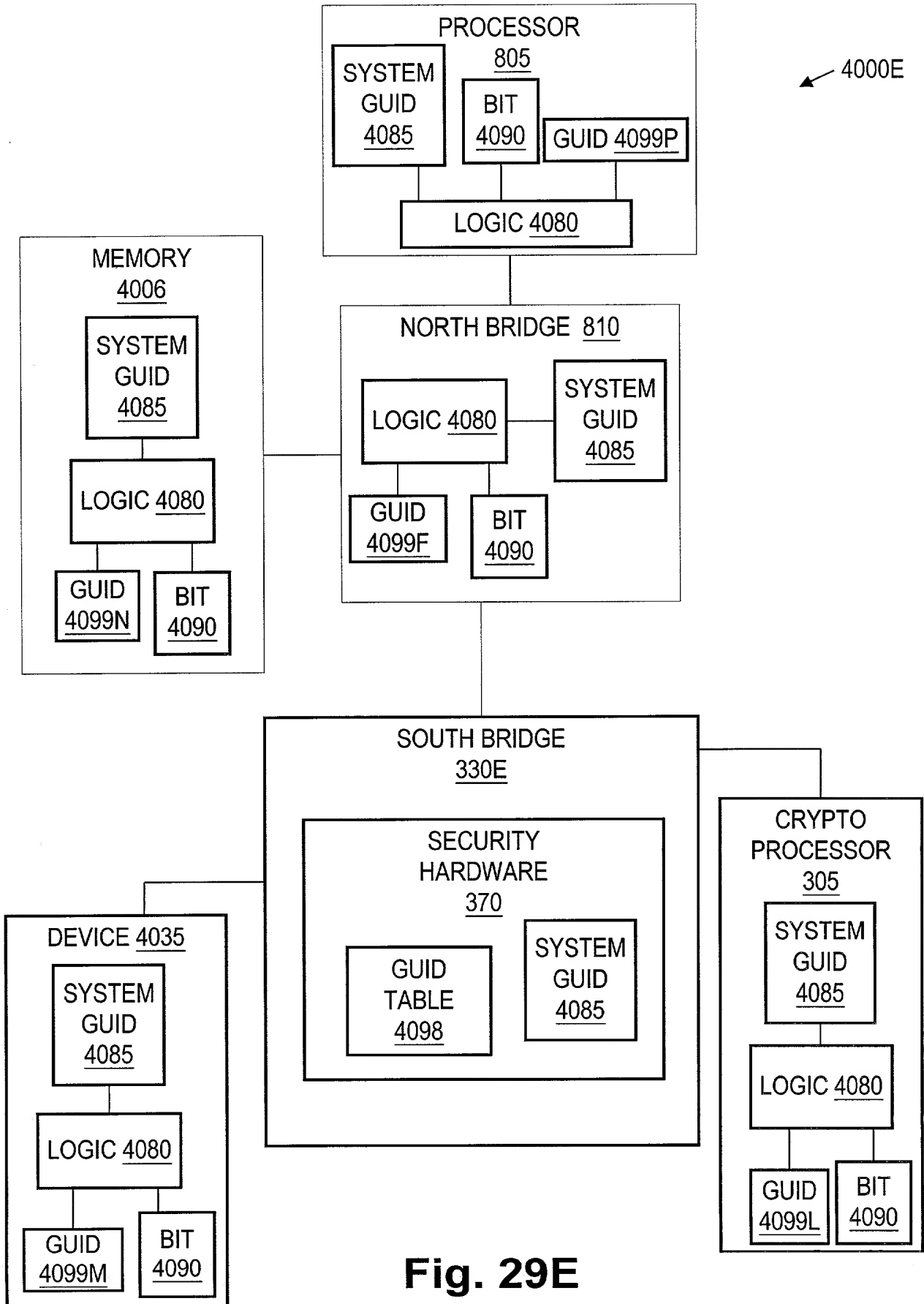
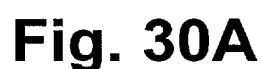


Fig. 29E



56 / 73

4100B

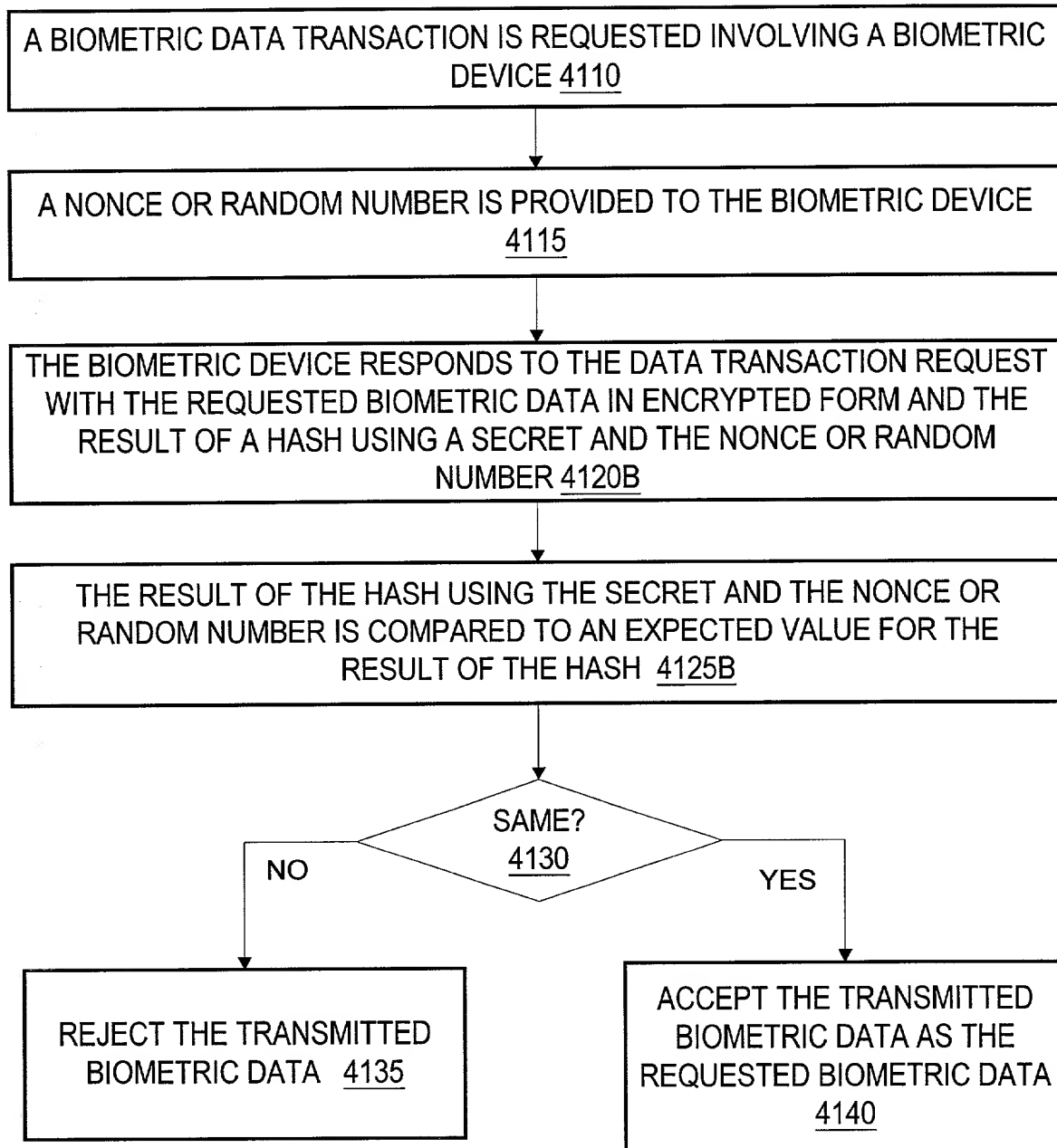


Fig. 30B



57 / 73

4200A

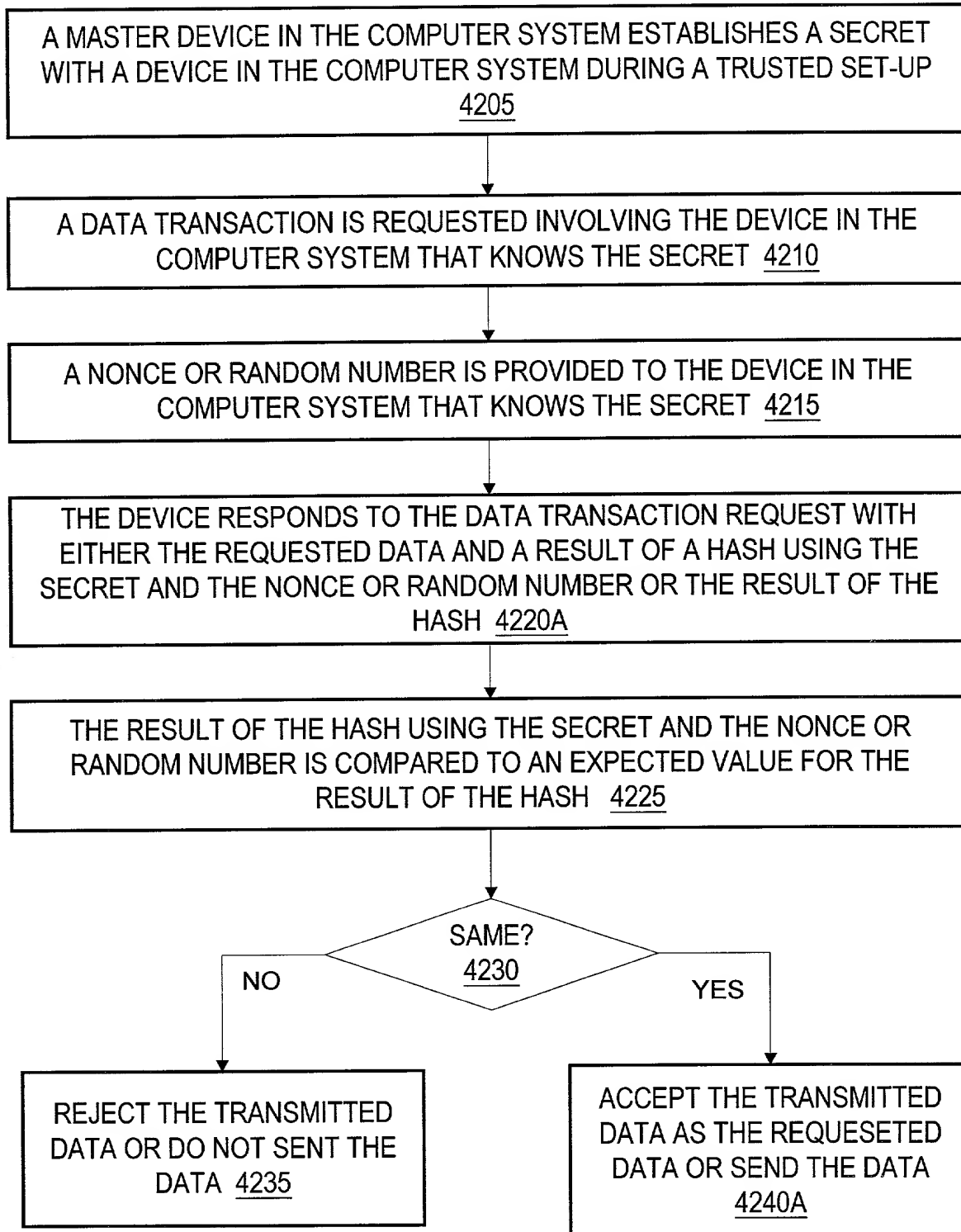


Fig. 31A

58 / 73

4200B

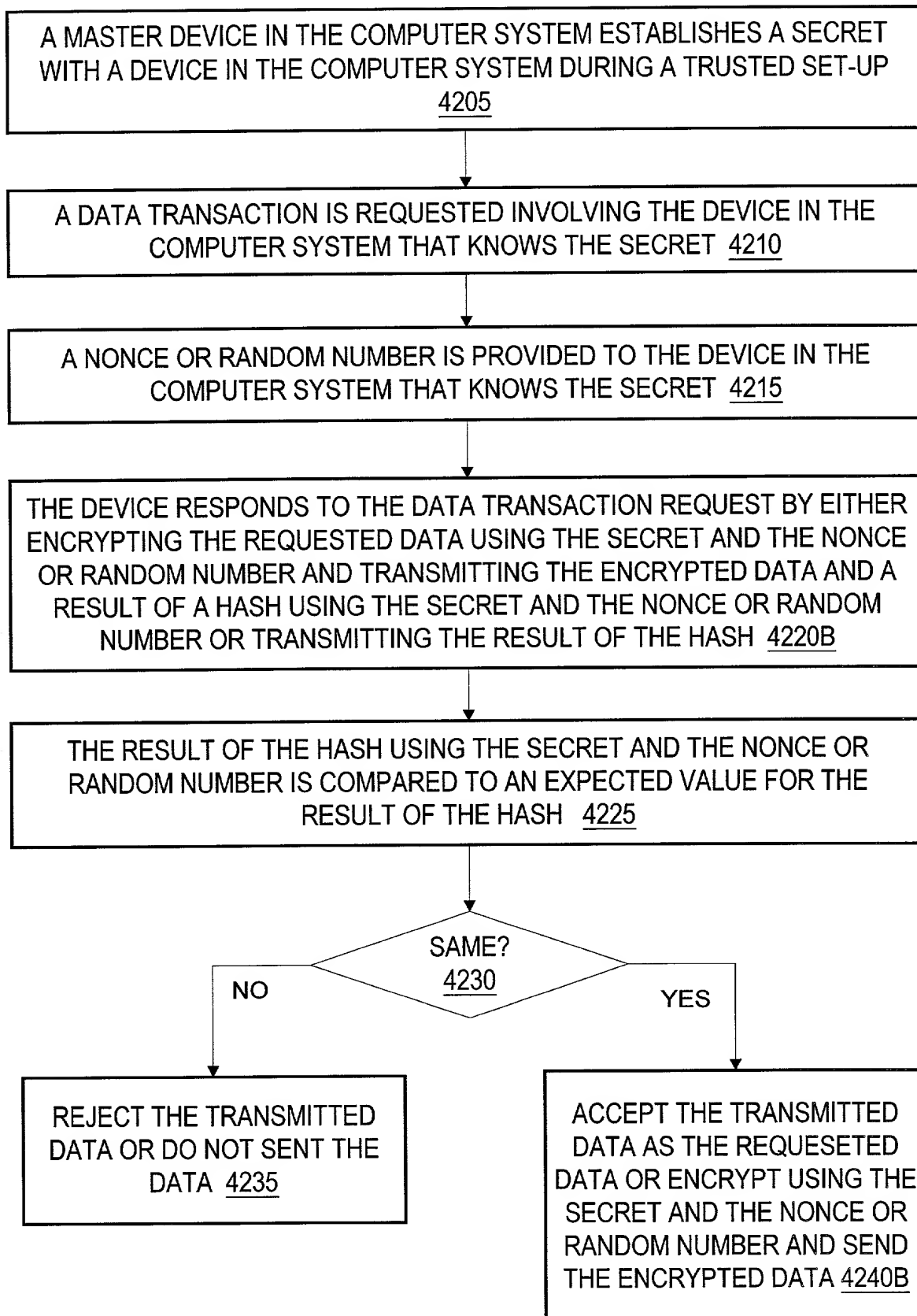


Fig. 31B

59 / 73

4300A

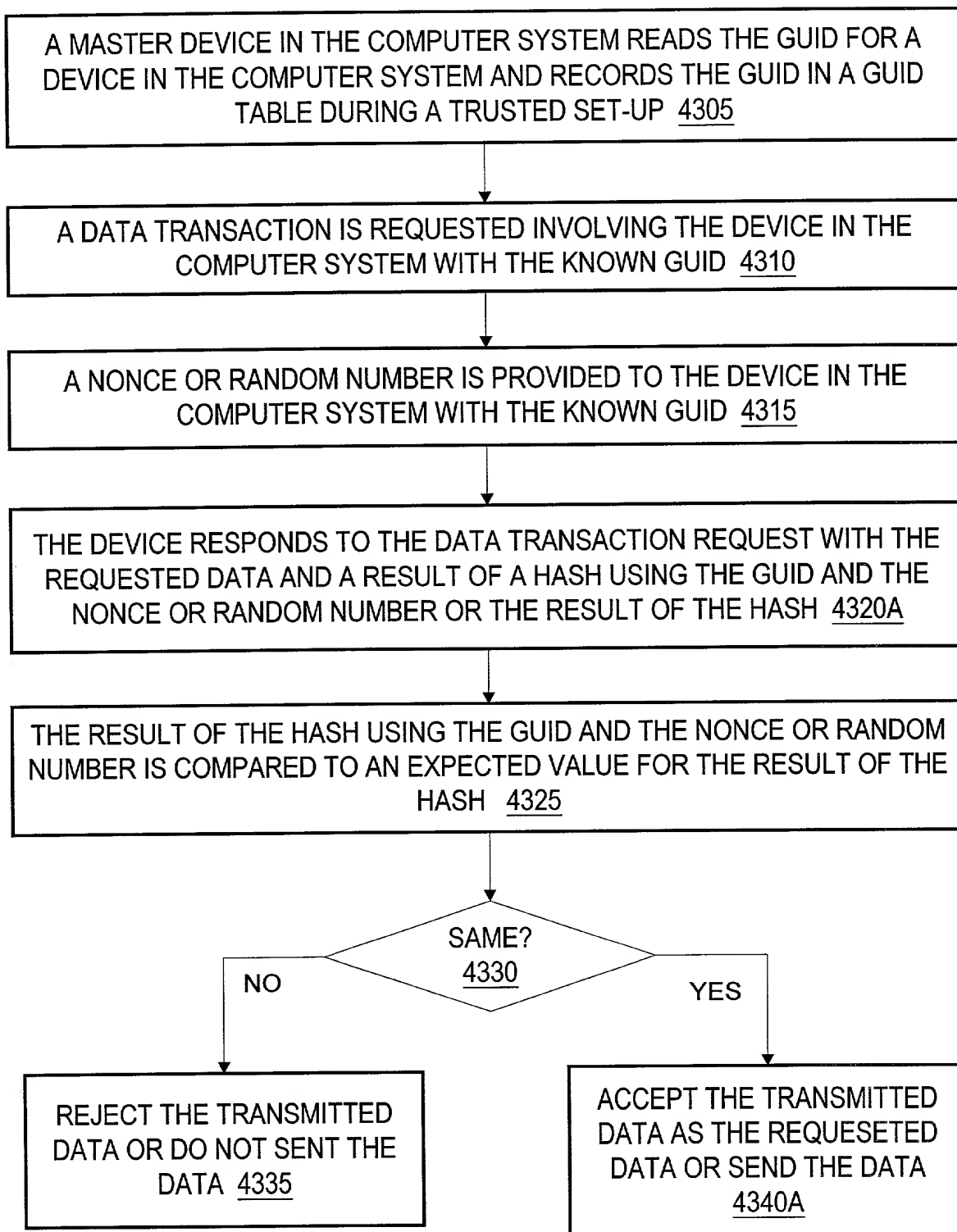


Fig. 32A

60 / 73

4300B

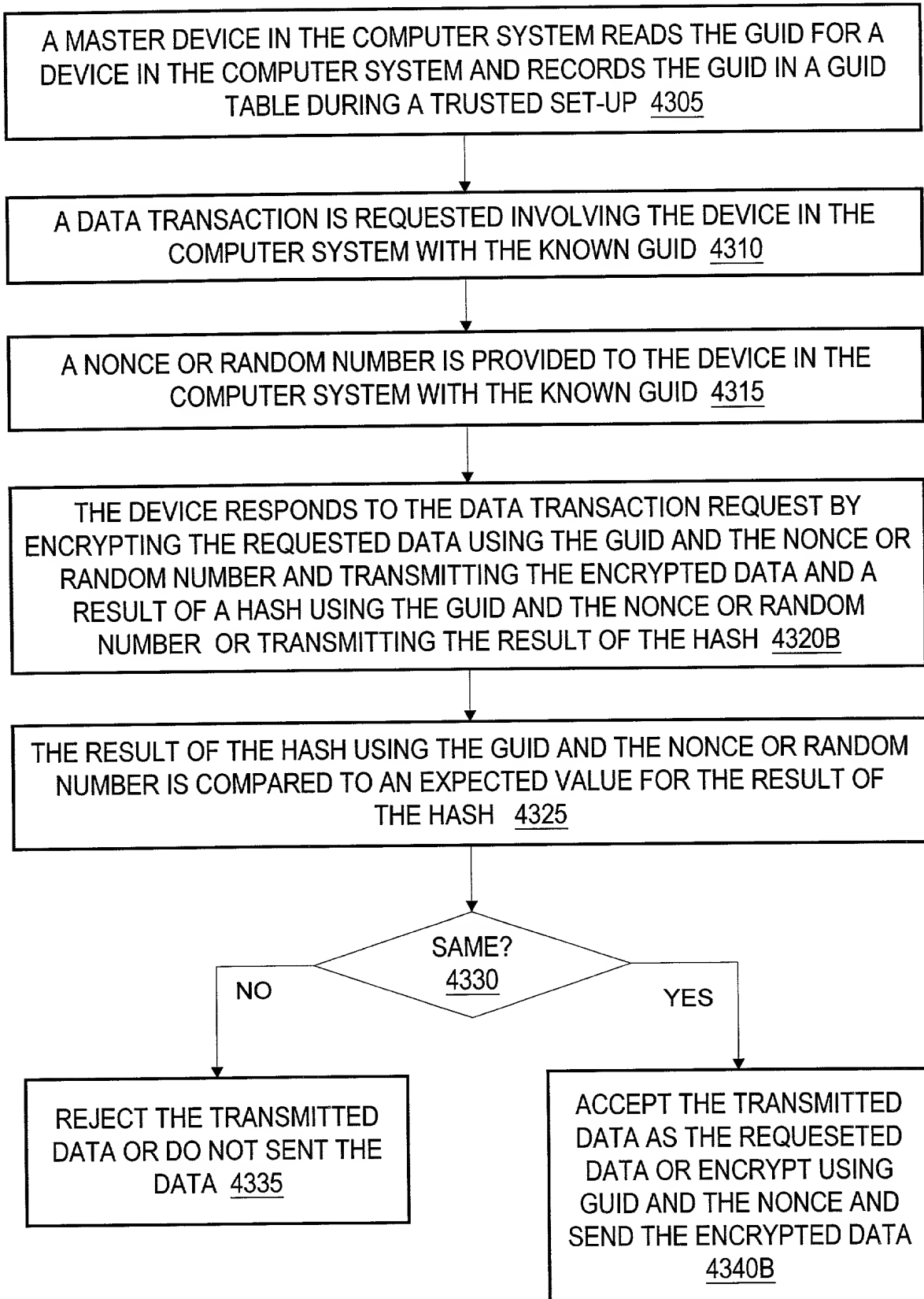


Fig. 32B

61 / 73

4300C

A MASTER DEVICE IN THE COMPUTER SYSTEM READS THE GUID FOR A DEVICE IN THE COMPUTER SYSTEM, RECORDS THE GUID IN A GUID TABLE, AND TRANSMITS A SECRET TO THE DEVICE DURING A TRUSTED SET-UP

4306

A DATA TRANSACTION IS REQUESTED INVOLVING THE DEVICE IN THE COMPUTER SYSTEM WITH THE KNOWN GUID THAT KNOWS THE SECRET

4311

A NONCE OR RANDOM NUMBER IS PROVIDED TO THE DEVICE IN THE COMPUTER SYSTEM WITH THE KNOWN GUID THAT KNOWS THE SECRET

4316

THE DEVICE RESPONDS TO THE DATA TRANSACTION REQUEST BY ENCRYPTING THE REQUESTED DATA USING THE SECRET, THE GUID, AND THE NONCE OR RANDOM NUMBER AND TRANSMITTING THE ENCRYPTED DATA AND A RESULT OF A HASH USING THE SECRET, THE GUID, AND THE NONCE OR RANDOM NUMBER OR TRANSMITTING THE RESULT OF THE HASH 4320C

THE RESULT OF THE HASH USING THE SECRET, THE GUID, AND THE NONCE OR RANDOM NUMBER IS COMPARED TO AN EXPECTED VALUE FOR THE RESULT OF THE HASH 4326

SAME?

4330

NO

YES

REJECT THE TRANSMITTED DATA OR DO NOT SENT THE DATA 4335

ACCEPT THE TRANSMITTED DATA AS THE REQUESTED DATA OR ENCRYPT USING THE SECRET, THE GUID, AND THE NONCE AND SEND THE ENCRYPTED DATA 4340C

**Fig. 32C**

62 / 73

4400

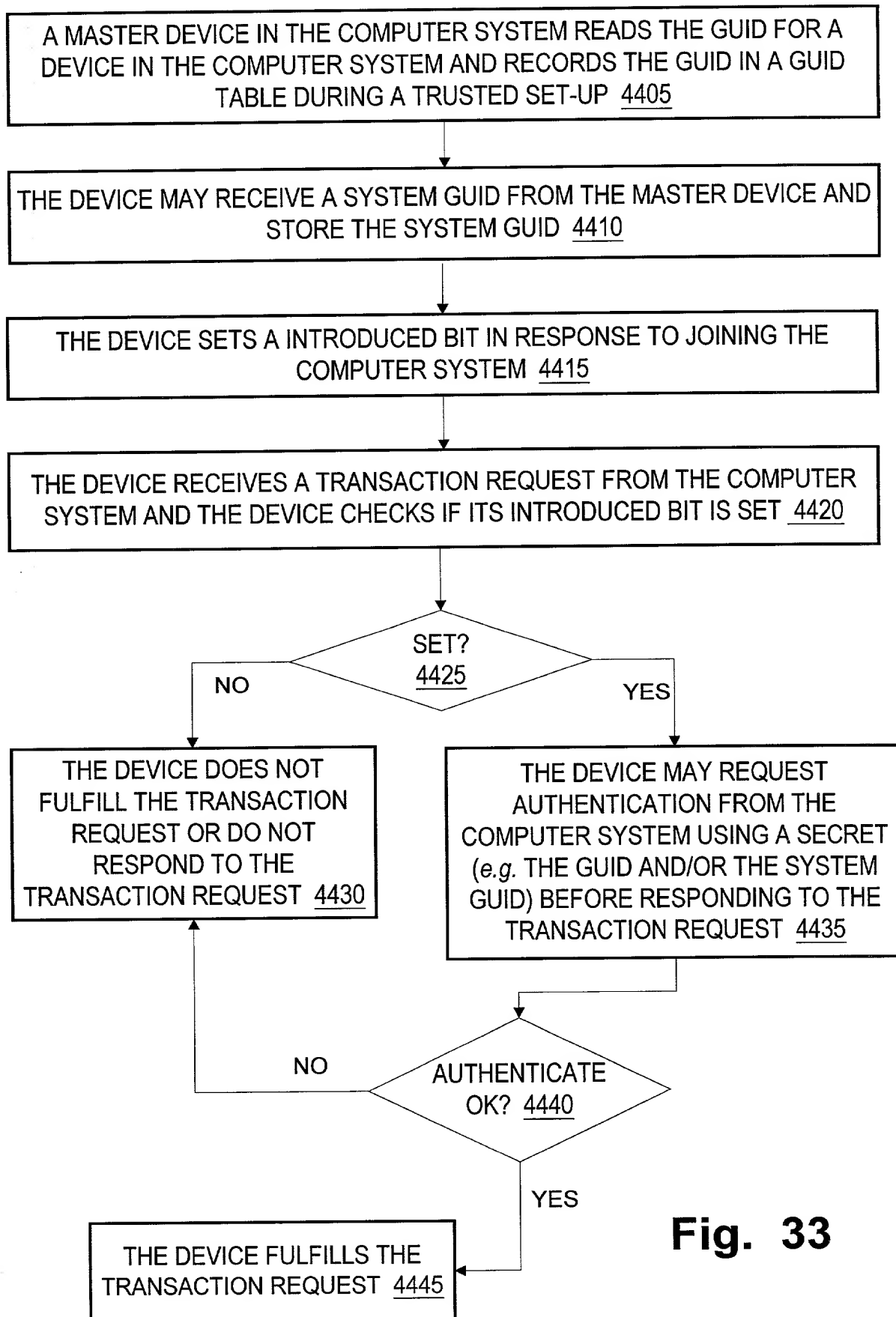


Fig. 33

63 / 73

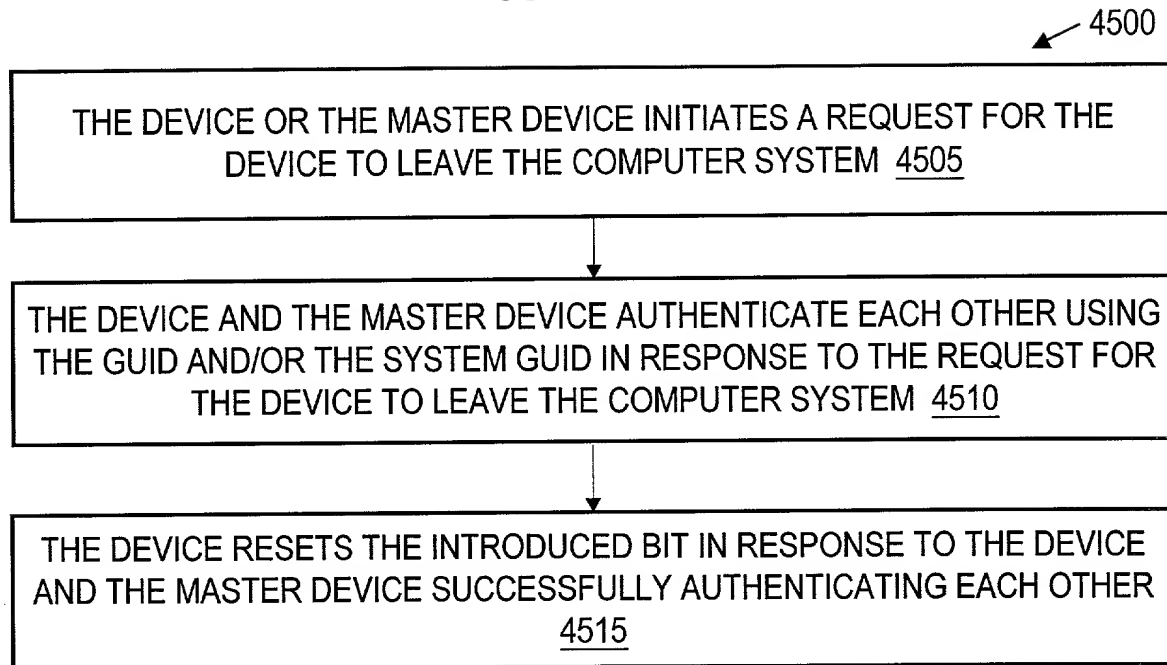


Fig. 34

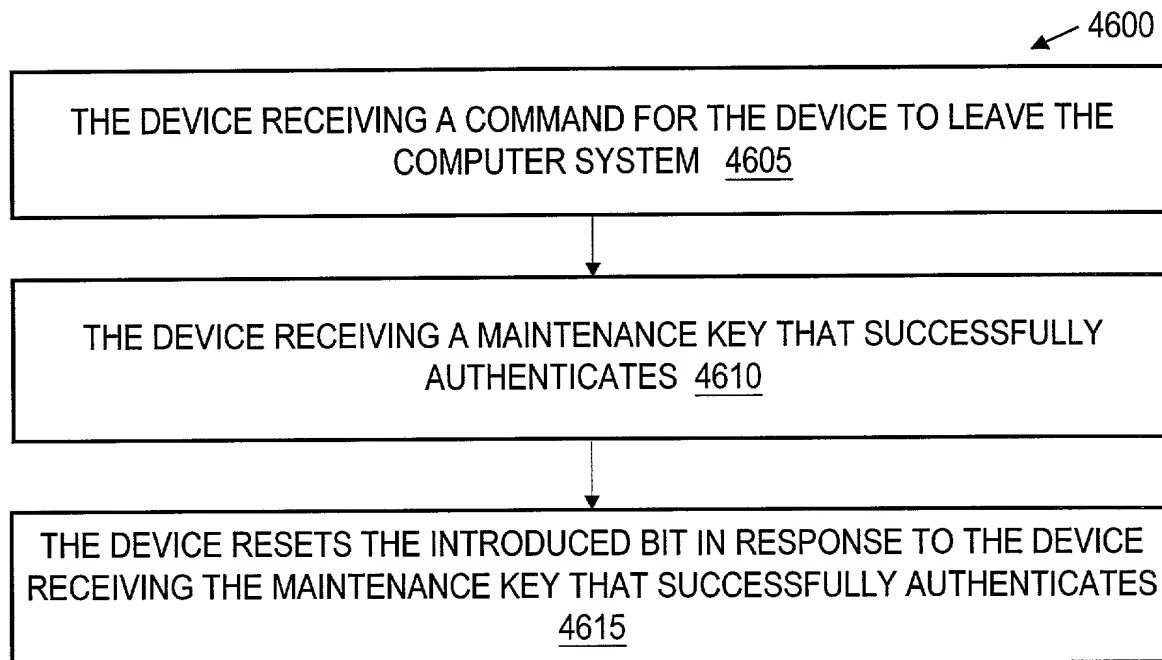
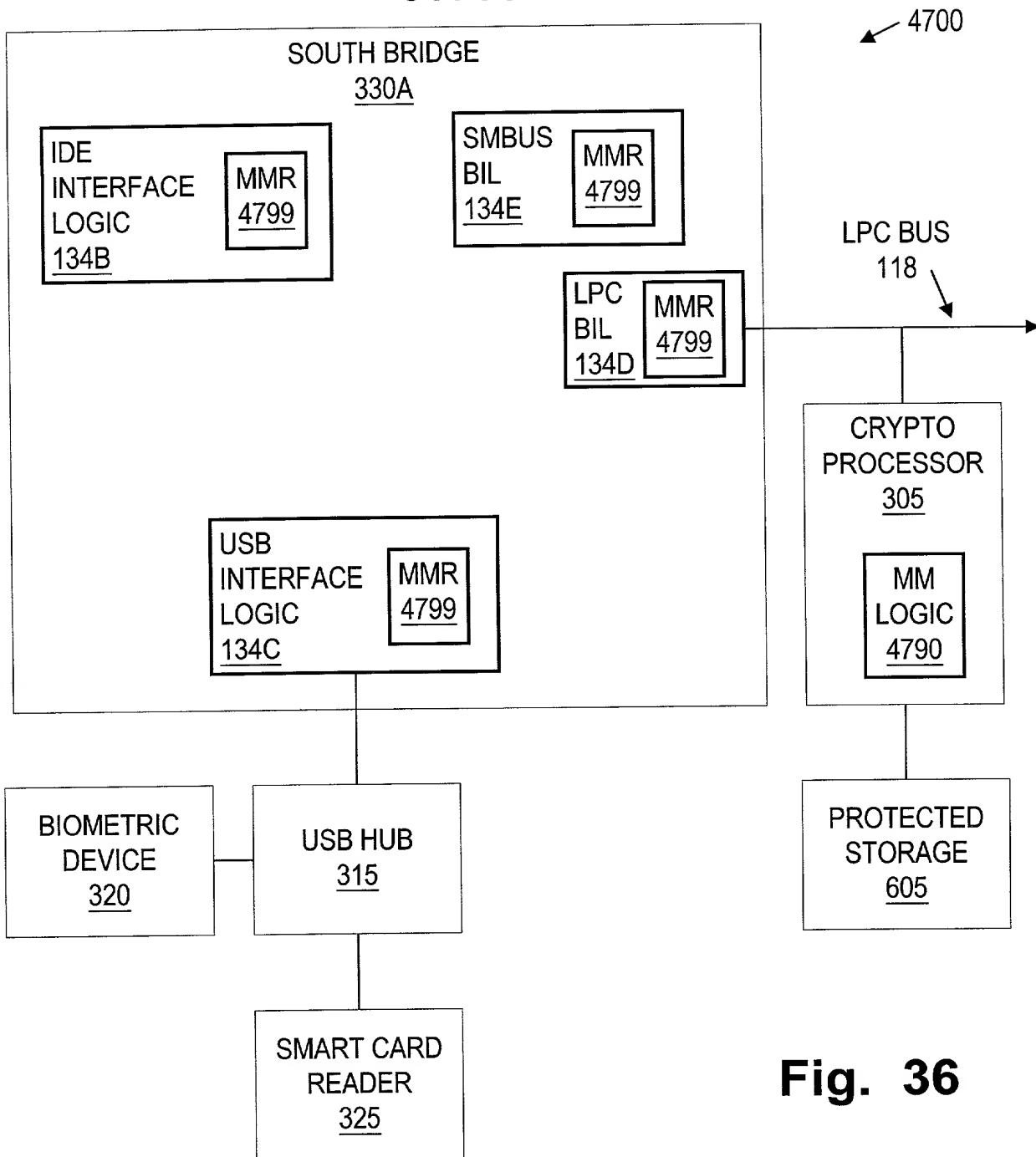


Fig. 35

64 / 73



**Fig. 36**



65 / 73

4800

TRANSMIT A MASTER MODE SIGNAL TO BUS INTERFACE LOGIC  
CONNECTED BETWEEN MASTER MODE LOGIC AND A DATA INPUT DEVICE,  
WHERE THE BUS INTERFACE LOGIC INCLUDES A MASTER MODE REGISTER

4805

SET A MASTER MODE BIT IN THE MASTER MODE REGISTER(S) TO  
ESTABLISH SECURE TRANSMISSION CHANNEL BETWEEN THE MASTER  
MODE LOGIC AND THE DATA INPUT DEVICE OUTSIDE THE OPERATING  
SYSTEM OF THE COMPUTER SYSTEM 4810

THE MASTER MODE LOGIC AND THE DATA INPUT DEVICE EXCHANGE DATA  
OUTSIDE THE OPERATING SYSTEM OF THE COMPUTER SYSTEM THROUGH  
THE BUS INTERFACE LOGIC(S) THAT INCLUDE THE MASTER MODE  
REGISTER 4815

THE MASTER MODE LOGIC FLUSHES THE BUFFERS OF THE BUS INTERFACE  
LOGIC(S) THAT INCLUDE THE MASTER MODE REGISTER AFTER  
CONCLUDING THE DATA TRANSMISSIONS 4820

THE MASTER MODE LOGIC SIGNALS THE BUS INTERFACE LOGIC(S) TO  
UNSET THE MASTER MODE BITS AFTER FLUSHING THE BUFFERS OF THE  
BUS INTERFACE LOGIC(S) THAT INCLUDE THE MASTER MODE REGISTER  
4825

**Fig. 37**

66 / 73

4900A

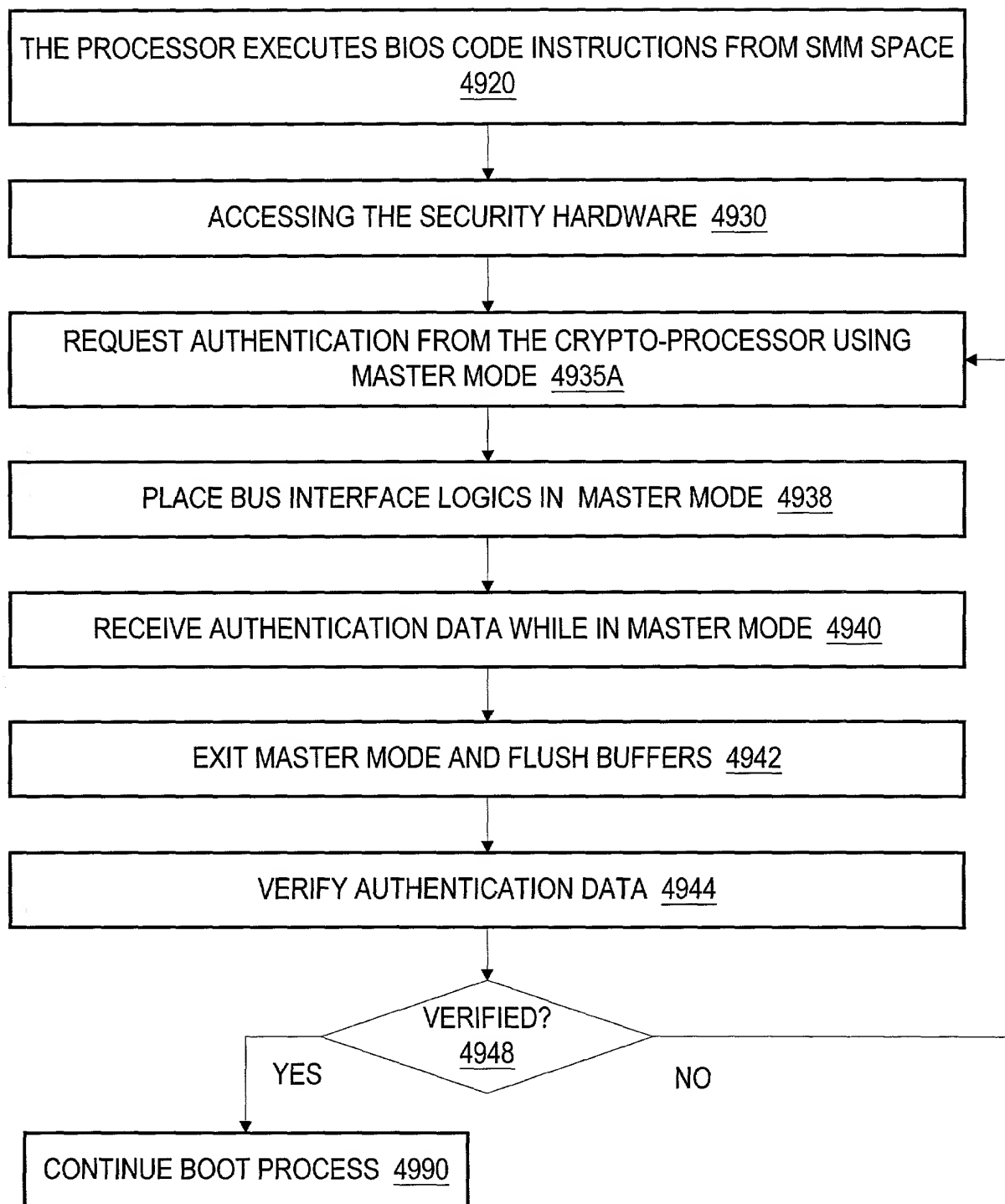


Fig. 38A

67 / 73

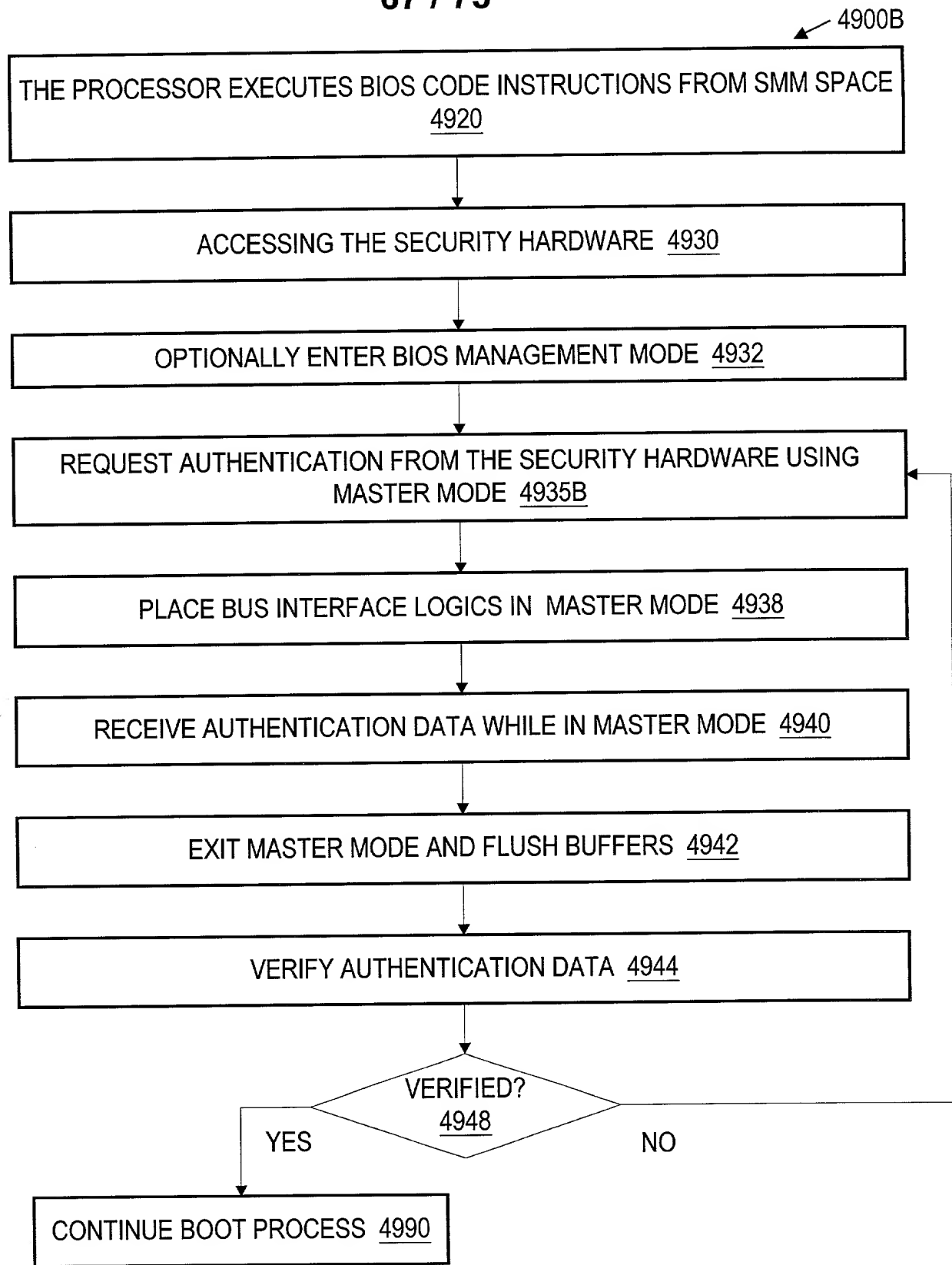


Fig. 38B

68 / 73

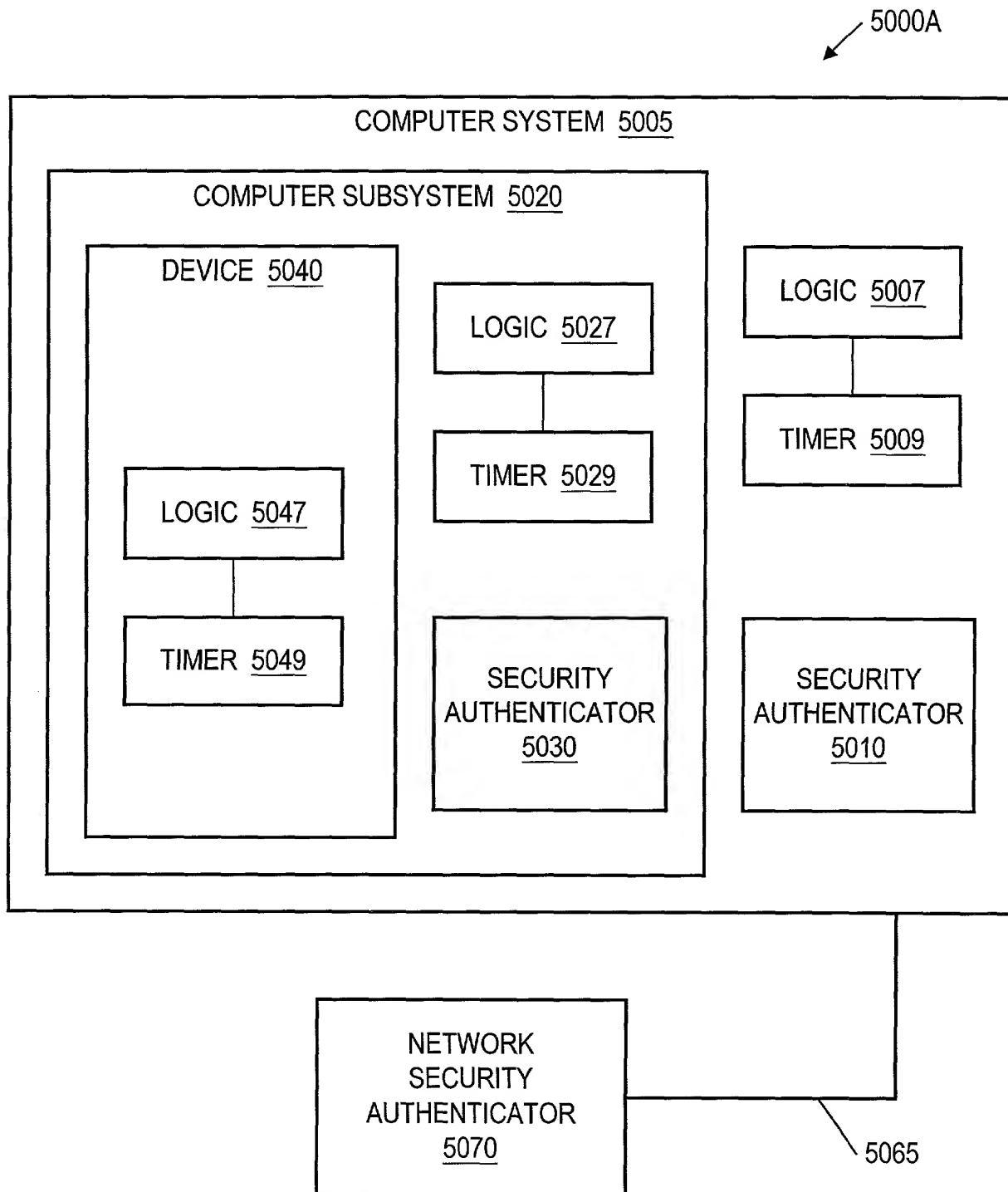
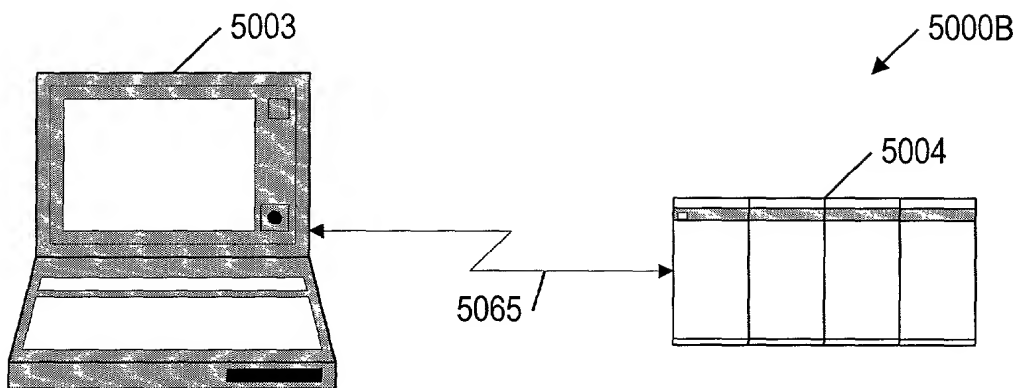
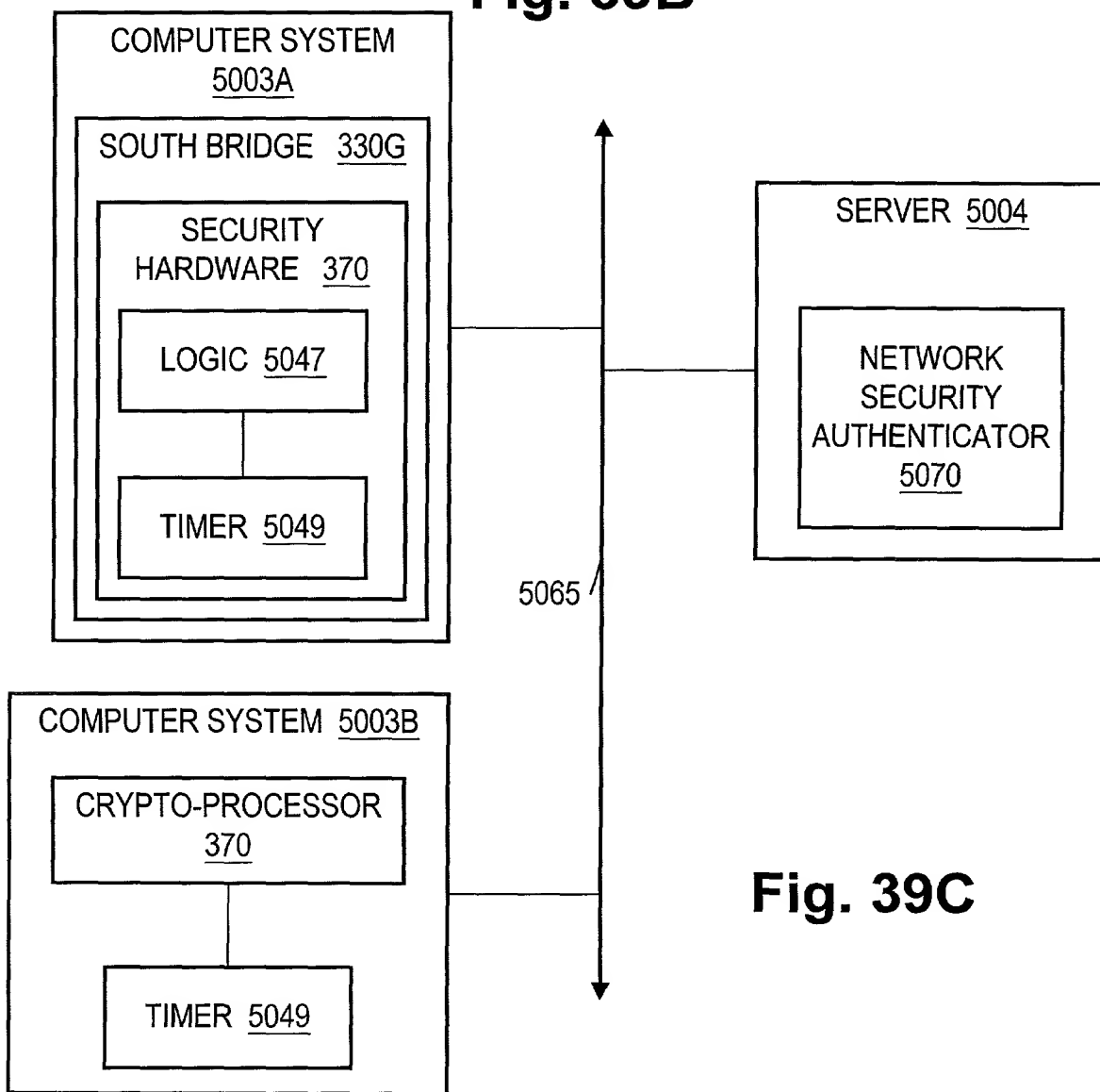


Fig. 39A

69 / 73



**Fig. 39B**



**Fig. 39C**

TT3761-6880-860

70 / 73

5100A

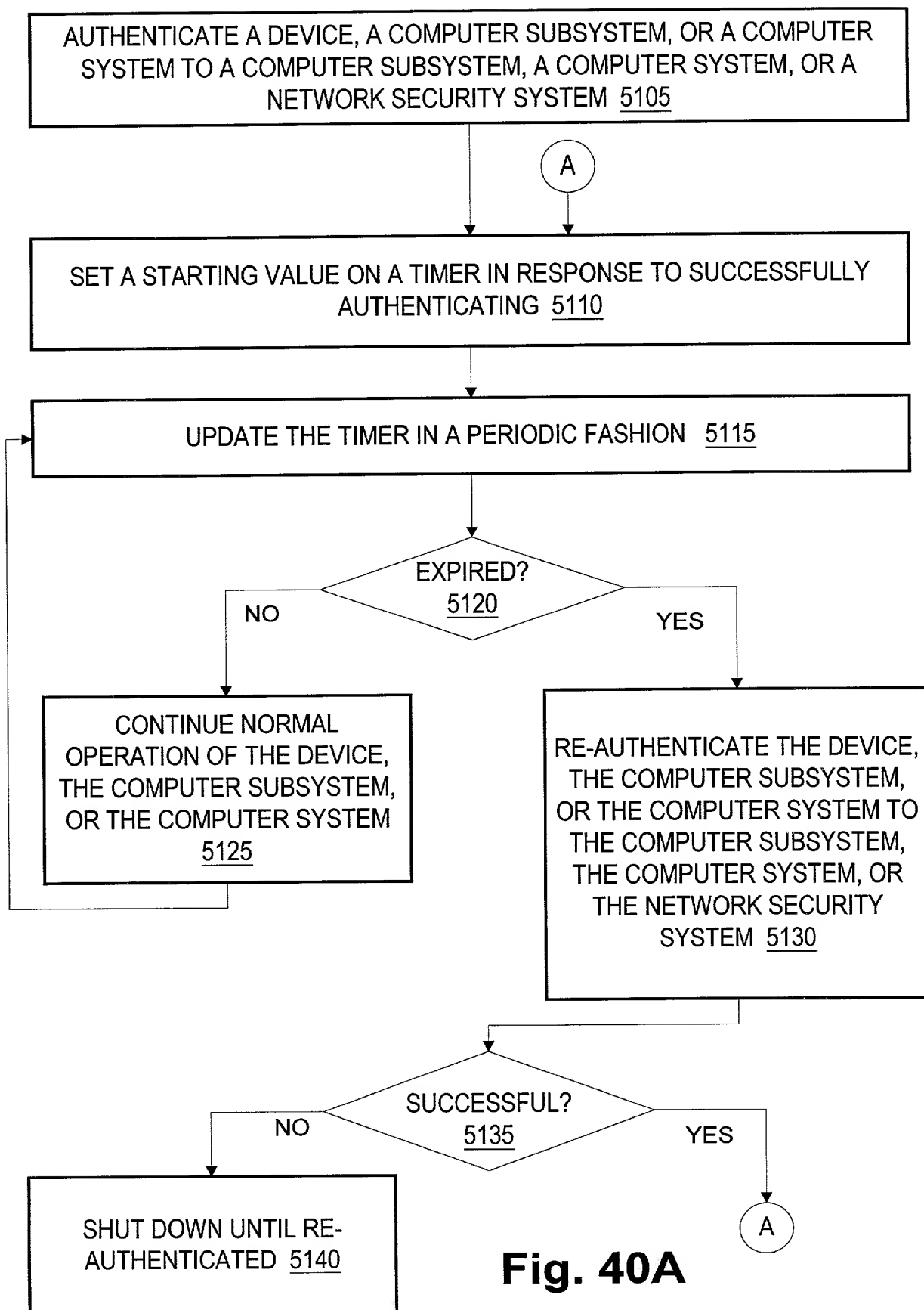
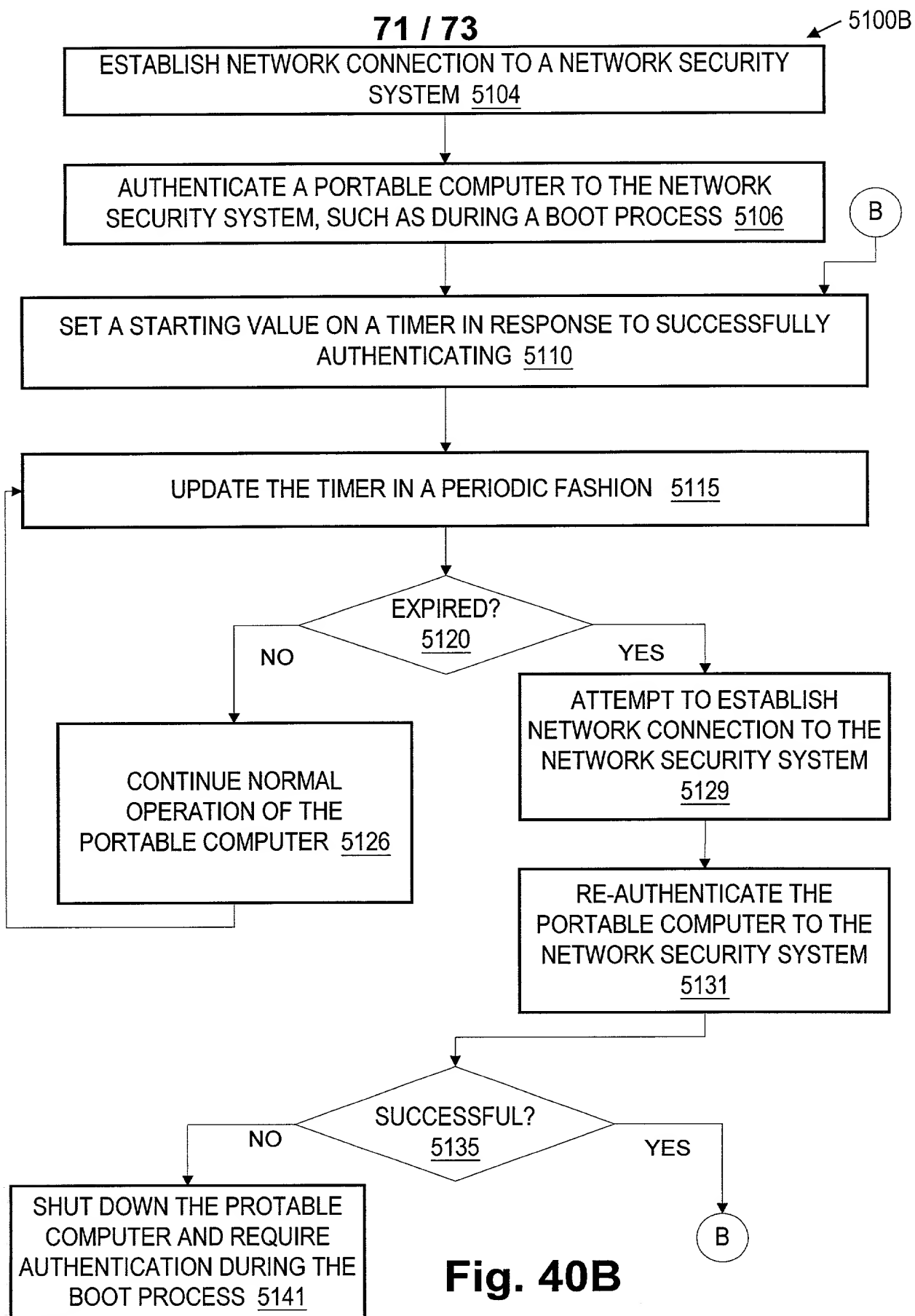


Fig. 40A



**Fig. 40B**

72 / 73

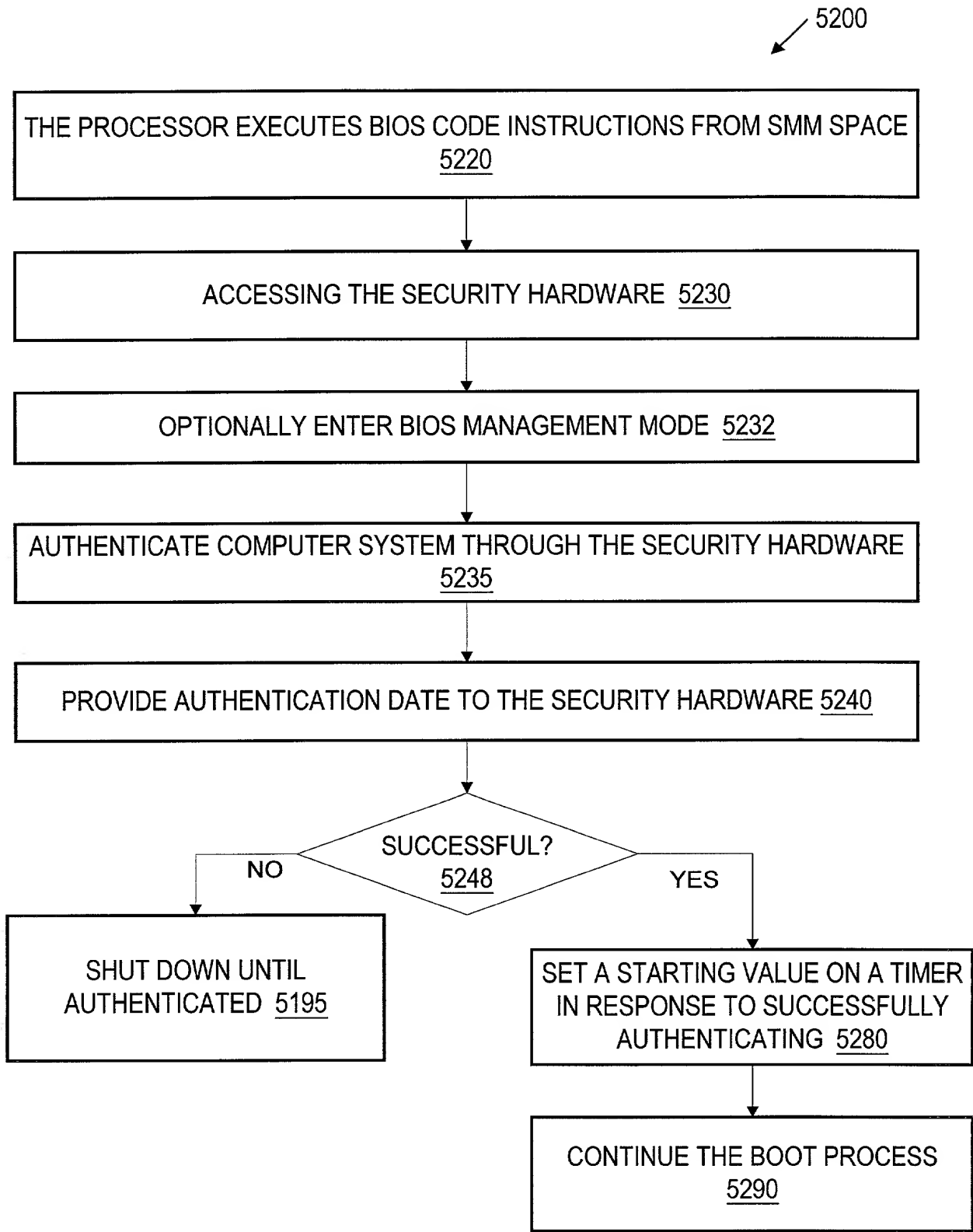


Fig. 41



73 / 73

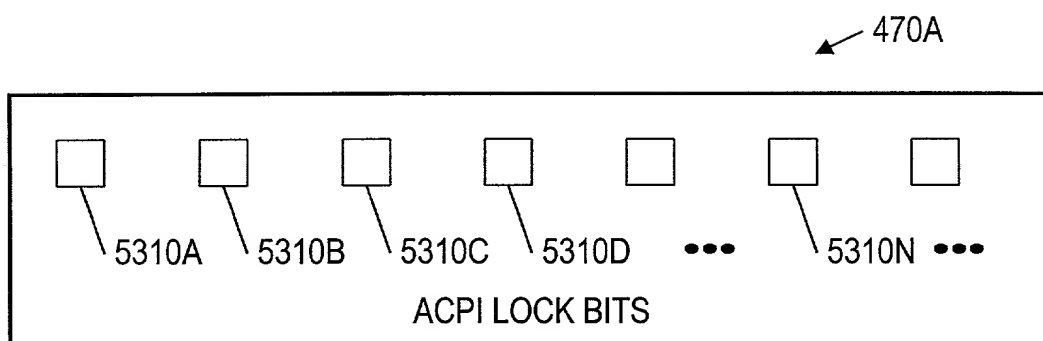


Fig. 42A

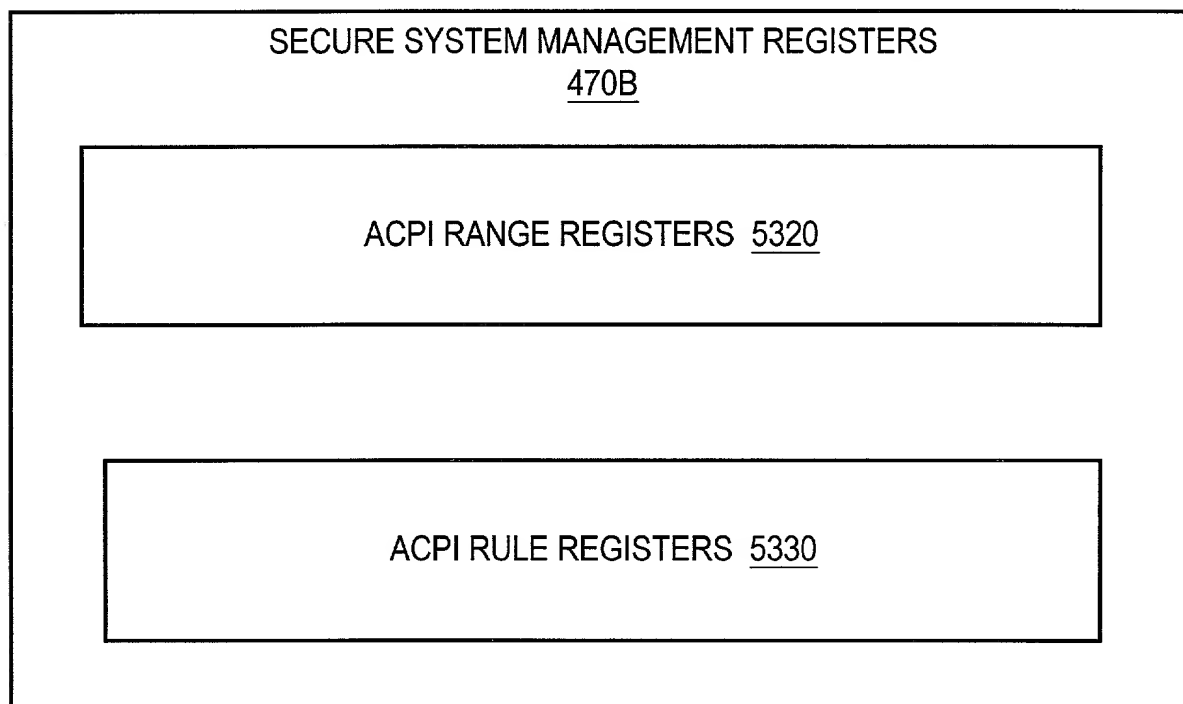


Fig. 42B